

Prof. Gabriel Alfonso Rincón-Mora, Ph.D.

Motorola Solutions Foundation Professor, Georgia Institute of Technology

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E-mail: Rincon-Mora@gatech.edu, **URL:** Rincon-Mora.gatech.edu**Synopsis**

Career: With Texas Instruments in 1994–2004 & Georgia Tech since 1999. Director of the Georgia Tech Analog Consortium in 2001–2004, Director of the TI Analog Fellowship Program in 2001–2015, & Visiting Professor at National Cheng Kung University in Taiwan in 2011–2019. **Distinctions:** Motorola Solutions Foundation Professor, Fellow of the National Academy of Inventors, Fellow of the American Association for the Advancement of Science, Fellow of the Institute of Electrical and Electronics Engineers, & Fellow of the Institution of Engineering and Technology. **Other Distinctions:** Inducted into Georgia Tech's Council of Outstanding Young Engineering Alumni, named one of "The 100 Most Influential Hispanics" by *Hispanic Business*, included in "List of Notable Venezuelan Americans" in Science, & selected IEEE Distinguished Lecturer. **Awards:** IEEE Charles A. Desoer Technical Achievement Award, Distinguished Faculty Achievement Award, Charles E. Perry Visionary Award, Three-Year Patent Award, National Hispanic in Technology Award, IEEE Joseph M. Biedebach Outstanding Engineering Educator Award, IEEE Outstanding Educator Award, Orgullo Hispano Award, Hispanic Heritage Award, State of California Commendation Certificate, & IEEE Service Award. **Recognition:** Featured on *IEEE Circuits and Systems Magazine*, *Electronic Engineering Times*, *Planet Analog*, *Intown* in Atlanta, *Summa Cum Laude* in Florida, *EEWeb*, & on the covers of *Hispanic Business*, *Official Magazine of the Society of Hispanic Professional Engineers*, *La Fuente* in Dallas, & 3 times on *Nuevo Impacto* in Atlanta. **Body of Work:** 4 textbooks, 5 slide books, 3 literary books, 8 handbooks, 4 book chapters, 44 patents, over 200 articles, over 26 commercial power-chip products released to production, 25 educational videos, educational SPICE code for over 200 analog and power circuits, & over 170 keynote addresses, distinguished lectures, & research seminars.

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VI. Honors and Awards

I. Degrees

- B.S. in Electrical Eng. (Magna Cum Laude), Florida Int. Univ., 1992.
- M.S. in Electrical Eng. (Highest Honors), Georgia Inst. of Tech., 1994.
- Ph.D. in Electrical Eng. (Outstanding Ph.D. Candidate), Georgia Inst. of Tech., 1996.

II. Employment

- Electrical Engineer and Laboratory Syst. Specialist, Northern Telecom, 1993.
- Analog IC Design Engineer, Texas Instruments, 1994–1996.
- Senior Design Engineer and Design Team Leader, Texas Instruments, 1997–2001.
- Member of Group Technical Staff, Texas Instruments, 1999–2003.
- Adjunct Professor, Electrical and Computer Eng., Georgia Inst. of Tech., 1999–2001.
- Senior Analog IC Des. Consultant, Texas Instruments, 2003–2004.
- Assistant Professor, Electrical and Computer Eng., Georgia Inst. of Tech., 2001–2007.
- Director, Georgia Tech Analog Consortium, Georgia Inst. of Tech., 2001–2004.
- Director, TI Analog Fellowship Program, Georgia Inst. of Tech., 2001–2015.
- Associate Professor with Tenure, Electrical and Computer Eng., Georgia Inst. of Tech., 2007–2012.
- Visiting Professor, Electrical Eng., Nat. Cheng Kung Univ., Taiwan, 2011–2019.
- Professor, Electrical and Computer Eng., Georgia Inst. of Tech., since 2012.
- Motorola Solutions Foundation Professor, Georgia Inst. of Tech., since 2022.

III. Research, Scholarship, and Creative Output

A. Books, Handbooks, and Book Chapters Published

Ph.D. Dissertation: *Current Efficient, Low Voltage, Low Dropout Regulators*. Georgia Inst. of Tech., 1996.

A1. Books (sole author)

1. *Voltage References*. New Jersey: IEEE Press and John Wiley & Sons, Inc., 2001 [Translated into Chinese].
2. *Short Stories and Poems to Boot!* New York: Vantage Press, 2001.
3. *Triple Engagement*. New York: iUniverse, 2004.
4. *Power Management ICs*. Raleigh: Lulu, 2005.
5. *Analog IC Design*. New York: KDP, 2008–2023.
6. *Analog IC Design with Low-Dropout Regulators*. New York: McGraw-Hill, Jan. 2009 [Translated into Chinese].
7. *Vanish*. Morrisville: Lulu, 2009.
8. *Analog IC Design with Low-Dropout Regulators, 2nd Edition*. New York: McGraw-Hill, 2014 [Translated into Chinese].
9. *Power IC Design*. Morrisville: Lulu, 2016.
10. *Analog Electronics*. New York: KDP, 2019–2024.
11. *Switched Inductor Power IC Design*. New York: Springer Nature, 2022.
12. *Power IC Design, 2nd Edition*. New York: KDP, 2023–2024.

A2. Handbooks (sole author)

1. *Diodes & BJTs*. New York: KDP, 2019.
2. *MOSFETs*. New York: KDP, 2019.
3. *Switched-Inductor Power Supplies*. New York: KDP, 2019.
4. *Power Losses in Switched Inductors*. New York: KDP, 2020.
5. *Switched Inductor's Frequency Response*. New York: KDP, 2020.
6. *Switched Inductors: Feedback Controller*. New York: KDP, 2020.
7. *Switched Inductors: Control Loops*. New York: KDP, 2021.
8. *Switched Inductors: Building Blocks*. New York: KDP, 2021.

A3. Invited Book Chapters (sole author)

1. "Harvesting Microelectronic Circuits," *Energy Harvesting Technologies*. Springer, Jan. 2009.

2. "Energizing and Powering Microsystems," *Integrated Microsystems*. CRC Press, Oct. 2011.
3. "Vibration-Based Energy-Harvesting Integrated Circuits," *Advances in Energy Harvesting Methods*. Springer, Feb. 2013.
4. "Energy-Harvesting Integrated Circuits," *Energy Harvesting with Functional Materials and Microsystems*. CRC Press, Nov. 2013.

B. Patents Issued (Prof. Rincón-Mora advised boldfaced inventors.)

1. G.A. Rincón *et al.*, "Amplifier circuit and method," U.S. 5,491,437, Feb. 13, 1996.
2. G.A. Rincón *et al.*, "Controlled current output stage amplifier circuit and method," U.S. 5,500,625, Mar. 19, 1996.
3. M. Corsi and G.A. Rincón, "Cross coupled quad comparator for current sensing...," U.S. 5,519,341, May 21, 1996.
4. G.A. Rincón *et al.*, "Output stage of amplifier circuit," EP 715405, Jun. 5, 1996.
5. G.A. Rincón *et al.*, "Controlled current output stage amplifier circuit and method," JP 8237046, Sept. 13, 1996.
6. M. Corsi and G.A. Rincón, "Current sensing circuit and method," U.S. 5,614,850, Mar. 25, 1997.
7. M. Corsi, G.A. Rincón *et al.*, "A voltage regulator," EP 851332, Jan. 7, 1998.
8. M. Corsi, G.A. Rincón *et al.*, "Drop-out voltage controller," JP 10187258, Jul. 14, 1998.
9. M. Corsi, G.A. Rincón *et al.*, "Low drop-out regulator with PMOS pass element," U.S. 5,867,015, Feb. 2, 1999.
10. G.A. Rincón-Mora, "DC-DC converter with voltage loss compensation," EP 928056, Jul. 7, 1999.
11. G.A. Rincón-Mora, "...piecewise-linear curvature corrected bandgap reference," U.S. 5,952,873, Sept. 14, 1999.
12. G.A. Rincón-Mora, "Optimized frequency shaping circuit topologies for LDOs," U.S. 5,982,226, Nov. 9, 1999.
13. G.A. Rincón-Mora and M. Corsi, "Low-drop-out voltage regulator...," EP 957421, Nov. 17, 1999.
14. G.A. Rincón-Mora *et al.*, "Low-dropout...regulator...transient...boost circuit," U.S. 6,046,577, Apr. 4, 2000.
15. G.A. Rincón-Mora, "Miller compensated amplifier for operation with capacitive loading," EP 1006648, Jun. 7, 2000.
16. G.A. Rincón-Mora, "Active compensating capacitive multiplier," U.S. 6,084,475, Jul. 4, 2000.
17. G.A. Rincón-Mora, "Bandgap circuits with curvature-correction," EP 1041480, Oct. 4, 2000.
18. G.A. Rincón-Mora, "Exact curvature-correcting method for bandgap circuits," U.S. 6,157,245, Dec. 5, 2000.
19. G.A. Rincón-Mora and M. Corsi, "Current-efficient low-drop-out voltage regulator...," U.S. 6,188,211, Feb. 13, 2001.
20. G.A. Rincón-Mora, "Accurate, fast, and user programmable hysteretic comparator," U.S. 6,229,350, May 8, 2001.
21. G.A. Rincón-Mora and M. Huggins, "High power supply...rejection...low drop-out...," U.S. 6,304,131, Oct. 16, 2001.
22. G.A. Rincón-Mora, "...low ripple, high frequency hysteretic...dc-dc converters," U.S. 6,369,555, Apr. 9, 2002.
23. G.A. Rincón-Mora and **B. Abesingha**, "Method of minimizing package-shift effects...," U.S. 6,432,753, Aug. 13, 2002.
24. G.A. Rincón-Mora, "Adjustable temperature-compensated threshold circuit...," EP 1265363, Dec. 11, 2002.
25. G.A. Rincón-Mora and **R. Stair**, "Buffer/driver for low dropout regulators," U.S. 6,501,305, Dec. 31, 2002.
26. G.A. Rincón-Mora, "...threshold circuit with trip-points exceeding the...supplies," U.S. 6,545,511, Apr. 8, 2003.
27. G.A. Rincón-Mora and **M. Pulkin**, "...low impedance driver for linear regulators," U.S. 6,573,694, Jun. 3, 2003.
28. G.A. Rincón-Mora, "Temperature-compensated threshold circuit," EP 1351063, Oct. 8, 2003.
29. G.A. Rincón-Mora and M. Corsi, "Current-efficient low-drop-out voltage regulator...," DE 69910888, Oct. 9, 2003.
30. G.A. Rincón-Mora, "Integrated low ripple, high frequency...hysteretic controller...," U.S. 6,628,109, Sept. 30, 2003.
31. G.A. Rincón *et al.*, "Output stage of amplifier circuit," DE 69532061, Dec. 11, 2003.
32. G.A. Rincón-Mora *et al.*, "...minimizes package-shift effects in integrated circuits...," U.S. 6,750,553, Jun. 15, 2004.
33. G.A. Rincón-Mora and **R. Stair**, "...threshold voltage extraction...capacitor multiplier," U.S. 6,806,762, Oct. 19, 2004.
34. M. Corsi, G.A. Rincón *et al.*, "A voltage regulator," DE 69727783, Dec. 30, 2004.
35. G.A. Rincón-Mora, **V. Gupta**, and P. Raha, "Low dropout...linear regulator...," U.S. 6,847,260, Jan. 25, 2005.
36. G.A. Rincón-Mora, "Active compensating capacitive multiplier," DE 69934566, Feb. 8, 2007.
37. G.A. Rincón-Mora and **M. Arnold**, "...buffer with rail-to-rail output for low dropout," U.S. 7,339,416, Mar. 4, 2008.
38. G.A. Rincón-Mora, "Temperature-compensated threshold circuit," DE 60225626, Apr. 30, 2008.
39. G.A. Rincón-Mora, "Exact curvature-correcting method for bandgap circuits," DE 60042142, June 18, 2009.
40. G.A. Rincón-Mora and **M. Arnold**, "Gate driver circuit for power transistor," U.S. 7,560,973, Jul. 14, 2009.
41. G.A. Rincón-Mora, "Increase in active compensation capacitive property," JP 4528394, Aug. 18, 2010.

42. **D. Kwon** and G.A. Rincón-Mora, "Rectifier-free piezoelectric energy harvester...", U.S. 8,368,290, Feb. 5, 2013.
43. **S. Yang** and G.A. Rincón-Mora, "Recycling piezoelectric energy-harvesting...charger," U.S. 12,199,529, Jan. 14, 2025.
44. **L. Cui, Q. Zhi,** and G.A. Rincón-Mora, "Compact...battery-charging voltage regulator," pending, 2023.

C. Articles Published

C1. Refereed Journal Articles (Prof. Rincón-Mora advised boldfaced authors.)

1. G.A. Rincón-Mora and P.E. Allen, "A low-voltage, low quiescent current, low drop-out regulator," *IEEE J. of Solid-State Circuits*, vol. 33, no. 1, pp. 36–44, Jan. 1998.
2. G.A. Rincón-Mora and P.E. Allen, "Optimized frequency-shaping circuit topologies for LDO's," *IEEE Trans. on Circuits and Syst. II*, vol. 45, no. 6, pp. 703–708, Jun. 1998.
3. B.J. Blalock, P.E. Allen, and G.A. Rincón-Mora, "Designing 1V op amps using standard digital CMOS technology," *IEEE Trans. on Circuits and Syst. II*, vol. 45, no. 7, pp. 769–780, Jul. 1998.
4. G.A. Rincón-Mora and P.E. Allen, "A 1.1 V current-mode and piecewise-linear curvature corrected bandgap reference," *IEEE J. of Solid-State Circuits*, vol. 33, no. 10, pp. 1551–1554, Oct. 1998.
5. G.A. Rincón-Mora, "Active capacitor multiplier in Miller-compensated circuits," *IEEE J. of Solid-State Circuits*, vol. 35, no. 1, pp. 26–32, Jan. 2000.
6. **R. Stair** and G.A. Rincón-Mora, "A low voltage, rail-to-rail, class AB CMOS amplifier with high drive and low output impedance characteristics," *IEEE Trans. on Circuits and Syst. II*, vol. 48, no. 8, pp. 753–761, Aug. 2001.
7. **B. Abesingha,** G.A. Rincón-Mora, and D. Briggs, "Voltage shift in plastic-packaged bandgap references," *IEEE Trans. on Circuits and Syst. II*, vol. 49, no. 10, pp. 681–685, Oct. 2002.
8. **R. Dokania** and G.A. Rincón-Mora, "Cancellation of load-regulation in low drop-out regulators," *IET Electronic Letters*, vol. 38, issue 22, pp. 1300–1302, Oct. 2002.
9. **B. Sahu** and G.A. Rincón-Mora, "A high-efficiency linear RF power amplifier with a power-tracking dynamically adaptive buck–boost Supply," *IEEE Trans. on Microwave Theory and Techniques*, vol. 52, no. 1, pp. 112–120, Jan. 2004.
10. **B. Sahu** and G.A. Rincón-Mora, "A low voltage, dynamic, non-inverting, synchronous buck–boost converter for portable applications," *IEEE Trans. on Power Electronics*, vol. 19, no. 2, pp. 443–452, Feb. 2004.
11. **S. Zhou** and G.A. Rincón-Mora, "A high efficiency, soft switching dc–dc converter with adaptive current-ripple control for portable applications," *IEEE Trans. on Circuits and Syst. II*, vol. 53, no. 4, pp. 294–298, Apr. 2006.
12. **M. Chen** and G.A. Rincón-Mora, "An accurate electrical battery model capable of predicting runtime and i–v performance," *IEEE Trans. on Energy Conversion*, vol. 21, no. 2, pp. 504–511, Jun. 2006.
13. **M. Chen** and G.A. Rincón-Mora, "Accurate, compact, and power efficient li-ion battery charger circuit," *IEEE Trans. on Circuits and Syst. II*, vol. 53, no. 11, pp. 1180–1184, Nov. 2006.
14. **H.P. Forghani-zadeh** and G.A. Rincón-Mora, "Low-power CMOS ramp generator circuit for dc–dc converters," *J. of Low Power Electronics*, vol. 2, no. 3, pp. 437–441, Dec. 2006.
15. **B. Sahu** and G. A. Rincón-Mora, "An accurate, low voltage, CMOS switching power supply with adaptive on-time pulse-frequency modulation," *IEEE Trans. on Circuits and Syst. I*, vol. 54, no. 2, pp. 312–321, Feb. 2007.
16. **B. Sahu** and G.A. Rincón-Mora, "A high efficiency WCDMA RF power amplifier (PA) with adaptive, dual-mode buck–boost supply and bias-current control," *IEEE Microwave and Wireless Components Letters*, vol. 17, no. 3, pp. 238–240, Mar. 2007.
17. **V. Gupta** and G.A. Rincón-Mora, "Achieving less than 2% 3- σ mismatch with minimum channel-length CMOS devices," *IEEE Trans. on Circuits and Syst. II*, vol. 54, no. 3, pp. 232–236, Mar. 2007.
18. **H.P. Forghani-zadeh** and G.A. Rincón-Mora, "An accurate, continuous, and lossless self-learning CMOS current-sensing scheme for inductor-based dc–dc converters," *IEEE J. of Solid-State Circuits*, vol. 42, no. 3, pp. 665–679, Mar. 2007.
19. **H.P. Forghani-zadeh** and G.A. Rincón-Mora, "A fast and reliable top-level simulation strategy for mixed-signal ICs and its application to dc–dc converter circuits," *IET Circuits, Devices, and Syst.*, vol. 1, no. 2, pp. 143–150, Apr. 2007.
20. **H.P. Forghani-zadeh** and G.A. Rincón-Mora, "A programmable 210 μ V offset rail-to-rail G_M -C filter," *IEEE Trans. on Circuits and Syst. I*, vol. 54, no. 8, pp. 1636–1646, Aug. 2007.
21. **V. Gupta** and G.A. Rincón-Mora, "Low output impedance 0.6 μ m-CMOS sub-bandgap reference," *IET Electronic Letters*, vol. 43, pp. 1085–1087, Sept. 2007.
22. **N. Keskar** and G.A. Rincón-Mora, "A fast, sigma–delta boost dc–dc converter tolerant to wide LC filter variations," *IEEE Trans. on Circuits and Syst. II*, vol. 55, pp. 198–202, Feb. 2008.

23. **N. Keskar** and G.A. Rincón-Mora, "A compact 1-30 μ H, 1-350 μ F, 5-50m Ω ESR compliant, 1.5% accurate 0.6 μ m CMOS differential sigma-delta boost dc-dc converter," *Analog Integrated Circuits and Signal Processing J.*, vol. 54, no. 3, pp. 157-169, 2008.
24. **M. Chen** and G.A. Rincón-Mora, "A compact electrical model for microscale fuel cells capable of predicting runtime and i-v polarization performance," *IEEE Trans. on Energy Conversion*, vol. 23, no. 3, pp. 842-850, Sept. 2008.
25. **E.O. Torres** and G.A. Rincón-Mora, "Energy-harvesting system-in-package (SiP) microsystem," *ASCE J. of Energy Eng.*, Invited, vol. 134, no. 4, pp. 121-129, Dec. 2008.
26. **S. Kim** and G.A. Rincón-Mora, "Achieving high efficiency under micro-watt loads with switching buck dc-dc converters," *J. of Low Power Electronics*, vol. 5, no. 2, pp. 229-240, Aug. 2009.
27. **D. Kwon** and G.A. Rincón-Mora, "Single-inductor multiple-output (SIMO) switching dc-dc converters," *IEEE Trans. on Circuits and Syst. II*, Invited, vol. 56, no. 8, Aug. 2009.
28. **E.O. Torres** and G.A. Rincón-Mora, "Electrostatic energy-harvesting and battery-charging CMOS system prototype," *IEEE Trans. on Circuits and Syst. I*, vol. 56, no. 9, pp. 1938-1948, Sept. 2009.
29. **L.A. Milner** and G.A. Rincón-Mora, "Limits of predictive current-ripple suppression in switching power supply ICs," *IET Power Electronics*, vol. 3, no. 1, pp. 43-53, Jan. 2010.
30. **V. Gupta** and G.A. Rincón-Mora, "A low-impedance, sub-bandgap 0.6 μ m CMOS reference with 0.84% trimless 3-sigma accuracy and -30dB worst-case PSRR up to 50MHz," *Analog Integrated Circuits and Signal Processing J.*, vol. 62, no. 3, p. 345, 2010.
31. **E.O. Torres** and G.A. Rincón-Mora, "A 0.7 μ m BiCMOS electrostatic energy-harvesting system IC," *IEEE J. of Solid-State Circuits*, vol. 45, no. 2, pp. 483-496, Feb. 2010.
32. **N. Keskar** and G.A. Rincón-Mora, "One clock-cycle response 0.5 μ m CMOS dual-mode sigma-delta dc-dc bypass boost converter stable over wide R_{ESRLC} variations," *Advances in Power Electronics*, vol. 2010, no. 253508, p. 9, 2010.
33. **L.A. Milner** and G.A. Rincón-Mora, "A feed-forward 10 \times CMOS current-ripple suppressor for switching power supplies," *IEEE Trans. on Circuits and Syst. II*, vol. 57, no. 5, pp. 354-378, May 2010.
34. **E.O. Torres** and G.A. Rincón-Mora, "Self-tuning electrostatic energy-harvester IC," *IEEE Trans. on Circuits and Syst. II*, vol. 57, no. 10, pp. 808-812, Oct. 2010.
35. **A. Patel** and G.A. Rincón-Mora, "High power-supply-rejection (PSR) current-mode low-dropout (LDO) regulator," *IEEE Trans. on Circuits and Syst. II*, vol. 57, no. 11, pp. 868-873, Nov. 2010.
36. **D. Kwon** and G.A. Rincón-Mora, "A 2- μ m BiCMOS rectifier-free ac-dc piezoelectric energy harvester-charger IC," *IEEE Trans. on Biomedical Circuits and Syst.*, Invited, vol. 4, no. 6, pp. 400-409, Dec. 2010.
37. **D. Kwon**, G.A. Rincón-Mora, and **E.O. Torres**, "Harvesting ambient kinetic energy with switched-inductor converters," *IEEE Trans. on Circuits and Syst. I*, Invited, vol. 58, no. 7, pp. 1551-1560, July 2011.
38. **R.D. Prabha**, **D. Kwon**, **O. Lazaro**, **K.D. Peterson**, and G.A. Rincón-Mora, "Increasing electrical damping in energy-harnessing transducers," *IEEE Trans. on Circuits and Syst. II*, Invited, vol. 58, no. 12, pp. 787-791, Dec. 2011.
39. **L.A. Milner** and G.A. Rincón-Mora, "Small saturating inductors for more compact switching power supplies," *IEEJ Trans. on Electrical and Electronic Eng.*, vol. 7, no. 1, pp. 69-73, Jan. 2012.
40. **S. Kim** and G.A. Rincón-Mora, "Single-inductor fuel cell-li ion charger-supply IC with nested hysteretic control," *Analog Integrated Circuits and Signal Processing J.*, vol. 70, no. 1, Page 33-45, Jan. 2012.
41. G.A. Rincón-Mora, **A.A. Blanco**, and **J.P. Vogt**, "A 1.3- μ W, 0.6-m CMOS current-frequency analog-digital converter for implantable blood-glucose monitors," *J. of Low Power Electronics*, vol. 8, pp. 47-57, Feb. 2012.
42. **O. Lazaro**, G.A. Rincón-Mora, and **J.P. Vogt**, "1-50-MHz VHF electromagnetic sensor-interface power-attenuation detector circuit," *Int. J. of Electronics and Communications*, vol. 66, no. 6, pp. 502-508, Jun. 2012.
43. **O. Lazaro** and G.A. Rincón-Mora, "Inductively coupled 180-nm CMOS charger with adjustable energy-investment capability," *IEEE Trans. on Circuits and Syst. II*, vol. 60, no. 8, pp. 482-486, Aug. 2013.
44. **O. Lazaro** and G.A. Rincón-Mora, "180-nm CMOS wideband capacitor-free inductively coupled power receiver and charger," *IEEE J. of Solid-State Circuits*, vol. 48, no. 11, pp. 2839-2849, Nov. 2013.
45. **D. Kwon** and G.A. Rincón-Mora, "A single-inductor 0.35- μ m CMOS energy-investing piezoelectric harvester," *IEEE J. of Solid-State Circuits*, vol. 49, no. 10, pp. 2277-2291, Oct. 2014.
46. **A. Blanco** and G.A. Rincón-Mora, "A 44-93- μ s 250-400-mV 0.18- μ m CMOS starter for dc-sourced switched-inductor energy harvesters," *IEEE Trans. on Circuits and Syst. II*, vol. 61, no. 12, pp. 1002-1006, Dec. 2014.

47. **O. Lazaro** and G.A. Rincón-Mora, "A non-resonant self-synchronizing inductively coupled 0.18- μm CMOS power receiver and charger," *IEEE J. of Emerging and Selected Topics in Power Electronics*, vol. 3, no. 1, pp. 261–271, Mar. 2015.
48. **S. Kim** and G.A. Rincón-Mora, "Dual-source hysteretic switched-inductor 0.18- μm complementary metal–oxide–semiconductor charger–supply system," *IET Circuits, Devices, and Syst.*, vol. 9, no. 4, pp. 275–282, 2015.
49. **R.D. Prabha** and G.A. Rincón-Mora, "Maximizing power-transfer efficiency in low-power DC–DC converters," *IET Electronic Letters*, vol. 51, no. 23, pp. 1918–1920, Nov. 2015.
50. **R.D. Prabha** and G.A. Rincón-Mora, "0.18- μm light-harvesting battery-assisted charger–supply CMOS system," *IEEE Trans. on Power Electronics*, vol. 31, no. 4, pp. 2950–2958, Apr. 2016.
51. G.A. Rincón-Mora and **S. Yang**, "Tiny piezoelectric harvesters: Principles, constraints, and power conversion," *IEEE Trans. on Circuits and Syst. I*, Invited, vol. 63, no. 5, pp. 639–649, May 2016.
52. **R.D. Prabha** and G.A. Rincón-Mora, "Drawing the most power from low-cost single-well 1- mm^2 CMOS photovoltaic cells," *IEEE Trans. on Circuits and Syst. II*, vol. 64, no. 1, pp. 46–50, Jan. 2017.
53. **C. Solís** and G.A. Rincón-Mora, "0.6- μm CMOS switched-inductor dual-supply hysteretic current-mode buck converter," *IEEE Trans. on Power Electronics*, vol. 32, no. 3, pp. 2387–2394, Mar. 2017.
54. **A. Blanco** and G.A. Rincón-Mora, "Bootstrapping and resetting CMOS starter for thermoelectric and photovoltaic chargers," *IEEE Trans. on Circuits and Syst. II*, vol. 65, no. 2, pp. 156–160, Feb. 2018.
55. **A. Blanco** and G.A. Rincón-Mora, "Compact fast-waking light/heat-harvesting 0.18- μm CMOS switched-inductor charger," *IEEE Trans. on Circuits and Syst. I*, vol. 65, no. 6, pp. 2024–2034, June 2018.
56. **C. Solís** and G.A. Rincón-Mora, "87%-efficient 330-mW 0.6- μm single-inductor triple-output buck–boost power supply," *IEEE Trans. on Power Electronics*, vol. 33, no. 8, pp. 6837–6844, Aug. 2018.
57. **R.D. Prabha** and G.A. Rincón-Mora, "Light-harvesting CMOS power-supply system for 0–10-mW wireless microsensors," *IEEE Sensors J.*, vol. 19, no. 2, pp. 726–734, Jan. 2019.
58. **S. Yang** and G.A. Rincón-Mora, "Energy-harvesting piezoelectric-powered CMOS series switched-inductor bridge," *IEEE Trans. on Power Electronics*, vol. 34, no. 7, pp. 6489–6497, Jul. 2019.
59. **N. Xing** and G.A. Rincón-Mora, "180-nm 85%-efficient inductively coupled switched resonant half-bridge power receiver," *IEEE Trans. on Circuits and Syst. II*, vol. 66, no. 6, pp. 983–987, Jun. 2019.
60. **C. Solís** and G.A. Rincón-Mora, "Stability and design limits of hysteretic current-mode switched-inductor converters," *IEEE J. of Semiconductor Tech. and Science*, vol. 19, no. 4, pp. 321–326, Jun. 2019.
61. **N. Xing** and G.A. Rincón-Mora, "Highest maximum power point of radially distant inductively coupled power receivers with deep submicron CMOS," *IEEE Trans. on Industrial Informatics*, vol. 16, no. 2, pp. 1086–1093, Feb. 2020.
62. **S. Yang** and G.A. Rincón-Mora, "Efficient power transfers in piezoelectric energy-harvesting switched-inductor chargers," *IEEE Trans. on Circuits and Syst. II*, vol. 68, no. 4, pp. 1248–1252, Apr. 2021.
63. **N. Xing** and G.A. Rincón-Mora, "A self-synchronized maximum-power-point inductively coupled wireless battery charger for embedded microsensors," *IEEE J. of Emerging and Selected Topics in Industrial Electronics*, vol. 2, no. 3, pp. 297–304, July 2021.
64. **A. Dinesh** and G.A. Rincón-Mora, "High-PSR LDOs: Variations, improvements, and best compromise," *IEEE Trans. on Circuits and Syst. II*, vol. 69, no. 3, pp. 924–928, Mar. 2022.
65. **Q. Zhi**, G.A. Rincón-Mora, and **P. Gu**, "Autonomous and programmable 12-W 10-kHz single-cell Li-Ion battery tester," *IEEE Trans. on Instrumentation & Measurement*, vol. 71, pp. 1–8, Apr. 2022.
66. **T. Chang** and G.A. Rincón-Mora, "Design of switched-inductor charging regulator for resistive on-chip thermoelectric generators," *IEEE Trans. on Circuits and Syst. II*, vol. 69, no. 6, pp. 2872–2876, Jun. 2022.
67. **T. Chang** and G.A. Rincón-Mora, "Lowest- V_{IN} CMOS single-inductor boost charger: Design, limits, and validation," *IEEE Trans. on Power Electronics*, vol. 37, no. 9, pp. 10808–10820, Sep. 2022.
68. **S. Yang** and G.A. Rincón-Mora, "Recycling piezoelectric switch-inductor charger," *Analog Integrated Circuits and Signal Processing J.*, no. 113, pp. 403–414, Sep. 2022.
69. **T. Chang** and G.A. Rincón-Mora, "Fast energy-harvesting TEG-supplied charging regulator microsystem," *IEEE Trans. on Power Electronics*, vol. 38, no. 7, pp. 9116–9126, July 2023.
70. **Q. Zhi** and G.A. Rincón-Mora, "Charging Li Ions with Minimal Energy: A Study on Current Profiles," *IEEE Trans. on Consumer Electronics*, vol. 70, no. 1, pp. 122–131, Feb. 2024.

71. **M. Isaf** and G.A. Rincón-Mora, "Piezoelectric Transducers: Complete Electromechanical Model with Parameter Extraction," *Sensors*, Electronic Sensors, **Invited**, vol. 24, iss. 13, no. 4367, July 2024.
 72. **G. Guerin** and G.A. Rincón-Mora, "Compact Switched-Inductor Power Supplies: Design Optimization with 2nd-Order Core-Loss Model," *Electronics*, Power Electronics, **Invited**, vol. 13, iss. 15, no. 2977, July 2024.
 73. **U. Vasudevan** and G.A. Rincón-Mora, "Digital LDO Analysis and All-Stable High-PSR One-LSB Oscillator Design," *Electronics*, Modern Circuits and Systems Technologies, **Invited**, vol. 13, iss. 24, no. 5033, Dec. 2024.
 74. **P. Manocha** and G.A. Rincón-Mora, "Transistor Frequency-Response Analysis: Recursive Shunt-Circuit Transformations," *Electronics*, Analog Circuits and Analog Computing, **Invited**, vol. 14, iss. 2, no. 3390, Jan. 2025.
 75. **T. Zhao** and G.A. Rincón-Mora, "Unraveling Feedback Translations: Gains, Peaking, Stability, and Loop Variations," *IEEE Trans. on Circuits and Syst. II*, vol. 72, iss. 3, pp. 454–458, Mar. 2025.
 76. **L. Cui** and G.A. Rincón-Mora, "Designing Low-Loss Single-Inductor Multiple-I/O (SL-MI/O) CMOS Power Supplies," *IEEE Trans. on Circuits and Syst. I*, **Invited**, vol. 72, iss. 8, pp. 3766– 3776, Aug. 2025.
 77. **G. Guerin** and G.A. Rincón-Mora, "Switched-Inductor DC-DC Converters: Direct Small-Signal Equivalent AC Circuit," *Electronics*, **Invited**, vol. 15, iss. 5, no. 1025, Feb. 2026.
- C2. Invited Journal Articles** (Prof. Rincón-Mora advised boldfaced authors.)
78. **N. Keskar** and G.A. Rincón-Mora, "A user-friendly boost dc–dc converter topology – it's fast and widely stable," *Power Management Des. Line*, Jan. 23, 2005.
 79. **N. Keskar** and G.A. Rincón-Mora, "A user-friendly boost dc–dc converter topology – it's fast and widely stable," *Planet Analog*, Jan. 26, 2005.
 80. **V. Gupta** and G.A. Rincón-Mora, "Inside the belly of the beast: A map for the wary bandgap reference designer when confronting process variations," *Power Management Des. Line*, Feb. 18, 2005.
 81. G.A. Rincón-Mora and **P. Forghani**, "Accurate and lossless current-sensing techniques: A practical myth?" *Power Management Des. Line*, Mar. 17, 2005.
 82. G.A. Rincón-Mora and **M. Chen**, "Self-powered chips – the work of fiction," *Power Management Des. Line*, Apr. 28, 2005.
 83. G.A. Rincón-Mora and **M. Chen**, "Self-powered chips – the work of fiction," *Planet Analog*, Apr. 28, 2005.
 84. **L. Milner** and G.A. Rincón-Mora, "Taming power inductors for system-on-Chip (SoC) integration," *Power Management Des. Line*, May 18, 2005.
 85. **N. Keskar** and G.A. Rincón-Mora, "A user-friendly boost dc–dc converter topology," *Electronic Eng. Times Japan*, no. 1, 2005.
 86. **E. Torres** and G.A. Rincón-Mora, "Energy-harvesting chips and the quest for everlasting life," *Power Management Des. Line*, Jun. 30, 2005.
 87. G.A. Rincón-Mora and **H. Pan**, "Quenching the thirst of RF power amps and extending the life of portable devices," *Power Management Des. Line*, Jul. 15, 2005.
 88. G.A. Rincón-Mora and **H. Pan**, "Quenching the thirst of RF power amps and extending the life of portable devices," *Planet Analog*, Jul. 31, 2005.
 89. **N. Keskar** and G.A. Rincón-Mora, "A fast, accurate, LC compliant dc–dc boost regulator...Is it possible?" *Power Management Des. Line*, Aug. 22, 2005.
 90. **E. Torres** and G.A. Rincón-Mora, "Harvesting ambient energy will make embedded devices autonomous," *Electronic Eng. Times' Embedded*, Aug. 29, 2005.
 91. G.A. Rincón-Mora and **V. Gupta**, "Power supply ripple rejection and linear regulators: What's all the noise about?" *Power Management Des. Line*, Sept. 20, 2005.
 92. **E. Torres** and G.A. Rincón-Mora, "Harvesting ambient energy," *Electronic Eng. Times*, Aug. 29, 2005.
 93. G.A. Rincón-Mora and **V. Gupta**, "Power supply ripple rejection and linear regulators: What's all the noise about?" *Planet Analog*, Sept. 20, 2005.
 94. G.A. Rincón-Mora and **P. Forghani**, "Self-learning switching dc–dc converters meet smart power," *Power Management Des. Line*, Oct. 13, 2005.
 95. G.A. Rincón-Mora and **M. Chen**, "Attempting clairvoyance with battery performance," *Power Management Des. Line*, Nov. 20, 2005.

96. G.A. Rincón-Mora and **L.A. Milner**, "How to fully integrate switching dc–dc supplies with inductor multipliers," *Power Management Des. Line*, Dec. 18, 2005.
 97. G.A. Rincón-Mora and **L.A. Milner**, "How to fully integrate switching dc–dc supplies with inductor multipliers," *Planet Analog*, Dec. 18, 2005.
 98. G.A. Rincón-Mora and **N. Keskar**, "Cloaking the non-idealities of dc–dc converter stability," *Power Management Des. Line*, Jan. 20, 2006.
 99. G.A. Rincón-Mora and **N. Keskar**, "Cloaking the non-idealities of dc–dc converter stability," *Planet Analog*, Jan. 20, 2006.
 100. **E. Torres** and G.A. Rincón-Mora, "Harvesting energy into lithium-ion batteries," *Power Management Des. Line*, Feb. 14, 2006.
 101. **E.O. Torres** and G.A. Rincón-Mora, "Harvesting energy into lithium-ion batteries," *Planet Analog*, Feb. 14, 2006.
 102. **V. Gupta** and G.A. Rincón-Mora, "Reduce transistor mismatch errors without costly trimming and noisy chopping schemes," *Power Management Des. Line*, Mar. 24, 2006.
 103. **V. Gupta** and G.A. Rincón-Mora, "Reduce transistor mismatch errors without costly trimming and noisy chopping schemes," *Planet Analog*, Mar. 24, 2006.
 104. **P. Forghani** and G.A. Rincón-Mora, "Improve top-level simulation strategy for switching dc–dc converters," *Power Management Des. Line*, Apr. 16, 2006.
 105. **P. Forghani** and G.A. Rincón-Mora, "Improve top-level simulation strategy for switching dc–dc converters," *Planet Analog*, Apr. 16, 2006.
 106. G.A. Rincón-Mora and **M. Chen**, "Li-ion battery chargers – not just another design," *Power Management Des. Line*, May 17, 2006.
 107. G.A. Rincón-Mora and **L.A. Milner**, "Can SoC switching regulators answer the challenge of their SiP counterparts?" *Power Management Des. Line*, Jun. 21, 2006.
 108. G.A. Rincón-Mora and **J. Vogt**, "Fooling Faraday: On-chip capacitor multipliers," *Power Management Des. Line*, Jul. 27, 2006.
 109. G.A. Rincón-Mora and **N. Keskar**, "Unscrambling the power losses in switching boost converters," *Power Management Des. Line*, Aug. 18, 2006.
 110. G.A. Rincón-Mora and **E. Torres**, "Energy harvesting: A battle against power losses," *Power Management Des. Line*, Sept. 23, 2006.
 111. G.A. Rincón-Mora and **E. Torres**, "Energy harvesting: A battle against power losses," *Planet Analog*, Oct. 8, 2006.
 112. G.A. Rincón-Mora and **V. Gupta**, "Bandgaps in the crosshairs: What's the trim target?" *Power Management Des. Line*, Oct. 18, 2006.
 113. G.A. Rincón-Mora and **V. Gupta**, "Bandgaps in the crosshairs: What's the trim target?" *Planet Analog*, Oct. 18, 2006.
 114. G.A. Rincón-Mora and **M. Chen**, "Squeezing operational life out of a shrinking energy capsule," *Power Management Des. Line*, Nov. 20, 2006.
 115. G.A. Rincón-Mora and **L. Milner**, "Inductors and multipliers in practice – Get efficient transfer of energy," *Power Management Des. Line*, Jan. 1, 2007.
 116. G.A. Rincón-Mora and **J. Vogt**, "Self-powered wireless sensor nodes: Among other things, a load management feat," *Power Management Des. Line*, Jan. 24, 2007.
 117. G.A. Rincón-Mora and **J. Vogt**, "Self-powered wireless sensor nodes: Among other things, a load management feat," *Planet Analog*, Jan. 24, 2007.
 118. G.A. Rincón-Mora and **J. Vogt**, "Self-powered wireless sensor nodes," *Electronic Components* (in Russian), no. 11, pp. 51-56, Nov. 15, 2007.
 119. **E.O. Torres**, **L.A. Milner**, and G.A. Rincón-Mora, "Hybrid supplies for wireless micro-systems," *The Electrochemical Soc.'s Interface*, vol. 17, no. 3, pp. 57–60, Fall 2008.
 120. G.A. Rincón-Mora, "Introduction to the special section on energy-harvesting/scavenging circuits and systems," *IEEE Trans. on Circuits and Syst. II*, vol. 58, no. 12, pp. 785–786, Dec. 2011.
- C3. Refereed Conference Articles** (Prof. Rincón-Mora advised boldfaced authors.)
121. P.E. Allen, B.J. Blalock, and G.A. Rincón, "A 1V CMOS op amp using bulk-driven MOSFETs," *IEEE Int. Solid-State Circuits Conf.*, pp. 192–193, San Francisco, CA, 1995.

122. P.E. Allen, B.J. Blalock, and G.A. Rincón, "Low voltage analog circuits using standard CMOS technology," IEEE Int. Symp. on Low Power Des., pp. 209–214, Laguna, CA, 1995.
123. **B. Sahu** and G.A. Rincón-Mora, "Syst.-level requirements of dc–dc converters for dynamic power supplies of power amplifiers," IEEE Asia-Pacific Conf. on ASICs, pp. 149–152, Taipei, Taiwan, 2002.
124. **M. Gildersleeve, H.P. Forghani-zadeh,** and G.A. Rincón-Mora, "A comprehensive power analysis and a highly efficient, mode-hopping dc–dc converter," IEEE Asia-Pacific Conf. on ASICs, pp. 153–156, Taipei, Taiwan, 2002.
125. **V. Gupta** and G.A. Rincón-Mora, "Predicting the effects of error sources in bandgap reference circuits and evaluating their design implications," IEEE Midwest Symp. on Circuits and Syst., vol. 3, pp. 575–578, Tulsa, OK, 2002.
126. **H.P. Forghani-zadeh** and G.A. Rincón-Mora, "Current-sensing techniques for dc–dc converters," IEEE Midwest Symp. on Circuits and Syst., vol. 2, pp. 577–580, Tulsa, OK, 2002.
127. **A. Makharia** and G.A. Rincón-Mora, "Integrating power inductors onto the IC – SOC implementation of inductor multipliers for dc–dc converters," IEEE Ind. Electronics Conf., vol. 1, pp. 556–561, Roanoke, VA, 2003.
128. **V. Gupta,** G. A. Rincón-Mora, and P. Raha, "Analysis and design of monolithic, high PSR, linear regulators for SoC applications," IEEE Int. Syst.-on-Chip Conf., pp. 311–315, Santa Clara, CA, 2004.
129. **N.A. Keskar** and G.A. Rincón-Mora, "Self-stabilizing, integrated, hysteretic boost dc–dc converter," IEEE Ind. Electronics Conf., TA3-4, vol. 1, pp. 586–591, Busan, Korea, 2004.
130. **M. Chen** and G.A. Rincón-Mora, "A self-powered, self-sustaining system-on-chip (SOC) solution powered from hybrid micro-fuel cells," Army Science Conf., Orlando, FL, 2004.
131. **B. Sahu** and G.A. Rincón-Mora, "Adaptive power management of linear RF power amplifiers – an integrated system design approach," IEEE Asia-Pacific Microwave Conf., New Delhi, India, 2004.
132. **B. Sahu** and G.A. Rincón-Mora, "A high-efficiency, dual-mode, dynamic, buck–boost power supply IC for portable applications," IEEE Int. Conf. on VLSI Des., pp. 858–861, Kolkota, India, 2005.
133. **V. Gupta** and G.A. Rincón-Mora, "Predicting and designing for the impact of process variations and mismatch on the trim range and yield of bandgap references," IEEE Int. Symp. on Quality Electronic Des., pp. 503–508, Santa Clara, CA, 2005.
134. **V. Gupta** and G.A. Rincón-Mora, "A low dropout, CMOS regulator with high PSR over wideband frequencies," IEEE Int. Symp. on Circuits and Syst., vol. 5, pp. 4245–4248, Tokyo, Japan, 2005.
135. **E.O. Torres** and G.A. Rincón-Mora, "Long lasting, self-sustaining, and energy-harvesting system-in-package (SiP) sensor solution," Int. Conf. on Energy, Environment, and Disasters, Sess. A-2, ID 368, pp. 1–33, Charlotte, NC, 2005.
136. **L.A. Milner** and G.A. Rincón-Mora, "A novel predictive inductor multiplier for integrated circuit dc–dc converters in portable applications," Int. Symp. on Low Power Electronics and Des., pp. 84–89, San Diego, CA, 2005.
137. **H.P. Forghani-zadeh** and G.A. Rincón-Mora, "A continuous, low glitch, low-offset, programmable gain and bandwidth, Gm–C filter," IEEE Midwest Symp. on Circuits and Syst., pp. 1629–1632, Cincinnati, OH, 2005.
138. **H.P. Forghani-zadeh** and G.A. Rincón-Mora, "A lossless, accurate, self-calibrating current-sensing technique for dc–dc converters," IEEE Ind. Electronics Conf., PE-03, pp. 549–554, Raleigh, NC, 2005.
139. **N. Keskar** and G.A. Rincón-Mora, "A high bandwidth, bypass, transient-mode sigma–delta dc–dc switching boost regulator with wide LC compliance," IEEE Ind. Electronics Conf., PE-03, pp. 543–548, Raleigh, NC, 2005.
140. **E. Torres, L. Milner, N. Keskar, M. Chen, H. Pan, V. Gupta, P. Forghani,** and G.A. Rincón-Mora, "SiP integration of intelligent, adaptive, self-sustaining power management solutions for portable applications," IEEE Int. Symp. on Circuits and Syst., pp. 5311–5314, Kos, Greece, 2006.
141. **H.I. Pan** and G.A. Rincón-Mora, "Asynchronous nonlinear power-tracking supply for power efficient linear RF PAs," IEEE Int. Conf. on Communications, Circuits, and Syst., pp. 2531–2535, Guilin, China, 2006.
142. **E.O. Torres** and G.A. Rincón-Mora, "Electrostatic energy harvester and li-ion charger for micro-scale applications," IEEE Midwest Symp. on Circuits and Syst., pp. 65–69, San Juan, Puerto Rico, 2006.
143. **V. Gupta** and G.A. Rincón-Mora, "A 5mA 0.6 μ m CMOS Miller-compensated LDO regulator with –27dB worst case power supply rejection using 60pF of on-chip capacitance," IEEE Int. Solid-State Circuits Conf., pp. 520–521, San Francisco, CA, 2007.
144. **J.P. Vogt** and G.A. Rincón-Mora, "SiP wireless micro-power sensors," Government Microcircuit Appl. and Critical Tech. Conf., Invited, Lake Buena Vista, FL, 2007.
145. **N. Keskar** and G.A. Rincón-Mora, "Designing an accurate and robust LC-compliant asynchronous sigma–delta boost dc–dc converter," IEEE Int. Symp. on Circuits and Syst., pp. 549–552, New Orleans, LA, 2007.

146. **M. Chen, J.P. Vogt**, and G.A. Rincón-Mora, "Des. methodology of a hybrid micro-scale fuel cell–thin-film lithium ion source," IEEE Int. Midwest Symp. on Circuits and Syst., pp. 674–677, Montreal, Canada, 2007.
147. **M. Chen** and G.A. Rincón-Mora, "Single inductor, multiple input, multiple output (SIMIMO) power mixer–charger–supply system," Int. Symp. on Low Power Electronics and Des., pp. 310–315, Portland, OR, 2007.
148. W. Mustain, S. Prakash, H. Kim, P. Kohl and G. Rincón-Mora, "Micro DMFC–lithium ion hybrid power source for low power applications," Meeting of the Electrochemical Soc., Washington D.C., 2007.
149. **S. Kim**, G.A. Rincón-Mora, S. Kim, P. Kohl, **O. Lazaro**, W. Mustain, S. Prakash, D. Rivera, J. Vogt, and F. Sienkiewicz, "1cm³ fuel-cell, li-ion powered, wireless sensor instrumentation chip," ITEA Test Instrumentation Workshop, Invited, Lancaster, CA, 2008.
150. **L.A. Milner** and G.A. Rincón-Mora, "Mixing sourcing technologies to extend the operational lifetime of ultra-portable micro-scale electronics," Annual Meeting of the Int. Soc. of Electrochemistry, Invited, Seville, Spain, 2008.
151. **S. Kim** and G.A. Rincón-Mora, "Single-inductor dual-input dual-output buck–boost fuel cell-li ion charging dc–dc converter," IEEE Int. Solid-State Circuits Conf., pp. 444–445, San Francisco, CA, 2009.
152. **O. Lazaro**, G.A. Rincón-Mora, **J. Vogt**, "1–50-MHz VHF EMI instrumentation sensor circuit," ITEA Test Instrumentation Workshop, Invited, Ridgecrest, CA, May 12–14, 2009.
153. **D. Kwon** and G.A. Rincón-Mora, "A rectifier-free piezoelectric energy harvester circuit," IEEE Int. Symp. on Circuits and Syst., pp. 1085–1088, Taipei, Taiwan, May 24–27, 2009.
154. **E. Torres** and G.A. Rincón-Mora, "Energy budget and high-gain strategies for voltage-constrained electrostatic harvesters," IEEE Int. Symp. on Circuits and Syst., pp. 1101–1104, Taipei, Taiwan, 2009.
155. **D. Kwon** and G.A. Rincón-Mora, "Operation-based signal-flow ac analysis of switching dc–dc converters in CCM and DCM," IEEE Int. Midwest Symp. on Circuits and Syst., pp. 957–960, Cancún, Mexico, 2009.
156. **D. Kwon** and G.A. Rincón-Mora, "A Single-inductor ac–dc piezoelectric energy-harvester/battery-charger IC converting $\pm(0.35$ to 1.2V) to (2.7 to 4.5V)," IEEE Int. Solid-State Circuits Conf., pp. 494–495, San Francisco, CA, 2010.
157. **D. Kwon**, G.A. Rincón-Mora, and **E. Torres**, "Harvesting kinetic energy with switched-inductor dc–dc converters," IEEE Int. Symp. on Circuits and Syst., Invited, Paris, France, 2010.
158. **O. Lazaro** and G.A. Rincón-Mora, "Comparative efficiency analysis of dynamically supplied power amplifiers (PA)," IEEE Int. Conf. on Electronics, Circuits, and Syst., Athens, Greece, 2010.
159. **R.D. Prabha**, G.A. Rincón-Mora, and **S. Kim**, "Harvesting circuits for miniaturized photovoltaic cells," IEEE Int. Symp. on Circuits and Syst., Invited, pp. 309–312, Rio de Janeiro, Brazil, 2011.
160. **O. Lazaro** and G.A. Rincón-Mora, "Minimizing MOSFET power losses in near-field electromagnetic energy-harnessing ICs," IEEE Int. Syst.-on-Chip Des. Conf., Jeju, Korea, 2011.
161. **S. Kim**, G.A. Rincón-Mora, and **D. Kwon**, "Extracting the frequency response of switching dc–dc converters in CCM and DCM from time-domain simulations," IEEE Int. Syst.-on-Chip Des. Conf., Jeju, Korea, 2011.
162. **K. Peterson** and G.A. Rincón-Mora, "High-damping energy-harvesting electrostatic CMOS charger," IEEE Int. Symp. on Circuits and Syst., Seoul, Korea, 2012.
163. **S. Kim** and G.A. Rincón-Mora, "Efficiency of switched-inductor dc–dc converter ICs across process technologies," IEEE Int. Symp. on Circuits and Syst., Seoul, Korea, 2012.
164. **D. Kwon** and G.A. Rincón-Mora, "Energy-investment schemes for increasing output power in piezoelectric harvesters," IEEE Midwest Symp. on Circuits and Syst., Invited, Boise, ID, 2012.
165. **D. Kwon** and G.A. Rincón-Mora, "A single-inductor 0.35- μ m CMOS energy-investing piezoelectric harvester," IEEE Int. Solid-State Circuits Conf., San Francisco, CA, 2013.
166. **R.D. Prabha** and G.A. Rincón-Mora, "Battery-assisted and photovoltaic-sourced switched-inductor CMOS harvesting charger–supply," IEEE Int. Symp. on Circuits and Syst., Beijing, China, 2013.
167. **A. Blanco** and G.A. Rincón-Mora, "On-chip starter circuit for switched-inductor dc–dc harvester systems," IEEE Int. Symp. on Circuits and Syst., Invited, Beijing, China, 2013.
168. **C. Solís** and G.A. Rincón-Mora, "Nested hysteretic current-mode single-inductor multiple-output (SIMO) boosting buck converter," IEEE Int. NEWCAS Conf., Paris, France, 2013.
169. **R.D. Prabha** and G.A. Rincón-Mora, "CMOS photovoltaic-cell layout configurations for harvesting microsystems," IEEE Midwest Symp. on Circuits and Syst., Invited, Columbus, OH, 2013.
170. **C. Solís** and G.A. Rincón-Mora, "Stability analysis & design of hysteretic current-mode switched-inductor buck dc–dc converters," IEEE Int. Conf. on Electronics, Circuits, and Syst., Abu Dhabi, UAE, 2013.

171. **S. Kim** and G.A. Rincón-Mora, "Dual-source single-inductor 0.18-um CMOS charger–supply with nested hysteretic and adaptive on-time PWM control," IEEE Int. Solid-State Circuits Conf., San Francisco, CA, 2014.
172. **A. Blanco** and G.A. Rincón-Mora, "Measuring micro-amp inductor currents in switched-inductor dc–dc power supplies," IEEE Int. Instrumentation and Measurement Tech. Conf., Montevideo, Uruguay, 2014.
173. **J. Cowan** and G.A. Rincón-Mora, "Harvesting the highest power from tiny electrostatic transducers with CMOS circuits," IEEE Midwest Symp. on Circuits and Syst., Invited, College Station, TX, 2014.
174. **J.Y. Lee** and G.A. Rincón-Mora, "Maximum power-point extraction of small switched-inductor piezoelectric harvesters," IEEE Int. Conf. on Electronics Circuits and Syst., Marseille, France, 2014.
175. **N. Xing** and G.A. Rincón-Mora, "Generating the highest power with a tiny and distant inductively coupled coil," IEEE Int. Symp. on Ind. Electronics, Santa Clara, CA, 2016.
176. **S. Yang** and G.A. Rincón-Mora, "Optimally pre-damped switched-inductor piezoelectric energy-harvesting charger," IEEE Int. NEWCAS Conf., Vancouver, Canada, 2016.
177. **A. Blanco** and G.A. Rincón-Mora, "Energy-harvesting microsensors: Low-energy task schedule & fast drought-recovery design," IEEE Midwest Symp. on Circuits and Syst., Invited, Abu Dhabi, UAE, 2016.
178. **R.D. Prabha** and G.A. Rincón-Mora, "How to design battery-assisted photovoltaic switched-inductor CMOS charger–supplies," IEEE Int. Symp. on Circuits and Syst., Baltimore, MD, 2017.
179. **C. Solís** and G.A. Rincón-Mora, "Stability and design of hysteretic current-mode single-inductor multiple-output power supplies," IEEE Midwest Symp. on Circuits and Syst., Invited, Boston, MA, 2017.
180. **N. Xing** and G.A. Rincón-Mora, "Loss analysis and maximum output power scheme in inductively coupled receivers," IEEE Midwest Symp. on Circuits and Syst., Invited, Boston, MA, 2017.
181. **D. Janke** and G.A. Rincón-Mora, "Ripple suppression of on-chip switched-inductor power supplies," IEEE Int. Symp. on Circuits and Syst., Invited, Florence, Italy, 2018.
182. **A. Wilson**, W.D. Hunt, and G.A. Rincón-Mora, "Series synchronous capacitor-clamped GaN electrostatic energy-harvesting power stage," IEEE Conf. on Technologies for Sustainability, Long Beach, CA, 2018.
183. **S. Yang** and G.A. Rincón-Mora, "Least lossy piezoelectric energy-harvesting charger," IEEE Midwest Symp. on Circuits and Syst., Dallas, TX, 2019.
184. **T. Chang** and G.A. Rincón-Mora, "Lowest v_{IN} possible for switched-inductor boost converters," IEEE Midwest Symp. on Circuits and Syst., Dallas, TX, 2019.
185. **N. Xing** and G.A. Rincón-Mora, "Highest wireless power: Inductively coupled or RF?" IEEE Int. Symp. on Quality Electronic Design, Santa Clara, CA, 2020.
186. **S. Yang** and G.A. Rincón-Mora, "Piezoelectric CMOS charger: Highest output power design," IEEE Int. Symp. on Quality Electronic Des., Santa Clara, CA, 2020.
187. **G. Guerin** and G.A. Rincón-Mora, "Understanding and quantifying i_{DS} – v_{DS} overlap losses in switched-inductor power supplies," IEEE Int. Conf. on Electronics, Circuits & Systems, Glasgow, Scotland, 2020.
188. **G. Guerin** and G.A. Rincón-Mora, "Power-conversion efficiency: Loss dominance, optimization, & design insight," IEEE Int. Symp. on Circuits and Syst., Invited, Daegu, Korea, 2021.
189. **T. Chang** and G.A. Rincón-Mora, "Optimal high-efficiency DCM design of switched-inductor CMOS power supplies," IEEE Midwest Symp. on Circuits and Syst., East Lansing, MI, 2021.
190. **V. Gupta** and G.A. Rincón-Mora, "Dimming dc–dc LED drivers: Luminous efficiency, power losses, & best-in-class," IEEE Ind. Electronics Conf., Toronto, Canada, 2021.
191. **T. Chang** and G.A. Rincón-Mora, "Fast & efficient hysteretic power supplies for IoT microsensors: Analysis & design with insight," IEEE Int. Symp. on Circuits and Syst., Invited, Austin, TX, 2022.
192. **M.L. Isaf** and G.A. Rincón-Mora, "Piezoelectric transducers for energy harvesting: Electromechanical model, ambient motion, and electrical loads," IEEE Int. Conf. on Electronics, Circuits & Systems, Glasgow, Scotland, 2022.
193. **G. Guerin** and G.A. Rincón-Mora, "Salvaging gate-drive power in switched power supplies," IEEE Midwest Symp. on Circuits and Syst., Phoenix, AZ, 2023.
194. **X. Li** and G.A. Rincón-Mora, "Maximum power-point theory for thermoelectric harvesters," IEEE Midwest Symp. on Circuits and Syst., Phoenix, AZ, 2023.
195. **Q. Chen** and G.A. Rincón-Mora, "Maximum dc–dc conversion in switched-inductor power supplies," IEEE Midwest Symp. on Circuits and Syst., Phoenix, AZ, 2023.

196. X. Li and G.A. Rincón-Mora, "80–300 mV, 255–1600 Ω Thermoelectric Maximum-Power-Point-Tracking CMOS Charger," Semiconductor Research Corporation TechCon, Austin, TX, 2024.
197. L. Cui and G.A. Rincón-Mora, "Switched-Inductor Multiple-I/O Power Supplies: MOSFET Selection and Cross Conduction," IEEE Midwest Symp. on Circuits and Syst., Springfield, MA, 2024.

C4. Engineering Industry Reports

198. G.A. Rincón-Mora, "Voltage References – Part I," Texas Instruments Inc., Jul. 1998.
199. G.A. Rincón-Mora, "Voltage References – Part II," Texas Instruments Inc., Jan. 1999.
200. G.A. Rincón-Mora, "Linear Regulators," Texas Instruments Inc., Nov. 1999.

C5. Refereed Artistic Publications (sole author unless otherwise indicated)

201. "Strawberry Delight" [poem], *Forgotten Moments* (ISBN: 1-58235-159-7), Editor's Choice Award, 2000.
202. "Ojitos Verdes" [poem], *Nuevo Impacto*, Oct. 2002.
203. "A Christmas Tale" [short story], *ISB Cafe*, Dec. 2002.
204. "Mi Querida Daniela" [short story], *Shades Of Romance Magazine (SORM)*, Jan.–Feb. 2003.
205. "Mi Querida Daniela" [short story], ECESIS, Georgia Tech, Spring 2004.
206. "Flor Andina" [poem], *Nuevo Impacto*, Jul. 2004.
207. "The Bund" [photograph] - *Chapter & Verse - A Publication of the Hong Kong Int. Literary Festival Ltd.*, 2004.
208. "Little Lots" [poem], ECESIS, Georgia Tech, Spring 2005.
209. "Just passing through (Island of Idra)" [photograph] – *Photographers of Greece*.
210. "Working and Teaching in Tanzania," *Volunteers for Peace*, Nov. 2005.
211. "Let me...let me in there! (Mwanga, Tanzania)," *Volunteers for Peace Newslett.*, Jan. 2006.
212. "Home!" [photograph] - ECESIS, Georgia Tech, Spring 2006.
213. "Mind and Heart" [poem] - ECESIS, Georgia Tech, Spring 2007.
214. M. Cheng, G.A. Rincón-Mora, and G. Heaney, "Volunteers For Peace," *Wishtank, J. of Intellectual Freedom*, Jun. 2007.
215. "The Girl in White" [poem] - ECESIS, Georgia Tech, Spring 2008.

D. Educational Videos

- "Two-port models," *YouTube*, 8 min. 45 sec., Jan. 2020.
- "Poles in analog circuits," *YouTube*, 14 min. 16 sec., Jan. 2020.
- "Gain–bandwidth product," *YouTube*, 5 min. 06 sec., Feb. 2020.
- "Bypass (feed-forward) zeros in analog circuits," *YouTube*, 14 min. 58 sec., Feb. 2020.
- "Current-limited zeros in analog circuits," *YouTube*, 12 min. 10 sec., Mar. 2020.
- "Feedback (Miller) capacitors," *YouTube*, 13 min. 22 sec., Mar. 2020.
- "Frequency response: Analysis," *YouTube*, 11 min. 23 sec., Apr. 2020.
- "Common-emitter/source transistor," *YouTube*, 15 min. 21 sec., Apr. 2020.
- "Emitter/source degeneration," *YouTube*, 30 min. 42 sec., May 2020.
- "Common-base/gate transistor," *YouTube*, 15 min. 25 sec., May 2020.
- "Base degeneration," *YouTube*, 12 min. 39 sec., Jun. 2020.
- "Common-collector/drain transistor," *YouTube*, 18 min. 56 sec., Jun. 2020.
- "Cascade amplifiers," *YouTube*, 14 min. 20 sec., Jul. 2020.
- "Energy-band diagram," *YouTube*, 10 min. 56 sec., Jul. 2020.
- "Semiconductors," *YouTube*, 10 min. 56 sec., Aug. 2020.
- "PN-junction diode," *YouTube*, 22 min. 13 sec., Aug. 2020.
- "Schottky diode," *YouTube*, 9 min. 28 sec., Sep. 2020.
- "BJT: Activation," *YouTube*, 17 min. 54 sec., Sep. 2020.
- "BJT: Modes," *YouTube*, 17 min. 53 sec., Oct. 2020.
- "N-channel JFET," *YouTube*, 19 min. 55 sec., Oct. 2020.
- "P-channel JFET," *YouTube*, 22 min. 46 sec., Nov. 2020.

22. "NMOS: Sub-Threshold," *YouTube*, 17 min. 20 sec., Nov. 2020.
23. "NMOS: Inversion," *YouTube*, 20 min. 10 sec., Dec. 2020.
24. "PMOS: Sub-Threshold," *YouTube*, 15 min. 33 sec., Dec. 2020.
25. "PMOS: Inversion," *YouTube*, 20 min. 38 sec., Jan. 2021.

E. Commercial Designs/Products Released to Production (RTP)

1. G.A. Rincón-Mora (Project Leader), TPS2810 - CMOS driver, RTP 1995.
2. G.A. Rincón-Mora (Project Leader), TPS2811 - CMOS driver, RTP 1995.
3. G.A. Rincón-Mora (Project Leader), TPS2816 - CMOS driver, RTP 1995.
4. G.A. Rincón-Mora (Project Leader), TPS2817 - CMOS driver, RTP 1995.
5. G.A. Rincón-Mora (Project Leader), TPS2818 - CMOS driver, RTP 1995.
6. G.A. Rincón-Mora (Project Leader), TPS2819 - CMOS driver, RTP 1995.
7. G.A. Rincón-Mora (Circuit Designer), Viper - BiCMOS Wireless Power Manag. IC, RTP 1995.
8. G.A. Rincón-Mora (Circuit Designer), Viper Lite - BiCMOS Low Dropout Regulator, RTP 1996.
9. G.A. Rincón-Mora (Circuit Designer), Maverick - BiCMOS Wireless Power Manag., RTP 1997.
10. G.A. Rincón-Mora (Circuit Designer), TPS912x - BiCMOS Wireless Power Manag., RTP 1998.
11. G.A. Rincón-Mora (Des. Team Leader), TPS5210 - BiCMOS PWM Controller, RTP 1998 - featured on *EDN's Top 100 Products* and on the cover of *Electronic Des.*.
12. G.A. Rincón-Mora (Des. Team Leader), TPS5615 - BiCMOS PWM Controller, RTP 1998.
13. G.A. Rincón-Mora (Des. Team Leader), TPS5618 - BiCMOS PWM Controller, RTP 1998.
14. G.A. Rincón-Mora (Des. Team Leader), TPS5625 - BiCMOS PWM Controller, RTP 1998.
15. G.A. Rincón-Mora (Des. Team Leader), TPS5633 - BiCMOS PWM Controller, RTP 1998.
16. G.A. Rincón-Mora (Des. Team Leader), SN104685DW - BiCMOS PWM Controller, RTP 1998.
17. G.A. Rincón-Mora (Des. Team Leader), TPS7415D - CMOS Liner Reg., RTP 1999.
18. G.A. Rincón-Mora (Des. Team Leader), TPS7418D - CMOS Liner Reg., RTP 1999.
19. G.A. Rincón-Mora (Des. Team Leader), TPS7425D - CMOS Liner Reg., RTP 1999.
20. G.A. Rincón-Mora (Des. Team Leader), TPS7430D - CMOS Liner Reg., RTP 1999.
21. G.A. Rincón-Mora (Des. Team Leader), TPS7433D - CMOS Liner Reg., RTP 1999.
22. G.A. Rincón-Mora (Des. Team Leader), TPS56100 - 5V BiCMOS PWM Controller, RTP 1999.
23. G.A. Rincón-Mora (Tech. Advisor/Circuit Designer), TPS56300 - BiCMOS Chrg Pump, RTP 1999.
24. G.A. Rincón-Mora (Des. Team Leader), TPS5211 - BiCMOS 1MHz Hyst. Controller, RTP 1999.
25. G.A. Rincón-Mora (Des. Team Leader), TPS5300 - BiCMOS Laptop PWM Controller, RTP 2001.
26. G.A. Rincón-Mora (Circuit Designer), MSP430's LDO - BiCMOS Low Dropout Regulator, RTP 2004.

F. Speeches and Presentations

F1. Keynotes Addresses and Plenary Speeches

1. "Orgullo Hispano," Robins Air Force Base, Sept. 23, 2003.
2. "Robins AFB Hispanic Heritage Luncheon," Robins Air Force Base, Oct. 3, 2005.
3. "Energy & Power Management Trends," Analog Leaders Forum, Seoul, Korea, Oct. 16, 2009.
4. "Energizing & Powering Microsystems," IEEE Int. Syst.-on-Chip Des. Conf., Busan, Korea, Nov. 23, 2009.
5. "Energy-Harvesting ICs," IEEE Circuits & Syst. for Medical & Environmental Appl., Mérida, Mexico, Dec. 13–15, 2010.
6. "Powering Wireless Microsensors," Semiconductor Equipment & Material Int. Conf., Seoul, Korea, Feb. 12–14, 2013.
7. "Non-academic Routes Beyond the Eng. Ph.D.," Sloan Foundation, Atlanta, GA, Apr. 14, 2014.
8. "Powering Microsystems," IEEE Int. Conf. on Microelectronics, Doha, Qatar, Dec. 14–17, 2014.
9. "Microwatt CMOS Harvesters," Infrared Radiation, Thermoelectricity and Chaos Workshop, U.S. Office of Naval Res., James Madison Univ., Harrisonburg, VA, Jun. 17, 2015.
10. "Higher Education, Success, and Life in Electrical Eng.," IEEE Solid-State Circuits & Circuits and Syst. Soc., Tainan, Taiwan, Nov. 19, 2015.

11. "Powering Intelligent IoT Microsensors," IEEE Int. Syst.-on-Chip Des. Conf., Jeju, Korea, Oct. 25, 2016.
12. "Life, Happiness, Success, and Higher Education in Electrical Eng.," IEEE Solid-State Circuits & Circuits and Syst. Soc., Tainan, Taiwan, Nov. 21, 2016.
13. "Energy-Harvesting IoT Microsensors," CMOS Emerging Technologies, Warsaw, Poland, Mar. 28–30, 2017.
14. "On The Elusive Art of Managing Time & Res. Projects," IEEE Solid-State Circuits & Circuits and Syst. Soc., Tainan, Taiwan, Nov. 15, 2017.
15. "Energizing Miniaturized IoT Sensors," IEEE Int. Conf. on Des. & Test of Integrated Micro & Nano-Syst., Gammarth, Tunisia, Apr. 29, 2019.

F2. Distinguished Lectures (plenary)

16. *Linear Regulators* (1 day). IEEE SSCS Distinguished Lecture in Hsinchu, Taiwan, Jun. 8, 2006.
17. *Linear Regulators* (1 day). IEEE SSCS Distinguished Lecture in Taipei, Taiwan, Jun. 9, 2006.
18. "Powering Micro-Syst.," IEEE Circuits and Syst. Soc., Montreal, Canada, Jul. 17, 2009.
19. "Harvesting Kinetic Energy in Miniaturized Syst.," IEEE Circuits and Syst. Soc., Montreal, Canada, Sept. 17, 2010.
20. "AC Des. & Performance of Low-Dropout Regulators," IEEE Circuits and Syst. Soc., Univ. of Puerto Rico at Mayagüez, Nov. 12, 2010.
21. "AC Des. & Performance of Low Dropout Regulators," IEEE Circuits and Syst. Soc., Tainan, Taiwan, Nov. 26, 2010.
22. "Energizing & Powering Microsystems," MediaTek-NTU Distinguished Professor Talk, Nanyang Technological Univ., Singapore, Feb. 29, 2016.
23. "Energizing & Powering Microsystems," IEEE Circuits and Syst. Soc., Univ. of Puerto Rico at Mayagüez, Mar. 22, 2018.
24. "Energizing & Powering Microsystems," IEEE Circuits and Syst. Soc., Montevideo, Uruguay, Apr. 30, 2018.
25. "Energizing & Powering Microsystems," IEEE Circuits and Syst. Soc., Curitiba, Brazil, May 2, 2018.
26. "Energizing & Powering Microsystems," IEEE Circuits and Syst. Soc., Santiago, Chile, May 10, 2018.
27. "Energizing & Powering Microsystems," IEEE Circuits and Syst. Soc., Valparaíso, Chile, May 11, 2018.
28. "Energizing & Powering Microsystems," IEEE Circuits and Syst. Soc., Thessaloniki, Greece, Dec. 7, 2018.
29. "Energizing & Powering Microsystems," IEEE Circuits and Syst. Soc., Santa Clara, California, Feb. 28, 2019.
30. "Energizing & Powering Microsystems," IEEE Circuits and Syst. Soc., Lima, Peru, Mar. 21, 2019.
31. "Energizing & Powering Microsystems," IEEE Circuits and Syst. Soc., Ajou Univ., Seoul, Korea, May 9, 2019.
32. "Energizing & Powering Microsystems," IEEE Circuits and Syst. Soc., Sogang Univ., Seoul, Korea, May 10, 2019.
33. "Unraveling Feedback Translations," IEEE Circuits and Syst. Soc., Philadelphia, Pennsylvania, June 9, 2022.
34. "Switched-Inductor Power Supplies: Compact Control Loops," IEEE Circuits and Syst. Soc., Hsinchu, Taiwan, Dec. 16, 2022.
35. "Compact Control Loops for Switched-Inductor Power Supplies," IEEE Circuits and Syst. Soc., Sogang Univ., Seoul, Korea, May 17, 2023.
36. "Compact Control Loops," IEEE Circuits and Syst. Soc., Korea Advanced Inst. of Science and Tech., Daejeon, Korea, May 18, 2023.
37. "Compact Control Loops," IEEE Circuits and Syst. Soc., Valparaiso, Chile, June 16, 2023.
38. "Compact Control Loops," IEEE Circuits and Syst. Soc., Tainan, Taiwan, June 26, 2024.
39. "Compact Control Loops," IEEE Circuits and Syst. Soc., Taichung, Taiwan, June 27, 2024.

F3. Invited Professional Courses (sole instructor)

40. *Integrated DC–DC Converters* (1 day). RF Micro-Devices in Greensboro, NC, Apr. 2002.
41. *Power Management ICs* (3 days). Hong Kong Science & Tech. Park, Hong Kong, China, Aug. 2003.
42. *CMOS Analog Integrated Circuits* (5 days). Global Learning & Conf. Center, Atlanta, GA, Sept. 26–30, 2005.
43. *Power Supply ICs for RF PAs* (1 hour). Invited IEEE Expert Now Module, Fall 2005.
44. *Power Management ICs* (4 days). ON Semiconductor in Bratislava, Slovakia, Dec. 12–15, 2005.
45. *Power Management ICs* (4 days). ON Semiconductor in Toulouse, France, Dec. 19–22, 2005.
46. *Power Management ICs* (4 days). Space & Naval Warfare Syst. Command, San Diego, CA, Apr. 10–13, 2006.
47. *Power Management ICs* (4 days). Cypress Semiconductor in Colorado Springs, CO, Apr. 17–20, 2006.

48. *Power Management ICs* (4 days). Toko Inc. in Saitama, Japan, Dec. 11–14, 2006.
49. *Power Management ICs* (3 days). Intel Corp. in Hillsboro, OR, May 9–11, 2007.
50. *Power Management ICs* (4 days). Spyro Tech. in Singapore, May 21–24, 2007.
51. *Analog IC Des.* (4 days). Integrated Device Tech. in Duluth, GA, Jan. 26, Feb. 23, and Mar. 9 and 23, 2009.
52. *Designing Bandgap Voltage References* (2 days). Nat. Cheng-Kung Univ. in Tainan, Taiwan, Dec. 5–6, 2012.
53. *Stabilizing Complex Single-Inductor DC–DC Power Supplies* (2 days), Nat. Cheng-Kung Univ., Tainan IEEE Solid-State Circuits and Circuits and Syst. Soc. Chapters in Tainan, Taiwan, Nov. 17 and 20, 2015.
54. *Linear Regulators* (3 days). Dialog Semiconductor in Swindon, United Kingdom, Dec. 1–3, 2015.
55. *Designing Photovoltaic-Sourced Charger-Supply Microsystems* (2 days). Nat. Cheng-Kung Univ., Tainan IEEE Solid-State Circuits and Circuits and Syst. Soc. Chapters in Tainan, Taiwan, Nov. 17–18, 2016.
56. *Des. Insight and Intuition of Negative Feedback at the Transistor Level* (2 days). Nat. Cheng-Kung Univ., Tainan IEEE Solid-State Circuits and Circuits and Syst. Soc. Chapters in Tainan, Taiwan, Nov. 18 and 21, 2016.
57. *Switched Power Supplies & Energy Harvesters* (4 days). Dialog Semiconductor in Santa Clara, CA, Mar. 20–23, 2017.
58. *Power IC Design* (3 days). Dolphin Design in Montreal, Canada, May 7–9, 2024.

F4. Conference Seminars and Tutorials (sole presenter)

59. "Self-Oscillating Hysteretic DC–DC Controllers," IEEE Power Electronics Specialists Conf., Vancouver, Canada, 2001.
60. "Integrated LDOs: From the Ground Up!" IEEE Int. Symp. on Circuits and Syst., Scottsdale, AZ, 2002.
61. "Integrated DC–DC Converters: A Topological Journey!" IEEE Midwest Symp. on Circuits and Syst., Tulsa, OK, 2002.
62. "Dynamically Adaptive Power Supply Circuits for PA Wireless Appl.," IEEE Int. Microwave Symp., Long Beach, CA 2005.
63. "Hybrid Fuel Cell/Lithium-Ion Powered, Power Conscious SiP ICs," 1st Int. Workshop on 3S - SOP, SIP, SOC Electronic Technologies, Atlanta, GA, 2005.
64. "AC Des. and Performance of Low-Dropout Regulators (LDOs)," IEEE European Conf. on Circuit Theory and Des., Seville, Spain, 2007.
65. "Powering Micro-Syst. with Fuel-Cell Hybrids," 10th Annual Int. Conf. on Small Fuel Cells, Atlanta, GA 2008.
66. "Low-Dropout Regulator (LDO) ICs," IEEE Int. NEWCAS–TAISA Conf., Montreal, Canada, 2008.
67. "Powering Microsystems," CMOS Emerging Technologies Workshop, Vancouver, Canada, 2009.
68. "Switching DC-DC Supplies and their Single-Inductor, Multiple-Output (SIMO) Derivatives," IEEE Int. Symp. on Circuits and Syst., Paris, France, 2010.
69. "Energy-Harvesting Switching Converter ICs," Int. Workshop on Power Supply On Chip, Cork, Ireland, 2010.
70. "Energyizing and Powering Microsystems," The Materials Res. Soc. Fall Meeting, Boston, MA, 2010.
71. "Power-Management Syst. on Chip for Mobile Appl.," IEEE Int. Conf. on Microelectronics, Cairo, Egypt, 2010.
72. "Power-Supply Circuits and Syst. for Battery-Powered Devices," IEEE Very Large-Scale Integration Des., Automation and Test, Hsinchu, Taiwan, 2011.
73. "Energy-Harvesting ICs," IEEE European Solid-State Circuits Conf., Helsinki, Finland, 2011.
74. "Energyizing and Powering Microsystems," IEEE Faible Tension Faible Consommation, Paris, France, 2012.
75. "Energy-Harvesting Integrated Circuits," IEEE Int. NEWCAS Conf., Montreal, Canada, 2012.
76. "Harvesting ICs," CMOS Emerging Technologies Workshop, Vancouver, Canada, 2012.
77. "Energy-Harnessing Integrated Circuits," Seminario de Nanoelectrónica y Diseño Avanzado, Departamento de Electrónica del Instituto Nacional de Astrofísica, Óptica y Electrónica, Puebla, México, 2012.
78. "Energyizing and Powering Microsystems," SHPE Nat. Conf., Ft. Worth, Texas, 2012.
79. "Feedback Control of Switched-Inductor Supplies," IEEE Asia Pacific Conf. on Circuits and Syst., Kaohsiung, Taiwan, 2012.
80. "Designing Bandgap-Voltage References," IEEE Int. Symp. on Circuits and Syst., Beijing, China, 2013.
81. "Energy-Harvesting Integrated Circuits," IEEE Int. Symp. on Ind. Electronics, Taipei, Taiwan, 2013.
82. "Single-Inductor Multiple-Output Power-Supply ICs," IEEE Int. NEWCAS Conf., Paris, France, 2013.
83. "Designing Bandgap-Voltage References," IEEE Faible Tension Faible Consommation, Paris, France, 2013.

84. "Power Electronic Interfaces for Energy Harvesters," PowerMEMS 2013, London, England, 2013.
85. "Energizing Wireless Microsensors," Int. Forum on Green Energy Electronics, Nat. Taiwan Univ. of Science and Tech., Taipei, Taiwan, July 28, 2014.
86. "Energizing Wireless Microsensors," Int. Forum on Green Energy Electronics, Nat. Cheng Kung Univ., Tainan, Taiwan, July 29, 2014.
87. "Tiny DC-Sourced Single-Inductor Charger-Supply ICs," IEEE Midwest Symp. on Circuits and Syst., College Station, TX, 2014.
88. "Miniaturized Energy-Harvesting Piezoelectric Chargers," IEEE Custom Integrated Circuits Conf., San Jose, CA, 2014.
89. "Miniaturized Energy-Harvesting Piezoelectric Chargers," IEEE Int. Symp. on Integrated Circuits, Singapore, 2014.
90. "Powering Microsystems," IEEE Int. Symp. on Quality Electronic Des., Santa Clara, CA, 2015.
91. "Energy-Harvesting Microsystems," IEEE Very Large-Scale Integration Des., Automation and Test, Hsinchu, Taiwan, 2015.
92. "Miniaturized Energy-Harvesting Piezoelectric Chargers," IEEE Int. Symp. on Circuits and Syst., Lisbon, Portugal, 2015.
93. "Miniaturized Energy-Harvesting Piezoelectric Chargers," IEEE Int. Symp. on Ind. Electronics, Rio de Janeiro, Brazil, 2015.
94. "Tiny DC-Sourced Single-Inductor Charger-Supply ICs," IEEE Int. Syst.-on-Chip Conf., Beijing, China, 2015.
95. "Tiny Inductively Powered Battery Chargers," IEEE Int. NEWCAS Conf., Vancouver, Canada, 2016.
96. "Tiny Inductively Powered Battery Chargers," IEEE Asia Pacific Conf. on Circuits and Syst., Jeju, South Korea, 2016.
97. "Low-Dropout Regulator ICs," IEEE Int. Conf. on Electronics Circuits and Syst., Monte Carlo, Monaco, 2016.
98. "Tiny Energy-Harvesting Piezoelectric Chargers," IEEE Int. Conf. on Ind. Tech., Toronto, Canada, 2017.
99. "Light-Harvesting Photovoltaic Charger-Supplies," IEEE Canadian Conf. on Electrical and Computer Eng., Windsor, Canada, 2017.
100. "Tiny Inductively Powered Battery Chargers," IEEE Int. Symp. on Ind. Electronics, Edinburgh, Scotland, 2017.
101. "Tiny Light-Harvesting Photovoltaic Charger-Supplies," IEEE/ACM Int. Symp. on Low Power Electronics and Des., Taipei, Taiwan, 2017.
102. "Energizing and Powering Intelligent Microsensors," IEEE/IEIE Int. Conf. On Consumer Electronics, Jeju, Korea, 2018.
103. "Tiny Inductively Powered Battery Chargers," IEEE Midwest Symp. on Circuits and Syst., Windsor, Canada, 2018.
104. "Energizing & Powering Wireless Biomedical Microsystems," Energy and Public Health Workshop, Atlanta, 2021.
105. "How to Stabilize and Control DC-DC Switched-Inductor Power Supplies," IEEE Int. Symp. on Circuits and Syst., Austin, Texas, 2022.
106. "Tiny Energy-Harvesting Piezoelectric Chargers," IEEE Int. Conf. on Electronic Circuits and Syst., Glasgow, United Kingdom, 2022.
107. "Compact Control Loops for Switched-Inductor DC-DC Power Supplies," IEEE Applied Power Electronics Conference & Exposition, Long Beach, California, 2024.
108. "Compact Control Loops for Switched-Inductor IoT Power Supplies," IEEE Latin America Conf. on Internet of Things, Fortaleza, Brazil, 2025.
109. "Energizing & Powering Intelligent Biomedical Microsystems," IEEE Int. NEWCAS Conf., Paris, France, 2025.
110. "The Ins & Outs of PWM Switched-Inductor Power Supplies," Invited, IEEE Midwest Symp. on Circuits and Syst., Lansing, Michigan, 2025.
111. "Designing compact SoC PWM switched-inductor power supplies," IEEE Asia Pacific Conf. on Circuits and Syst., Busan, South Korea, 2025.

F5. Invited Research Seminars (sole presenter)

112. "Integrated Power Management Circuits." Nat. Semiconductor Corp., Santa Clara, CA, Dec. 2002.
113. "Power Conscious ICs." Texas A&M Univ., College Station, TX, June 21, 2004.
114. "El Mundo es Análogo, y las Oportunidades son Muchas." Univ. of Puerto Rico at Mayagüez, Oct. 18, 2004.
115. "Hybrid Fuel Cell/Lithium-Ion Powered, Power Conscious ICs." Nat. Semiconductor Corp., Santa Clara, CA, June 2005.
116. "Microsystems: Power and Energy." Army Res. Lab Advanced Microsystems Workshop, Langley, VA, Jan. 30, 2006.

117. "Self-Sustaining, Self-Powered, Energy and Power Conscious ICs for Micro-Scale Devices," Universitat Politècnica de Catalunya, Barcelona, Spain, Jul. 10, 2006.
118. "Self-Powered, Self-Sustaining Syst.-on-Chip and Syst.-in-Package Power Solutions," Nat. Science Foundation and Intelligence Community Workshop on Micro-Scale Power Sources, Langley, VA, Apr. 24-25, 2007.
119. "Powering Micro-Syst.," Nat. Semiconductor Corp., Santa Clara, CA, Nov. 30, 2007.
120. "AC Des. and Performance of Low-Dropout Regulators," Texas A&M Univ. at College Station, TX, Jun. 9, 2008.
121. "Power Losses in Switching DC-DC Converter ICs," Texas A&M Univ. at College Station, TX, Jun. 9, 2008.
122. "Powering Micro-Syst.," Shanghai Jiao Tong Univ., Shanghai, China, Oct. 8, 2008.
123. "Powering Micro-Syst.," Linear Tech. Corp., San Jose, CA, Feb. 13, 2009.
124. "Energizing and Powering Microsystems," *IEEE Electron Device Soc. Chapter*, Vancouver, Canada, Sept. 24, 2009.
125. "Harvesting Ambient Energy in Miniaturized Syst.," *Energy and Power Analog Circuit Challenges Workshop*, Semiconductor Res. Corp. Texas Analog Center of Excellence, Dallas, TX, Sept. 28, 2009.
126. "Single-Inductor Multiple-Output Switching DC-DC Converters," Inha Univ., Incheon, Korea, Nov. 19, 2009.
127. "Single-Inductor Multiple-Output Switching DC-DC Converters," Samsung, Seoul, Korea, Nov. 20, 2009.
128. "Power Management ICs for Portable Devices," Univ. of Seoul, Korea, Feb. 17, 2010.
129. "Energizing & Powering Microsystems," Electronics & Telecommunications Res. Inst., Daejeon, Korea, Feb. 18, 2010.
130. "Power Management ICs for Portable Devices," Silicon Works Co., Daejeon, Korea, Feb. 18, 2010.
131. "Energizing & Powering Microsystems," Korea Advanced Inst. of Science and Tech., Feb. 19, 2010.
132. "Harvesting Energy in Miniaturized Syst.," *IT Convergence Res. Project Workshop*, Korea Advanced Inst. of Science and Tech., Korea, Feb. 19, 2010.
133. "Energizing & Powering Microsystems," Texas Instruments, Dallas, TX, Oct. 25, 2010.
134. "Harvesting Kinetic Energy in Miniaturized Syst.," Nat. Taiwan Univ., Taipei, Taiwan, Nov. 23, 2010.
135. "Energizing & Powering Microsystems," IEEE SSCS Hsinchu Chapter, Taiwan, Nov. 24, 2010.
136. "Power Losses in Switching DC-DC Converter ICs," Nat. Cheng Kung Univ., Tainan, Taiwan, Nov. 26, 2010.
137. "Energy-Harnessing ICs," Nat. Semiconductor Corp., Santa Clara, CA, July 15, 2011.
138. "AC Des. & Performance of LDOs," IEEE CASS Taipei Chapter, Hsinchu, Taiwan, Sept. 5, 2011.
139. "Energy-Harnessing ICs," ON Semiconductor, Phoenix, AZ, Oct. 4, 2011.
140. "Energy-Harnessing ICs," Texas Instruments, Dallas, TX, Oct. 5, 2011.
141. "Energy-Harnessing ICs," IEEE Ind. Electronics, Power Electronics, & Industry Appl. Soc., Nat. Tsing Hua Univ., Hsinchu, Taiwan, Dec. 9, 2011.
142. "Frequency Response of Switching DC-DC Converters," Nat. Cheng Kung Univ., Tainan, Taiwan, Dec. 14, 2011.
143. "Feedback Control of Switching DC-DC Converters," Nat. Cheng Kung Univ., Tainan, Taiwan, Dec. 14, 2011.
144. "Harvesting Kinetic Energy in Miniaturized Syst.," Space and Naval Warfare Syst. Command, San Diego, CA, Mar. 2, 2012.
145. "Energy-Harnessing Microchips," IEEE Power Electronics Soc. & IEEE Life Members, Atlanta, GA, Mar. 28, 2012.
146. "Harvesting Kinetic Energy in Miniaturized Syst.," Texas Instruments, Dallas, TX, Jun. 5, 2012.
147. "Harnessing Ambient Energy with Integrated Circuits," Nat. Cheng Kung Univ., Tainan, Taiwan, Dec. 6, 2012.
148. "Des. of High-Performance Low-Dropout Regulator ICs," Nat. Cheng Kung Univ. & IEEE Circuits and Syst. Soc., Tainan, Taiwan, Dec. 17, 2013.
149. "Designing Tiny DC-Sourced Single-Inductor Charger-Supply ICs," Nat. Cheng Kung Univ., IEEE Circuits and Syst. Soc., & IEEE Solid-State Circuits Soc., Tainan, Taiwan, Dec. 18, 2013.
150. "Designing Tiny Energy-Harvesting Piezoelectric Chargers," IEEE Circuits and Syst. Soc., Nat. Sun Yat-Sen Univ., Kaohsiung, Taiwan, Dec. 19, 2013.
151. "Designing Tiny Energy-Harvesting Piezoelectric Chargers," IEEE Ind. Electronics Soc. & IEEE Power Electronics Soc., Nat. Taiwan Univ. of Science and Tech., Taipei, Taiwan, Dec. 20, 2013.
152. "Energizing & Powering Microsystems," Texas Instruments, Dallas, TX, May 8, 2014.
153. "Power-Supply Rejection in Amplifiers and LDOs," Nat. Cheng Kung Univ., Tainan, Taiwan, Nov. 19, 2014.

154. "Miniaturized Energy-Harvesting Piezoelectric Chargers," Nat. Cheng Kung Univ., Tainan, Taiwan, Nov. 20, 2014.
155. "Powering Microsensors," Department of Energy, Nat. Security Campus, Kansas City, Feb. 6, 2015.
156. "Tiny and Distant Inductively-Powered Battery Chargers," Nat. Cheng Kung Univ., Tainan IEEE Solid-State Circuits & Circuits and Syst. Soc., Tainan, Taiwan, Nov. 19, 2015.
157. "Energizing & Powering Microsystems," Department of Energy, Nat. Security Campus, Albuquerque, NM, Jan. 7, 2016.
158. "Energizing & Powering Microsystems," Khalifa Univ., Abu Dhabi, UAE, May 2, 2016.
159. "Energizing & Powering Microsystems," Texas Instruments, Dallas, TX, June 10, 2016.
160. "Energizing & Powering Microsystems," KTH Royal Inst. of Tech., Stockholm, Sweden, June 9, 2017.
161. "Energizing & Powering Microsystems," Nat. Cheng Kung Univ., IEEE Circuits and Syst. Soc., Tainan, Taiwan, Nov. 16, 2017.
162. "On-Chip Bias Currents," Nat. Cheng Kung Univ., IEEE Circuits and Syst. Soc., Tainan, Taiwan, Nov. 20, 2017.
163. "On-Chip Voltage References," Nat. Cheng Kung Univ., IEEE Circuits and Syst. Soc., Tainan, Taiwan, Nov. 21, 2017.
164. "Powering Microsystems," Analog Devices, Phoenix, AZ, Mar. 9, 2018.
165. "Energizing & Powering Wireless Microsensors," Texas A&M Univ., College Station, TX, Mar. 19, 2018.
166. "Energizing & Powering Microsystems," Texas Instruments, Dallas, TX, Jun. 15, 2018.
167. "Energizing & Powering Microsystems," Analog Devices, Milpitas, CA, Nov. 9, 2018.
168. "Energizing & Powering Microsystems," Univ. of Illinois at Urbana–Champaign, IL, Nov. 26, 2018.
169. "Powering Wireless Microsystems," Texas A&M Univ., College Station, TX, Nov. 20, 2020.
170. "Powering Wireless Microsystems," IEEE Power Electronics Chapter, Atlanta, GA, Dec. 4, 2020.
171. "Energizing & Powering Wireless/Portable Microelectronics," Analog Devices, Colorado Springs, CO, July 14, 2022.
172. "1-W Battery-Charging CMOS Buck Regulator," SRC/TxACE Seminar, Dallas, TX, July 22, 2022.
173. "Analog, Power, Energy ICs: Powering Microelectronic Systems," Analog Devices, Santa Clara, CA, June 27, 2023.
174. "Analog, Power, Energy ICs: Powering Microelectronic Systems," Texas Instruments, Dallas, TX, July 18, 2023.
175. "Powering Microelectronic Systems," Texas Instruments, Santa Clara, CA, Mar. 22, 2024.
176. "Multiple-I/O Switched-Inductor Battery-Charging Regulators," SRC/TxACE Seminar, Dallas, TX, June 21, 2024.
177. "On Power IC Research at Georgia Tech," Analog Devices, Santa Clara, CA, July 10, 2024.
178. "The ins & outs of PWM loops in switched-inductor power supplies," Texas Instruments, Dallas, TX, Mar. 20, 2025.
179. "The ins & outs of PWM loops in switched-inductor power supplies," Analog Devices, Santa Clara, CA, July 18, 2025.

F6. Panelist

180. "Power Management for SoCs," IEEE VLSI Symp., Hawaii, Jun. 15–17, 2006.
181. "Non-Academic Routes Beyond the Eng. Ph.D.," Sloan Foundation, Atlanta, GA, Apr. 14, 2015.
182. "Research Funding: A View from the Other Side," Univ. Center of Exemplary Mentoring, Atlanta, GA, Feb. 13, 2018.
183. "Flexible Career Navigation/Agility," Sloan Foundation, Atlanta, GA, Mar. 2022.

F7. Artistic Performances (with audition)

184. Tenor in *A Festival of Nine Lessons and Carols* (Concert). St. Rita's Contemporary Choir, Dallas, TX, Dec. 2000.
185. Tenor in *The American Folk Spirit: A Concert of Folk Songs, Hymns, and Spirituals* (Concert). Alpharetta Chamber Singers, Alpharetta, GA, Nov. 2001.
186. Tenor in *Refresh of 2002: Five Services of Preaching and Proclamation* (Concert). Alpharetta Chamber Singers, Alpharetta, GA, Jan. 2002.
187. Zebulon in *Joseph and the Amazing Technicolor Dreamcoat* (Musical). Christ the King Theatre Ministry, Atlanta, GA, Apr. 2002.
188. Bernardo in *West Side Story* (Musical). Cobb Playhouse/Little General, Marietta, GA, Sept. 2002.
189. Tenor in *Mass of Remembrance* (Concert). Holy Spirit's Traditional Choir, Atlanta, GA, Nov. 2002.
190. Tenor in *Expressions of Praise* (Concert). Alpharetta Chamber Singers, Alpharetta, GA, Nov. 2002.
191. Homer Smith in *Lilies of the Field* (Play). Gwinnett County Seat Players, Lawrenceville, GA, Feb. 2003.
192. Tenor in *Mass of a New Millennium* (Concert). Alpharetta Chamber Singers, Alpharetta, GA, Apr. 2003.

193. Soldier in the *Sound of Music* (Musical). Christ the King Theatre Ministry, Atlanta, GA, Apr. 2003.
194. Tenor in *Sing a Song of Shakespeare* (Concert). Alpharetta Chamber Singers, Alpharetta, GA, May 2003.
195. Tenor in *Magnificat* (Concert). Archdiocesan Festival Choir, Atlanta, GA, May 2003.
196. Sid Philips in *Singin' in the Rain* (Musical). Marietta First Methodist Church, Marietta, GA, Jul. 2003.
197. Jimmy Luv in *Snuff Darlings from Dahlonaga* (Play). Art Farm, Cabbage Town, GA, Aug. 2003.
198. Henry Steward in *Civil War* (Musical). Neighborhood Playhouse, Decatur, GA, Sept. 2003.
199. Tenor in *Wonder Tidings* (Concert). Alpharetta Chamber Singers, Alpharetta, GA, Dec. 2003.
200. Fred Casely in *Chicago* (Musical). Theatre Arts Guild, Clarkston, GA, Feb. 2004.
201. Soldier, Guard, and Servant in *Life is a Dream/La Vida es Sueño* (Play: Performed in English and Spanish), Aurora Theatre, Duluth, GA, Apr. 2004.
202. Paco in *Man of La Mancha* (Musical). Corpus Christi Theatre Ministry, Stone Mountain, GA, Oct. 2004.
203. Tommy Keeler in *Annie Get Your Gun* (Musical). ACT 1, Alpharetta, GA Nov.–Dec. 2004.
204. Martinez in *Take Me Out* (Play). Theatre in the Square, Marietta, GA, Mar.–Apr. 2005.
205. Ali Hakim in *Oklahoma* (Musical). Christ the King Theatre Ministry, Atlanta, GA, Mar.–Apr. 2006.
206. Boxhall in *Titanic* (Musical). Stage 2 Players, Roswell, GA, Oct. 2006.
207. Roberto Nuñez & Charlie Blossom in *Working* (Musical). Theatre Arts Guild, Clarkston, GA, Oct.–Nov. 2006.
208. Tenor in St. Rita's Contemporary Choir, Dallas, TX, 1999–2001.
209. Tenor in Alpharetta Chamber Singers, Alpharetta, GA, 2001–2004.
210. Tenor in Holy Spirit's Traditional Choir, Atlanta, GA, 2001–2004.
211. Tenor in Archdiocesan Festival Choir, Atlanta, GA, 2002–2004.
212. Tenor in Sacred Heart's Spanish Choir, Atlanta, GA, 2004–2005.

IV. Education

A. Courses Developed

A1. University Courses (at Georgia Tech)

1. ECE 3400 Analog Electronics (re-developed)
2. ECE 4803 Energy/Power Microelectronics
3. ECE 6412 Analog Integrated Circuit Design (re-developed)
4. ECE 6445 Power IC Design

A2. Professional Courses

5. Power Management ICs
6. CMOS Analog Integrated Circuits
7. Bandgap Voltage References
8. Linear Regulators
9. Energy Harvesters

B. Courses Taught (at Georgia Tech)

1. ECE 3040 Microelectronic Circuits: Sp. '02, '03.
2. ECE 3050/3400 Analog Electronics: Fa. '01, '02, '03, '04, '05, '06, '07, '10, Sp. '05, '06, '20, '21, '22, '23, '24, '25.
3. ECE 4430 Analog Integrated Circuits: Fa. '02, '03.
4. ECE 4803 Energy/Power Microelectronics: Su. '18.
5. ECE 6412 Analog Integrated Circuit Design: Sp. '04, '07, '08, '09, '10, '11, '12, '13, '14, '15, '16, '17, '18, '19, '20, '21, '22, '23, '24, Su '25; at GT-Shanghai in Fa. '08, '11, '13; & GT-Shenzhen in Fa. '14, '16, '18.
6. ECE 6445 Power IC Design: Su. '15, Fa. '22, '24, '25; at GT-Shanghai in Fa. '09, '12; & at GT-Shenzhen in Fa. '15, '17, '19, '21, '23.

C. Individual Student Guidance (at Georgia Tech)

C1. Ph.D. Students

1. Biranchi Sahu, *Dynamically Adaptive Supplies for Linear RF Power Amplifiers*, Ph.D. Dec. 2004.

2. Pooya Forghani, *Lossless Current-Sensor IC for Switching DC-DC Converters*, Ph.D. Jun. 1, 2006.
3. Vishal Gupta, *An Accurate, Trimless, High PSRR, Low-Voltage, CMOS Reference IC*, Ph.D. Jul. 3, 2007.
4. Neeraj Keskar, *High-Bandwidth, Wide LC- R_{ESR} Compliant $\Sigma\Delta$ Boost DC-DC Converters*, Ph.D., Mar. 24, 2008.
5. Erick O. Torres, *An Electrostatic CMOS/BiCMOS Vibration-Based Harvester-Charger IC*, Ph.D., May 4, 2010.
6. Dongwon Kwon, *Piezoelectric Kinetic Energy-Harvesting ICs*, Ph.D., Mar. 4, 2013.
7. Orlando Lazaro, *CMOS Inductively Coupled Power Receiver for Wireless Microsensors*, Ph.D., Apr. 2, 2014.
8. Suhwan Kim, *Mixed-Source Charger-Supply CMOS IC*, Ph.D., Apr. 14, 2014.
9. Andrés Arturo Blanco, *Fast-Waking & Low-Voltage Thermoelectric & Photovoltaic CMOS Chargers for Energy-Harvesting Wireless Microsensors*, Ph.D., Jul. 14, 2017.
10. Rajiv Damodaran Prabha, *Light-Harvesting Photovoltaic Charger-Supply Microsystems*, Ph.D., Dec. 7, 2017.
11. Carlos Javier Solís, *Battery-Sourced Switched-Inductor Multiple-Output CMOS Power-Supply Syst.*, Ph.D., Apr. 3, 2018.
12. Nan Xing, *Inductively Coupled Charger*, Ph.D., Apr. 3, 2020.
13. Siyu Yang, *Piezoelectric Energy-Harvesting Chargers*, Ph.D., Oct. 21, 2020.
14. Tianyu Chang, *Thermoelectric Battery-Charging Voltage Regulators for IoT Microsensors*. Ph.D., Sep. 23, 2022.
15. Guillaume Guérin, *Energy-Saving Switched-Inductor Power Supply*. Start: Su. '19, Pre. Exam: Sp. '18, Proposal: Su. '21.
16. Xi Li, *Thermoelectric MPP-Tracking Battery-Charging Voltage Regulator*. Start: Sp. '22.
17. Linyuan Cui, *Battery-Charging Voltage Regulator*. Start: Su. '22.
18. Tiancheng Zhao, *Energy-Harvesting Self-Starting Piezoelectric Power Supply*. Start: Su '24.
19. Devangna Dubei, *Compact Switched-Inductor Power Supplies*. Start: Sp '25.
20. Aaryan Tiwary, *Harmonic Oscillations in Switched-Inductor Power Supplies*. Start: Sp '25.

C2. M.S. Students

21. Mark Guildersleeve, *Low Voltage Power Saving Techniques for DC-DC Converters*, M.S.E.E., Aug. 2002.
22. Abbas Poonawala, *Precision, Low-Voltage, Integrated Capacitor Multipliers*, M.S.E.E., Dec. 2003.
23. Aditya Makharia, *Inductorless DC-DC Converters for Portable Appl.*, M.S.E.E., Dec. 2003.
24. Oscar Palomino, M.S.E.E., Dec. 2007.
25. Amisha Manek, M.S.E.E., Dec. 2008.
26. Justin Vogt, *nW Analog-Digital Converter for Blood-Glucose Monitors*, M.S.E.E., Dec. 2008.
27. Amit Patel, *"High PSR Low Dropout Regulator ICs,"* M.S.E.E., May 2009.
28. Priyanka Lakhe, M.S., May 2010.
29. Luke Milner, M.S., May 2010.
30. Tim Guglielmo, M.S., May 2011.
31. José Vidal, M.S., May 2011.
32. Joshua Cowan, M.S., Dec. 2013.
33. Jun-Yang Lei, M.S., Dec. 2014.
34. Amy Wilson, *Electrostatic Energy-Harvesting CMOS Charger ICs*, M.S., Dec. 2017.
35. Pengyu Gu, M.S., 2020.
36. Sowmya Sankaranarayanan, M.S. 2021.
37. Avinash Dinesh, M.S. 2021.
38. Vasu Gupta, *Dimming DC-DC LED Drivers: Power Losses, Luminous Efficiency & Best-in-Class*, M.S. 2021.
39. Qian Zhi, *Battery-Charging Voltage Regulator*, M.S. 2022.
40. Qiwei Chen, *Hybrid Buck-Boost Power Supplies*, M.S. 2023.
41. Utsav Vasudevan, *Design of Digital Low-Dropout Regulator*, M.S. '23-'24.
42. Michael Isaf, *Energy-Harvesting CMOS Power Supplies*, Fa '21-Sp '24.
43. Pratyush Manocha, *Switched-Mode Hybrid Power Supplies*. Su '23-Fa '24.
44. Deepankar Deshmukh, *Fast-Responding Low-Dropout Regulator*. Fa '24-Fa '25.

C3. B.S. Students

45. R. Dokania (Su. '02), *Cancellation of Load Regulation in Low Drop-out Regulators*.
 46. K. Dash (Su. '03), *Active Bulk Capacitor Multipliers*.
 47. Carlos Cubero Ponce (Su. '05), *Drain Follower Buffer*.
 48. Freddie Alequín Ramos (Su. '07), *Syst.-in-Package Integration*.
 49. LaVonda Brown (Su. '08), *Piezoelectric Modeling*.
 50. Adilson Cardoso (Fa. '06–Fa. '07).

C4. Visiting Scholars

51. H.I. Pan (from the Univ. of Taiwan: Jan. to Dec. 2005), *Asynchronous Power-Tracking Supplies for RF PAs*.

C5. Service on M.S. Thesis and Ph.D. Dissertation Committees:

C5a. Georgia Tech

	Student	Proposal Committee	Reading Committee	Defense Committee	Degree
1	Sidharth Dalmia	Chair: 3/14/02			Ph.D.
2	Zhiwei Dong		Member: 7/15/02	Member: 7/15/02	Ph.D.
3	Theocharis Boukas	Chair: 8/12/02		Member: 03/26/03	Ph.D.
4	Susanta Sengupta	Member: 4/15/02	Member: 07/08/04	Member: 07/08/04	Ph.D.
5	Kyu-won Choi	Chair: 10/29/02	Member: 09/09/03	Member: 09/09/03	Ph.D.
6	Woopoung Kim	Chair: 4/30/03			Ph.D.
7	Biranchinath Sahu	Advisor: 3/24/04	Chair: 11/4/04	Chair: 11/4/04	Ph.D.
8	Bhyrav Mutnury	Member: 1/28/05			Ph.D.
9	Pooya Forghani	Advisor: 6/24/04	Chair: 6/1/06	Chair: 6/1/06	Ph.D.
10	Vishal Gupta	Advisor: 9/20/05	Chair: 7/3/07	Chair: 7/3/07	Ph.D.
11	Neeraj Keskar	Advisor: 9/20/05	Chair: 3/24/08	Chair: 3/24/08	Ph.D.
12	Jau-Hong Chen	Chair: 9/22/05	Member: 5/25/06	Member: 06/30/06	Ph.D.
13	Soumendu Bhattacharya			Member: 06/23/05	Ph.D.
14	Jacob Minz	Member: 10/19/05		Member: 07/19/06	Ph.D.
15	Shruti Prakash	Member: 7/27/06	Member: 03/04/09	Member: 03/04/09	Ph.D.
16	Kenta Nakayashiki	Member: 9/28/06	Member: 10/2/07	Member: 10/2/07	Ph.D.
17	Ripal Nathuji	Member: 5/2/07			Ph.D.
18	Rajeswari Chandrasekaran	Member: 08/22/07		Member: 7/15/10	Ph.D.
19	David Pritchett	Member: 12/13/07		Member: 2/4/09	Ph.D.
20	N. Lalgudi Subramanian	Member: 1/17/07		Member: 3/26/08	Ph.D.
21	Erick Torres	Advisor: 4/9/08	Chair: 5/4/10	Chair: 5/4/10	Ph.D.
22	Krishna Bharath	Member: 4/21/08			Ph.D.
23	Muhammad Nisar	Member: 7/30/08			Ph.D.
24	Dale Scott Douglas		Member: Fall 08		M.S.
25	Tahir Zaidi	Member: 6/1/09			Ph.D.
26	Luke Milner	Advisor: 7/15/09			Ph.D.
27	Suhwan Kim	Advisor: 3/13/11	Chair: 4/14/14	Chair: 4/14/14	Ph.D.
28	Sang Taek Han	Member: 3/3/11			Ph.D.
29	Mauricio Pardo Gonzalez	Chair: 4/29/11	Member: 1/18/12	Member: 1/18/12	Ph.D.
30	Debrup Das	Member: 6/1/11			Ph.D.
31	Dongwon Kwon	Advisor: 7/21/11	Chair: 3/4/13	Chair: 3/4/13	Ph.D.
32	Hengzhao Yang	Member: 4/24/12		Member: 5/2/13	Ph.D.
33	Hakan Toreyin	Member: 11/25/12			Ph.D.
34	Orlando Lazaro	Advisor: 7/23/12	Chair: 4/2/14	Chair: 4/2/14	Ph.D.
35	Chris Valenta	Member: 7/25/12		Member: 6/25/14	Ph.D.
36	Jae Won Shim	Member: 3/13/13			Ph.D.
37	Yaesuk Jeong	Member: 7/24/14	Member: 5/11/17	Member: 5/11/17	Ph.D.
38	Andres Blanco	Advisor: 8/21/14	Chair: 7/14/17	Chair: 7/14/17	Ph.D.
39	Rajiv Damodaran	Advisor: 6/22/15	Chair: 12/7/17	Chair: 12/7/17	Ph.D.
40	Carlos Solís	Advisor: 1/27/16	Chair: 4/3/18	Chair: 4/3/18	Ph.D.
41	Jaemyum Lim	Chair: 3/16/16	Member: 3/16/17	Member: 3/16/17	Ph.D.
42	Nan Xing	Advisor: 4/16/18	Chair: 3/12/20	Chair: 4/3/20	Ph.D.

43	Siyu Yang	Advisor: 7/13/18	Chair: 8/21/20	Chair: 10/21/20	Ph.D.
44	Aniruddh Marellapudi		Member: 8/17/20		M.S.
45	Tianyu Chang	Advisor: 8/20/20	Chair: 8/26/22	Chair: 9/23/22	Ph.D.
46	Guillaume Guerin	Advisor: 7/27/21			Ph.D.
47	Vasu Gupta		Advisor: 12/9/21		M.S.
48	Blake Capella		Member: 6/6/22		M.S.
49	Qian Zhi		Advisor: 12/12/22		M.S.

C5b. International Ph.D. Committees

- [Rapporteur, Jury] Vincent Telandro, *On-Chip Voltage Regulator Protecting Against Power Analysis Attacks*, Laboratoire Matériaux et Microélectronique de Provence, Institut Supérieur d'Electronique du Nord, France, Nov. 2007.
- [External Examiner, Chair] Mohammad Radwan Alhawari, *Multi-Source Energy-Harvesting Interface Circuits for Biomedical Wearable Electronics*, Khalifa Univ., Abu Dhabi, United Arab Emirates, May 2, 2016.
- [Examiner, Opponent] Janko Katic, *Efficient Energy Harvesting Interfaces for Implantable Appl.*, KTH Royal Inst. of Tech., Stockholm, Sweden, June 9, 2017.
- [Member, Jury] Youssef Driouich, *Model Checking Cyber-Physical Systems*, Università degli Studi Di Salerno, Salerno, Italy, March 29, 2019.
- [Member, Jury] Kateryna Stoyka, *Methods and Algorithms for Behavioral Modeling of Ferrite Power Inductors*, Università degli Studi Di Salerno, Salerno, Italy, March 29, 2019.

V. Service

A. Professional Contributions

A1. Professional Leadership

- Chapter Vice-Chair, Atlanta's IEEE Solid-State Circuits and Circuits and Syst. Soc., 2003–2004.
- Chapter Chair, Atlanta's IEEE Solid-State Circuits and Circuits and Syst. Soc., 2004–2011.
- Technical Program Co-Chair, IEEE Midwest Symp. on Circuits and Syst., Puerto Rico, 2006.
- Technical Program Chair, Joint IEEE Midwest Symp. on Circuits and Syst./Int. NEWCAS Conf., Canada, 2007.
- Circuit Des. Vice Chair, IEEE Int. Caribbean Conf. on Devices, Circuits and Syst., Mexico, 2008.
- General Chair, Energy & Power IC Workshop, SRC Texas Analog Center of Excellence, Sept. 28-29, 2009.
- Special Session Co-Organizer, "Emerging Energy/Power ICs," IEEE Int. Symp. on Circuits and Syst., Brazil, May 2011.
- Technical Program Co-Chair, IEEE Int. Syst.-on-Chip Conf., Korea, Nov. 2011.
- Advisory Panel, IEEE Int. Conf. on Power Electronics and Energy Syst., India, 2012.
- Technical Program Committee, IEEE Faible Tension Faible Consommation, France, 2013–2014.
- Int. Advisory Board, IEEE Int. Conf. on Power Electronics and Drive Syst., Japan, 2013.
- General Co-Chair, IEEE Int. Syst.-on-Chip Conf., Korea, Nov. 2013.
- General Vice Chair, IEEE Int. Syst.-on-Chip Conf., Korea, Nov. 2014.
- Int. Steering Committee, Int. Future Energy Electronics Conf., Taiwan, Nov. 2015.
- Technical Program Chair, IEEE Int. Symp. on Circuits and Syst., Canada, May 2016.
- Int. Liaison, IEEE Conf. on Des. of Circuits and Integrated Syst., España, Nov. 2017.
- Technical Program Co-Chair, IEEE Int. Symp. on Circuits and Syst., Japan, May 2019.
- Technical Program Co-Chair, IEEE Int. Symp. on Circuits and Syst., S. Korea, May 2021.
- Conference Tutorial Co-Chair, IEEE Midwest Symp. on Circuits and Syst., Springfield, MA, Aug. 2024.

A2. Editorial Boards

- Guest Co-Editor, *Analog Integrated Circuits and Signal Processing J.*, Special Issue on Analog and RF, Aug. 2009.
- Associate Editor, *IEEE Trans. on Circuits and Syst. II*, 2007–2009 and 2010–2011.
- Associate Editor, *IEEE J. of Solid-State Circuits*, 2011.
- Editorial Board Member, *J. of Low-Power Electronics*, 2009–2018.
- Guest Editor, *IEEE Trans. on Circuits and Syst. II*, Special Issue on Energy Harvesting, Dec. 2011.

A3. Committee Memberships

30. Technical Program Committee, IEEE Southwest Symp. on Mixed-Signal Des., 2002.
31. Selection Committee Review Panel, NSF SBIR/STTR Committee on Power Management, Mar. 2003.
32. Selection Committee Review Panel, NSF SBIR/STTR Committee on Power Management, Sept. 2003.
33. Technical Committee, IEEE CASS Analog Signal Processing, since 2003.
34. Selection Committee Review Panel, NSF SBIR/STTR Committee on Signal Processing & IC Des., Oct. 2004.
35. Selection Committee Review Panel, NSF SBIR/STTR Committee on IC Des.: Testing, Aug. 2005.
36. Steering Committee, IEEE Midwest Symp. on Circuits and Syst., since 2006.
37. Selection Committee Review Panel, NSF SBIR/STTR Committee on IC Des. I, Feb. 2007.
38. Technical Committee, IEEE CASS Power and Energy Circuits and Syst., since 2009.
39. Fellows Evaluation Committee, IEEE Circuits and Syst. Soc., 2011 and 2014.
40. Distinguished Lecturer Committee, IEEE Circuits and Syst. Soc., 2011–2013.
41. Ind. Advisory Committee, IEEE Circuits and Syst. Soc., 2012–2013, 2014–2015.
42. Steering Committee, IEEE Int. Symp. on Circuits and Syst., 2016–2017.
43. IEEE Awards Committee, IEEE Atlanta Section, 2025.

A4. Professional Memberships

44. Inst. of Electrical and Electronics Engineers: Student '90, Member '97, Sr. Member '01, Fellow '11.
45. Institution of Eng. and Tech.: Member '06, Fellow '09.
46. Soc. of Hispanic Professional Engineers: Life Member '00.
47. American Association for the Advancement of Science: Member '21.

B. Community Service

1. Volunteers for Peace (VFP), Kigonigoni, Tanzania (school and levy construction), Su. '06.
2. Volunteers for Peace (VFP), Bangalore, India (taught children with AIDS and disabilities), Su. '08.
3. Service Civil Int. (SCI), Ulaan Baatar, Mongolia (construction and farming at orphanage), Su. '09.
4. Service Civil Int. (SCI) in Viet Tri, Vietnam (taught orphans English), Su. '10.

C. Institute Contributions (at Georgia Tech)

1. Adjunct Professor, School of Electrical & Comp. Eng. (ECE), 1999–2001: Advised M.S./Ph.D. students.
2. Director, Georgia Tech Analog Consortium (GTAC), 2001–2004: Membership funds from Analog Devices, Raytheon, RF Micro-Devices, Intersil, Schlumberger, Adtran, Texas Instruments, and ON Semiconductor used to support over 50 M.S./Ph.D. students for EDA faculty.
3. Director, TI Analog Fellowship Program, 2001–2015: Supported over 100 M.S./Ph.D. students for EDA faculty.
4. Chair, GT's IEEE SSCS–CASS Chapter, 2004–2011: Invited/coordinated/hosted IEEE Distinguished Lecturers.
5. Chair, ECE Student–Faculty Committee, 2008–2011.
6. On-site Professor, GT-Shanghai/Shenzhen, 2008–2023: Teach ECE 6412 & ECE 6445 for GT in China.
7. Chair, Electronic Design & Applications (EDA) Faculty, 2013–2016, 2022–Present.
8. Faculty Recruitment Representative, Electronic Design & Applications (EDA) Faculty, 2016–2017.
9. Graduate Student Recruitment Representative, Electronic Design & Applications (EDA) Faculty, 2017–2022.
10. College of Engineering's (CoE) Diversity, Equity, and Inclusion (DEI) Council, 2021–2023.
11. College of Engineering's (CoE) External Relations Sub-Committee, 2021–2023.
12. Committee Membership:
 - ECE Graduate Student Recruitment Committee, 2001–2003, 2004–2005.
 - ECE Student–Faculty Committee, 2003–2012, 2021–2022.
 - ECE Student Award Committee, 2006.
 - ECE Georgia Power Distinguished Professor Search Committee, 2006.
 - Freshmen Partner for Freshmen Partnership Program, 2006.
 - ECE Student Awards Committee, 2009–2011.
 - ECE Course Content Review Panel for ECE 3040, 2015.

ECE Chair Search Committee, 2021.
ECE Ed and Pat Joy Chair Search Committee, 2023.

VI. Honors and Awards

A. Distinctions

1. One of "The 100 Most Influential Hispanics," *Hispanic Business*, 2000.
2. Sr. Member, Inst. of Electrical and Electronics Engineers, 2001.
3. Fellow, Institution of Eng. and Tech., 2009. (Less than 5% of members earn rank of Fellow.)
4. Distinguished Lecturer, IEEE Circuits and Syst. Soc., 2009–2010.
5. Fellow "for contributions to energy and power integrated circuit design," Inst. of Electrical and Electronics Engineers, 2011. (Less than 1% of members earn rank of Fellow.)
6. Included in "List of Notable Venezuelan Americans" in *Science*, 2014.
7. Fellow, National Academy of Inventors, 2017.
8. Distinguished Lecturer, IEEE Circuits and Syst. Soc., 2018–2019.
9. Distinguished Lecturer, IEEE Circuits and Syst. Soc., 2022–2023.
10. Motorola Solutions Foundation Professor, Georgia Inst. of Tech., 2022.
11. Fellow, American Association for the Advancement of Science, 2025.
12. Distinguished Lecturer, IEEE Circuits and Syst. Soc., 2026–2027.

B. Awards

13. Three-Year Patent Award for U.S. 5491437, U.S. 5500625, & U.S. 5519341, Texas Instruments, 1999.
14. Charles E. Perry Visionary Award, Florida Int. Univ., 2000.
15. Council of Outstanding Young Eng. Alumni Inductee "for outstanding achievements," Georgia Inst. of Tech., 2000.
16. Hispanic in Technology Award, Soc. of Hispanic Professional Engineers, 2000.
17. State of California Commendation Certificate from Lieutenant Governor Cruz M. Bustamante, 2001.
18. Orgullo Hispano Award, Robins Air Force Base, Sep. 23, 2003.
19. Hispanic Heritage Award, Robins Air Force Base, 2005.
20. IEEE Service Award, MWSCAS–NEWCAS, 2007.
21. IEEE Outstanding Educator Award "For ground-breaking developments to power-regulation and energy-harvesting technologies and outstanding contributions to analog microcircuit education," IEEE Atlanta Section, 2024.
22. IEEE Joseph M. Biedenbach Outstanding Engineering Educator Award "For continuous and impactful contributions to teaching and advancing the fields of analog, power, and energy microelectronics," IEEE Region 3, 2025.
23. Distinguished Faculty Achievement Award, School of Electrical & Computer Engineering, Georgia Institute of Tech., 2025.
24. IEEE Charles A. Desoer Technical Achievement Award "For seminal contributions to power-supply and energy-harvesting integrated circuits," IEEE Circuits and Systems Society, 2025.

C. Magazine Covers (Mag. with Prof. Rincón-Mora on their cover.)

25. "Bravo – National Award Winners," *Official Magazine of the Soc. of Hispanic Professional Engineers*, Sp. 2000.
26. "The 100 Most Influential Hispanics," *Hispanic Business*, Oct. 2000.
27. "A high-tech engineer with a low-tech lifestyle," *La Fuente* (Dallas publication), Mar. 2000.
28. "Gabriel Rincón-Mora - Impacta en la alta tecnología," *Nuevo Impacto* (Atlanta publication), Aug. 2002.
29. "Profesionales Latinos – La nueva cara de Georgia," *Nuevo Impacto* (Atlanta publication), Oct. 2003.
30. "Gabriel Rincón Mora – Outstanding engineer and author," *Nuevo Impacto* (Atlanta publication), Nov. 2004.
31. "CAS Society Achievement Awards," *IEEE Circuits and Systems Magazine*, Fall 2025.

D. Feature Stories (on Prof. Rincón-Mora)

32. "Passion for design, apathy for gizmos," *Electronic Engineering Times*, Jun. 2000.
33. "Designer has passion for work, apathy for gizmos," *Planet Analog*, Jun. 2000.
34. "By Day an Engineer," *Intown* (Atlanta publication), Aug. 2002.

35. "World-class training workshop on analog IC power management by top Integrated Circuit (IC) expert from the United States," *Hong Kong Science & Tech. Parks News & Newslett.*, Oct. 2003.
36. "Alumni Profile: Gabriel A. Rincón-Mora," *Summa Cum Laude*, Florida Int. Univ. Honors College, vol. 1, no. 3, 2011.
37. "Featured Engineer: Gabriel Alfonso Rincón-Mora," *EEWeb – Electrical Eng. Community*, Nov. 2012.
38. "Interview With Prof. Gabriel Alfonso Rincón-Mora," *IEEE Circuits and Systems Magazine*, Fall 2025.

E. Recognitions

39. TIDN Forum's Significant TI Contributor, Texas Instruments, 1999.
40. "Notar – Short Stories and Poems to Boot," *Official Mag. of the Soc. of Hispanic Professional Engineers*, Aug. 2002.
41. "Innovators Matter," *Hispanic Business*, Sept. 2002.
42. "Innovators Matter," *Hispanic Business*, Dec. 2002.
43. "Hispanic Eng. Talent," *Georgia Tech Soc. of Professional Hispanic Engineers*, Feb. 2003.
44. HENAAC Role Model of the Week, Hispanic Engineer Nat. Achievement Awards Corp., Jul. 5, 2005.
45. "SSCS Subsidizes Short Course on Linear Regulator Des. in Taipei," *IEEE Solid-State Circuits Soc. Newslett.*, Sep. 2006.
46. IEEE Plaque of Appreciation, IEEE APCCAS, 2009.
47. IEEE CAS Certificate of Appreciation, IEEE Circuits and Syst. Soc., 2009.
48. IEEE Plaque of Appreciation, IEEE ISOCC, 2009.
49. Thanks for Being a Great Teacher Certificate, Georgia Inst. of Tech., 2010.
50. Certificate of Appreciation, IEEE Circuits and Syst. Soc., 2010.
51. Certificate of Appreciation, IEEE Circuits and Syst. Soc., 2011.
52. Thanks for Being a Great Teacher Certificate, Georgia Inst. of Tech., 2012.
53. Certificate of Appreciation, Inst. of Electrical and Electronics Engineers, 2012.
54. Certificate of Appreciation, IEEE Circuits and Syst. Soc., 2012.
55. Certificate of Appreciation, IEEE Ind. Electronics Soc./Power Electronics Soc./IEEE Ind. App. Soc., 2013.
56. "List of Notable Venezuelan Americans" in Science, 2014.
57. NTU/Mediatek Plaque, Nanyang Technological Univ., Singapore, 2016.
58. Plaque of Appreciation, IEEE ISOCC, 2016.
59. Thanks for Being a Great Teacher Certificate, Georgia Inst. of Tech., 2018.
60. Thanks for Being a Great Teacher Certificate, Georgia Inst. of Tech., 2019.
61. Thanks for Being a Great Teacher Certificate, Georgia Inst. of Tech., 2020.
62. Thanks for Being an Excellent Teacher Certificate, Georgia Inst. of Tech., 2022.
63. Thanks for Being a Great Teacher Certificate for Analog IC Design, Georgia Inst. of Tech., 2024.
64. Thanks for Being a Great Teacher Certificate for Power IC Design, Georgia Inst. of Tech., 2024.

F. Product/Book/Article Distinctions

65. "Top 100 Products" of 1998 by *EDN* (on the cover of *Electronic Des.*) for TPS5210.
66. Top 7 Most Cited IEEE TCAS II '98 Paper: "Designing 1V Op Amps Using Standard Digital CMOS Technology," *IEEE Trans. on Circuits and Syst. II*, vol. 45, no. 7, pp. 769–780, Jul. 1998.
67. Top 25 Most Downloaded IEEE TCAS II '02 Paper: "A low voltage, rail-to-rail, class AB CMOS amplifier with highdrive and low output impedance characteristics," *IEEE Trans. on Circuits and Syst. II*, vol. 48, no. 8, pp. 753–761, Aug. 2001.
68. Top 200 Most Downloaded IEEE '04 Paper: "A High-Efficiency Linear RF Power Amplifier With a Power-Tracking Dynamically Adaptive Buck–Boost Supply," *IEEE Trans. Microw. Theory Techn.*, vol. 52, no. 1, pp. 112–120, Jan. 2004.
69. "7th Most Read Power Management Des. Line How-To Article in 2005": "Power Supply Ripple Rejection and Linear Regulators: What's all the noise about?" *Power Management Des. Line*, Sept. 20, 2005.
70. "2nd Most Read Power Management Des. Line How-To Article in 2006": "Harvesting energy into lithium-ion batteries," *Power Management Des. Line*, Feb. 14, 2006.
71. 2nd Place Award, '09 Science App. Int. Corp.'s Georgia Tech Paper Competition: "A Rectifier-Free Piezoelectric Energy Harvester Circuit."
72. 2nd Best Seller, IEEE Int. Solid-State Circuits Conf. '09: *Analog IC Des. with Low-Dropout Regulators*.

73. 3rd Best in Show, IEEE Int. Solid-State Circuits Conf. '14: *Analog IC Des. with Low-Dropout Regulators, 2nd Edition*.
74. Best Student Paper Award, 3rd Place, X. Li and G.A. Rincón-Mora, "Maximum power-point theory for thermoelectric harvesters," IEEE Midwest Symp. on Circuits and Syst., Phoenix, AZ, 2023.
75. Editor's Choice in 2025, M. Isaf and G.A. Rincón-Mora, "Piezoelectric Transducers: Complete Electromechanical Model with Parameter Extraction," *Sensors, Electronic Sensors, Invited*, vol. 24, iss. 13, no. 4367, July 2024.

G. Academic Distinctions

76. *Presidential Academic Fitness Award*, (signed by President George Bush, Sr.), 1989.
77. *Insignis Scholarship*, Univ. of Detroit, 1989.
78. *Dean's List*, Florida Int. Univ., 1989–1992.
79. *Florida Undergraduate Scholars Fund Scholarship*, State of Florida, 1989–1992.
80. *Faculty Scholars Scholarship*, Florida Int. Univ., 1989–1992.
81. *Honorary Award Recognition*, Nat. Dean's List, 1990–1992.
82. *Phi Kappa Phi* (honor society), Florida Int. Univ., 1991.
83. *Eta Kappa Nu* (nat. electrical eng. honor society), Florida Int. Univ., 1992.
84. *B.S.E.E. Magna Cum Laude*, Florida Int. Univ., 1992.
85. *Honorable Mention*, Nat. Science Foundation, 1993.
86. *Tau Beta Pi* (nat. eng. honor society), Georgia Inst. of Tech., 1994.
87. *Outstanding Ph.D. Graduate*, Georgia Inst. of Tech., 1996.