

## Biographical Sketch

### Prof. Gabriel Alfonso Rincón-Mora, Ph.D.

Motorola Solutions Foundation Professor, Georgia Institute of Technology  
Fellow of the National Academy of Inventors  
Fellow of the American Association for the Advancement of Science  
Fellow of the Institution of Engineering and Technology  
Fellow of the Institute of Electrical and Electronics Engineers



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**Background:** Born in Caracas, grew up in Maracay (Venezuela), and migrated to North Miami Beach (Florida) when he was 11 years old. He graduated from North Miami Beach Senior High, Florida International University with a Bachelors of Science (B.S.) degree in Electrical Engineering, & the Georgia Institute of Technology (Georgia Tech) with a Masters of Science (M.S.) degree in Electrical Engineering and a Minor in Mathematics and with a doctorate (Ph.D.) degree in Electrical Engineering.

**Career:** With Texas Instruments in 1994–2004 & the Georgia Institute of Technology (Georgia Tech) since 1999. Director of the Georgia Tech Analog Consortium in 2001–2004, Director of the TI Analog Fellowship Program in 2001–2015, & Visiting Professor at National Cheng Kung University in Taiwan in 2011–2019.

**Awards:** IEEE Charles A. Desoer Technical Achievement Award from the IEEE Circuits and Systems Society, Distinguished Faculty Achievement Award from the Georgia Institute of Technology, Charles E. Perry Visionary Award from Florida International University, Three-Year Patent Award from Texas Instruments, National Hispanic in Technology Award from the Society of Hispanic Professional Engineers, IEEE Joseph M. Biedenbach Outstanding Engineering Educator Award from IEEE Region Three, IEEE Outstanding Educator Award from the IEEE Atlanta Section, Orgullo Hispano Award from Robins Air Force Base, Hispanic Heritage Award from Robins Air Force Base, State of California Commendation Certificate from Lieutenant Governor Cruz M. Bustamante, & IEEE Service Award.

**Other Distinctions:** Inducted into Georgia Tech's Council of Outstanding Young Engineering Alumni, named one of "The 100 Most Influential Hispanics" by *Hispanic Business*, included in "List of Notable Venezuelan Americans" in Science, & selected IEEE Distinguished Lecturer three two-year terms.

**Body of Work:** 4 textbooks, 5 slide books, 3 literary books, 8 handbooks, 4 book chapters, 44 patents, over 200 articles, over 26 commercial power-chip products released to production, 25 educational videos, educational SPICE code for over 200 analog and power circuits, & over 170 keynote addresses, distinguished lectures, & research seminars.

**Recognition:** Featured on *IEEE Circuits and Systems Magazine*, *Electronic Engineering Times*, *Planet Analog*, *Intown* in Atlanta, *Summa Cum Laude* in Florida, *EEWeb*, *Hong Kong Science & Tech. Parks News & Newsletter*, & on the covers of *Hispanic Business*, *Official Magazine of the Society of Hispanic Professional Engineers*, *La Fuente* (in Dallas), & 3 times on *Nuevo Impacto* (in Atlanta).

**Professional Service:** Served IEEE & his profession on numerous occasions as General Chair & Co-Chair, Technical Program Chair & Co-Chair, Chapter Chair & Vice-Chair, International Liaison, Steering Committee Member, Advisory Panel Member, Associate Editor, Guest Editor & Co-Editor, & Editorial Board Member.

**Research:** On voltage references, linear regulators, switching power supplies, & energy-harvesting microchips. Design and develop silicon-based microchips and microsystems that harness power from batteries, fuel cells, magnetically coupled coils, & ambient sources, like motion, light, heat, & electromagnetic radiation, to power & supply mobile, portable, & self-sustaining devices for biomedical, consumer, & industrial applications.