ABSTRACT

A low drop-out regulator 16 includes an error amplifier 18 having a first input for receiving a reference voltage Vref, a second input, and an output, a pass element 22 having a control terminal coupled to the output of the error amplifier and a current path coupled between an input voltage Vin and an output terminal Vout, and a pair of resistors 24, 26 coupled in series between the output terminal Vout and ground. The second input of the error amplifier 18 coupled to a node B between the pair of resistors. The error amplifier provides an added pole/zero pair in the frequency response of the regulator.

4 Claims, 4 Drawing Sheets