



(12) EUROPEAN PATENT APPLICATION

(43) Date of publication:  
08.10.2003 Bulletin 2003/41

(51) Int Cl.7: G01R 19/32

(21) Application number: 02100346.2

(22) Date of filing: 05.04.2002

(84) Designated Contracting States:  
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU  
MC NL PT SE TR  
Designated Extension States:  
AL LT LV MK RO SI

(72) Inventor: Rincon-Mora, Gabriel  
Texas 75002, Allen (US)

(74) Representative: Holt, Michael et al  
Texas Instruments Ltd.,  
EPD MS/13,  
800 Pavilion Drive  
Northampton Business Park,  
Northampton NN4 7YL (GB)

(71) Applicant: Texas Instruments Incorporated  
Dallas, Texas 75251 (US)

(54) Temperature-compensated threshold circuit

(57) The temperature compensated threshold circuit includes: a positive trip point circuit 60 for providing a positive trip point when an input voltage PSM is higher than a positive supply voltage VCC; a negative trip point circuit 62 for providing a negative trip point when the

input voltage PSM is below a negative supply voltage AGND; and a bias circuit 64 for providing to the positive and negative trip point circuits 60 and 62 a first current proportional to absolute temperature and a second current proportional to a base emitter voltage.













