

# CURRICULUM VITA

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### **I. Earned Degrees**

**B.S.** in Electrical Engineering, **Florida International University** (GPA 3.83, *Faculty Scholar, FL Undergraduate Scholar, High Honors*), **1992**.

**M.S.** in Electrical Engineering (Minor: Mathematics), **Georgia Institute of Technology** (GPA 3.82), **1994**.

**Ph.D.** in Electrical Engineering under Prof. Phillip E. Allen (Minor: Mathematics) **Georgia Institute of Technology** (*Outstanding Ph.D. Graduate*), **1996**.

### **II. Employment**

*Electrical Engineer and Laboratory System Specialist*, **Northern Telecom**, Atlanta, Georgia, **1993**.

*Analog IC Design Engineer*, Standard Linear Group, **Texas Instruments**, Dallas, Texas, **1994–1996**.

*Senior Design Engineer and Design Team Leader*, **Texas Instruments**, Dallas, Texas, **1997–2001**.

*Member of Group Technical Staff*, **Texas Instruments**, Dallas, Texas, **1999–2003**.

*Adjunct Professor*, Electrical and Computer Engineering, **Georgia Institute of Technology**, **1999–2001**.

*Senior Analog IC Design Consultant*, **Texas Instruments**, Dallas, Texas, **2003–2004**.

*Assistant Professor*, Electrical and Computer Engineering, **Georgia Institute of Technology**, **2001–2007**.

*Director*, Georgia Tech Analog Consortium, **Georgia Institute of Technology**, **2001–2004**.

*Associate Professor with Tenure*, Electrical and Computer Engineering, **Georgia Institute of Technology**, **2007–2012**.

*Visiting Professor*, Electrical Engineering, **National Cheng Kung University**, Taiwan, **since 2011**.

*Professor*, Electrical and Computer Engineering, **Georgia Institute of Technology**, **since 2012**.

### **III. Scholarly Products**

#### **Ph.D. Dissertation:**

G.A. Rincón-Mora, *Current Efficient, Low Voltage, Low Dropout Regulators*. Georgia Institute of Technology, **1996** (Advisor: Prof. Phil Allen).

#### **Books:**

B1. G.A. Rincón-Mora, *Voltage References*. New Jersey: IEEE Press and John Wiley & Sons, Inc. (192 pages), **2001** [Translated into Chinese].

B2. G.A. Rincón-Mora, *Power Management ICs*. Raleigh: Lulu (268 pages), **2005**.

B3. G.A. Rincón-Mora, *Analog IC Design with Low-Dropout Regulators*. New York: McGraw-Hill (400 pages), Jan. **2009** [Translated into Chinese].

B4. G.A. Rincón-Mora, *Analog IC Design with Low-Dropout Regulators, Second Edition*. New York: McGraw-Hill (507 pages), **2014** [Translated into Chinese].

B5. G.A. Rincón-Mora, *Analog IC Design, Fifth Edition*. Raleigh: Lulu (244 pages), **2016**.

B6. G.A. Rincón-Mora, *Power IC Design, Fifth Edition*. Raleigh: Lulu (262 pages), **2016**.

B7. G.A. Rincón-Mora, *Short Stories and Poems to Boot!* New York: Vantage Press (86 pages), **2001** [Short Stories/Poetry].

B8. G.A. Rincón-Mora, *Triple Engagement*. New York: iUniverse (160 pages), **2004** [Short Stories/Poetry].

B9. G.A. Rincón-Mora, *Vanish*. Raleigh: Lulu (148 pages), **2009** [Novella].

#### **Book Chapters:**

BC1. G.A. Rincón-Mora, "Harvesting Microelectronic Circuits," *Energy Harvesting Technologies* (Editors: S. Priya and D.J. Inman), Springer, Jan. **2009**.

BC2. G.A. Rincón-Mora, "Energizing and Powering Microsystems," *Integrated Microsystems: Electronics, Photonics, and Biotechnology* (Editor: K. Iniewski), CRC Press, Oct. **2011**.

BC3. G.A. Rincón-Mora, "Vibration-Based Energy-Harvesting Integrated Circuits," *Advances in Energy Harvesting Methods* (Editors: N. Elvin and A. Erturk), Springer, Feb. **2013**.

## CURRICULUM VITA

BC4. G.A. Rincón-Mora, "**Energy-Harvesting Integrated Circuits**," *Energy Harvesting with Functional Materials and Microsystems* (Editors: M. Bhaskaran, S. Sriram, and K. Iniewski), CRC Press, Nov. 2013.

**Patents Issued:** \* Boldface inventors are engineers and students Prof. Rincón-Mora advised.

- P1. G.A. Rincón *et al.*, "Amplifier circuit and method," **U.S. 5,491,437**, Feb. 13, 1996.
- P2. G.A. Rincón *et al.*, "Controlled current output stage amplifier circuit and method," **U.S. 5,500,625**, Mar. 19, 1996.
- P3. M. Corsi and G.A. Rincón, "Cross coupled quad comparator for current sensing independent of temperature," **U.S. 5,519,341**, May 21, 1996.
- P4. G.A. Rincón *et al.*, "Output stage of amplifier circuit," **EP 715405**, Jun. 5, 1996.
- P5. G.A. Rincón *et al.*, "Controlled current output stage amplifier circuit and method," **JP 8237046**, Sept. 13, 1996.
- P6. M. Corsi and G.A. Rincón, "Current sensing circuit and method," **U.S. 5,614,850**, Mar. 25, 1997.
- P7. M. Corsi, G.A. Rincón *et al.*, "A voltage regulator," **EP 851332**, Jan. 7, 1998.
- P8. M. Corsi, G.A. Rincón *et al.*, "Drop-out voltage controller," **JP 10187258**, Jul. 14, 1998.
- P9. M. Corsi, G.A. Rincón *et al.*, "Low drop-out regulator with PMOS pass element," **U.S. 5,867,015**, Feb. 2, 1999.
- P10. G.A. Rincón-Mora, "DC-DC converter with voltage loss compensation," **EP 928056**, Jul. 7, 1999.
- P11. G.A. Rincón-Mora, "Low voltage, current-mode, piecewise-linear curvature corrected bandgap reference," **U.S. 5,952,873**, Sept. 14, 1999.
- P12. G.A. Rincón-Mora, "Optimized frequency shaping circuit topologies for LDOs," **U.S. 5,982,226**, Nov. 9, 1999.
- P13. G.A. Rincón-Mora and M. Corsi, "Low-drop-out voltage regulator for electronic system, such as wireless telephone," **EP 957421**, Nov. 17, 1999.
- P14. G.A. Rincón-Mora *et al.*, "Low-dropout voltage regulator incorporating a current efficient transient response boost circuit," **U.S. 6,046,577**, Apr. 4, 2000.
- P15. G.A. Rincón-Mora, "Miller compensated amplifier for operation with capacitive loading," **EP 1006648**, Jun. 7, 2000.
- P16. G.A. Rincón-Mora, "Active compensating capacitive multiplier," **U.S. 6,084,475**, Jul. 4, 2000.
- P17. G.A. Rincón-Mora, "Bandgap circuits with curvature-correction," **EP 1041480**, Oct. 4, 2000.
- P18. G.A. Rincón-Mora, "Exact curvature-correcting method for bandgap circuits," **U.S. 6,157,245**, Dec. 5, 2000.
- P19. G.A. Rincón-Mora and M. Corsi, "Current-efficient low-drop-out voltage regulator with improved load regulation and frequency response," **U.S. 6,188,211**, Feb. 13, 2001.
- P20. G.A. Rincón-Mora, "Accurate, fast, and user programmable hysteretic comparator," **U.S. 6,229,350**, May 8, 2001.
- P21. G.A. Rincón-Mora and **M. Huggins**, "High power supply ripple rejection internally compensated low drop-out voltage regulator using PMOS pass device," **U.S. 6,304,131**, Oct. 16, 2001.
- P22. G.A. Rincón-Mora, "Integrated low ripple, high frequency hysteretic controller for dc-dc converters," **U.S. 6,369,555**, Apr. 9, 2002.
- P23. G.A. Rincón-Mora and **B. Abesingha**, "Method of minimizing package-shift effects in integrated circuits by using a thick metallic overcoat," **U.S. 6,432,753**, Aug. 13, 2002.
- P24. G.A. Rincón-Mora, "Adjustable temperature-compensated threshold circuit with trip-points exceeding the given supplies," **EP 1265363**, Dec. 11, 2002.
- P25. G.A. Rincón-Mora and **R. Stair**, "Buffer/driver for low dropout regulators," **U.S. 6,501,305**, Dec. 31, 2002.
- P26. G.A. Rincón-Mora, "Adjustable temperature-compensated threshold circuit with trip-points exceeding the given supplies," **U.S. 6,545,511**, Apr. 8, 2003.
- P27. G.A. Rincón-Mora and **M. Pulkin**, "Stable low dropout, low impedance driver for linear regulators," **U.S. 6,573,694**, Jun. 3, 2003.
- P28. G.A. Rincón-Mora, "Temperature-compensated threshold circuit," **EP 1351063**, Oct. 8, 2003.
- P29. G.A. Rincón-Mora and M. Corsi, "Current-efficient low-drop-out voltage regulator with improved load regulation and frequency response," **DE 69910888**, Oct. 9, 2003.
- P30. G.A. Rincón-Mora, "Integrated low ripple, high frequency power efficient hysteretic controller for dc-dc converters," **U.S. 6,628,109**, Sept. 30, 2003.
- P31. G.A. Rincón *et al.*, "Output stage of amplifier circuit," **DE 69532061**, Dec. 11, 2003.

## CURRICULUM VITA

- P32. G.A. Rincón-Mora *et al.*, "Semiconductor device which minimizes package-shift effects in integrated circuits by using a thick metallic overcoat," **U.S. 6,750,553**, Jun. 15, **2004**.
- P33. G.A. Rincón-Mora and **R. Stair**, "Circuit and method to facilitate threshold voltage extraction and facilitate operation of a capacitor multiplier," **U.S. 6,806,762**, Oct. 19, **2004**.
- P34. M. Corsi, G.A. Rincón *et al.*, "A voltage regulator," **DE 69727783**, Dec. 30, **2004**.
- P35. G.A. Rincón-Mora, **V. Gupta**, and P. Raha, "Low dropout monolithic linear regulator having wide operating load range," **U.S. 6,847,260**, Jan. 25, **2005**.
- P36. G.A. Rincón-Mora, "Active compensating capacitive multiplier," **DE 69934566**, Feb. 8, **2007**.
- P37. G.A. Rincón-Mora and **M. Arnold**, "Voltage regulator with low dropout voltage (mode-hopping buffer with rail-to-rail output for low dropout)," **U.S. 7,339,416**, Mar. 4, **2008**.
- P38. G.A. Rincón-Mora, "Temperature-compensated threshold circuit," **DE 60225626**, Apr. 30, **2008**.
- P39. G.A. Rincón-Mora, "Exact curvature-correcting method for bandgap circuits," **DE 60042142**, June 18, **2009**.
- P40. G.A. Rincón-Mora and **M. Arnold**, "Gate driver circuit for power transistor," **U.S. 7,560,973**, Jul. 14, **2009**.
- P41. G.A. Rincón-Mora, "Increase in active compensation capacitive property," **JP 4528394**, Aug. 18, **2010**.
- P42. **D. Kwon** and G.A. Rincón-Mora, "Rectifier-free piezoelectric energy harvester and battery charger," **U.S. 8,368,290**, Feb. 5, **2013 (licensed in 2015)**.

**Refereed Journal Articles:** \* Boldface authors are engineers and students Prof. Rincón-Mora advised.

- J1. G.A. Rincón-Mora and P.E. Allen, "A low-voltage, low quiescent current, low drop-out regulator," *IEEE Journal of Solid-State Circuits (JSSC)*, vol. 33, no. 1, pp. 36–44, Jan. **1998**.
- J2. G.A. Rincón-Mora and P.E. Allen, "Optimized frequency-shaping circuit topologies for LDO's," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 45, no. 6, pp. 703–708, Jun. **1998**.
- J3. B.J. Blalock, P.E. Allen, and G.A. Rincón-Mora, "Designing 1V op amps using standard digital CMOS technology," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 45, no. 7, pp. 769–780, Jul. **1998**.
- J4. G.A. Rincón-Mora and P.E. Allen, "A 1.1 V current-mode and piecewise-linear curvature corrected bandgap reference," *IEEE Journal of Solid-State Circuits (JSSC)*, vol. 33, no. 10, pp. 1551–1554, Oct. **1998**.
- J5. G.A. Rincón-Mora, "Active capacitor multiplier in Miller-compensated circuits," *IEEE Journal of Solid-State Circuits (JSSC)*, vol. 35, no. 1, pp. 26–32, Jan. **2000**.
- J6. **R. Stair** and G.A. Rincón-Mora, "A low voltage, rail-to-rail, class AB CMOS amplifier with high drive and low output impedance characteristics," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 48, no. 8, pp. 753–761, Aug. **2001**.
- J7. **B. Abesingha**, G.A. Rincón-Mora, and D. Briggs, "Voltage shift in plastic-packaged bandgap references," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 49, no. 10, pp. 681–685, Oct. **2002**.
- J8. **R. Dokania** and G.A. Rincón-Mora, "Cancellation of load-regulation in low drop-out regulators," *IET Electronic Letters (EL)*, vol. 38, issue 22, pp. 1300–1302, Oct. **2002**.
- J9. **B. Sahu** and G.A. Rincón-Mora, "A high-efficiency linear RF power amplifier with a power-tracking dynamically adaptive buck–boost Supply," *IEEE Transactions on Microwave Theory and Techniques (TMTT)*, vol. 52, no. 1, pp. 112–120, Jan. **2004**.
- J10. **B. Sahu** and G.A. Rincón-Mora, "A low voltage, dynamic, non-inverting, synchronous buck–boost converter for portable applications," *IEEE Transactions on Power Electronics (TPE)*, vol. 19, no. 2, pp. 443–452, Feb. **2004**.
- J11. **S. Zhou** and G.A. Rincón-Mora, "A high efficiency, soft switching dc–dc converter with adaptive current-ripple control for portable applications," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 53, no. 4, pp. 294–298, Apr. **2006**.
- J12. **M. Chen** and G.A. Rincón-Mora, "An accurate electrical battery model capable of predicting runtime and i–v performance," *IEEE Transactions on Energy Conversion (TEC)*, vol. 21, no. 2, pp. 504–511, Jun. **2006**.
- J13. **M. Chen** and G.A. Rincón-Mora, "Accurate, compact, and power efficient li-ion battery charger circuit," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 53, no. 11, pp. 1180–1184, Nov. **2006**.
- J14. **H.P. Forghani-zadeh** and G.A. Rincón-Mora, "Low-power CMOS ramp generator circuit for dc–dc converters," *Journal of Low Power Electronics (JOLPE)*, vol. 2, no. 3, pp. 437–441, Dec. **2006**.

## CURRICULUM VITA

- J15. **B. Sahu** and G. A. Rincón-Mora, "An accurate, low voltage, CMOS switching power supply with adaptive on-time pulse-frequency modulation," *IEEE Transactions on Circuits and Systems I (TCAS I)*, vol. 54, no. 2, pp. 312–321, Feb. 2007.
- J16. **B. Sahu** and G.A. Rincón-Mora, "A high efficiency WCDMA RF power amplifier (PA) with adaptive, dual-mode buck-boost supply and bias-current control," *IEEE Microwave and Wireless Components Letters (MWCL)*, vol. 17, no. 3, pp. 238–240, Mar. 2007.
- J17. **V. Gupta** and G.A. Rincón-Mora, "Achieving less than 2% 3- $\sigma$  mismatch with minimum channel-length CMOS devices," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 54, no. 3, pp. 232–236, Mar. 2007.
- J18. **H.P. Forghani-zadeh** and G.A. Rincón-Mora, "An accurate, continuous, and lossless self-learning CMOS current-sensing scheme for inductor-based dc–dc converters," *IEEE Journal of Solid-State Circuits (JSSC)*, vol. 42, no. 3, pp. 665–679, Mar. 2007.
- J19. **H.P. Forghani-zadeh** and G.A. Rincón-Mora, "A fast and reliable top-level simulation strategy for mixed-signal ICs and its application to dc–dc converter circuits," *IET Circuits, Devices, and Systems (CDS)*, vol. 1, no. 2, pp. 143–150, Apr. 2007.
- J20. **H.P. Forghani-zadeh** and G.A. Rincón-Mora, "A programmable 210  $\mu$ V offset rail-to-rail  $G_M$ –C filter," *IEEE Transactions on Circuits and Systems I (TCAS I)*, vol. 54, no. 8, pp. 1636–1646, Aug. 2007.
- J21. **V. Gupta** and G.A. Rincón-Mora, "Low output impedance 0.6 $\mu$ m-CMOS sub-bandgap reference," *IET Electronic Letters (EL)*, vol. 43, pp. 1085–1087, Sept. 2007.
- J22. **N. Keskar** and G.A. Rincón-Mora, "A fast, sigma–delta boost dc–dc converter tolerant to wide LC filter variations," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 55, pp. 198–202, Feb. 2008.
- J23. **N. Keskar** and G.A. Rincón-Mora, "A compact 1-30 $\mu$ H, 1-350 $\mu$ F, 5-50m $\Omega$  ESR compliant, 1.5% accurate 0.6 $\mu$ m CMOS differential sigma–delta boost dc–dc converter," *Analog Integrated Circuits and Signal Processing Journal (AICSP)*, vol. 54, no. 3, pp. 157–169, 2008.
- J24. **M. Chen** and G.A. Rincón-Mora, "A compact electrical model for microscale fuel cells capable of predicting runtime and i–v polarization performance," *IEEE Transactions on Energy Conversion (TEC)*, vol. 23, no. 3, pp. 842–850, Sept. 2008.
- J25. **E.O. Torres** and G.A. Rincón-Mora, "Energy-harvesting system-in-package (SiP) microsystem," *ASCE Journal of Energy Engineering (JEE)*, **Invited**, vol. 134, no. 4, pp. 121–129, Dec. 2008.
- J26. **S. Kim** and G.A. Rincón-Mora, "Achieving high efficiency under micro-watt loads with switching buck dc–dc converters," *Journal of Low Power Electronics (JOLPE)*, vol. 5, no. 2, pp. 229–240, Aug. 2009.
- J27. **D. Kwon** and G.A. Rincón-Mora, "Single-inductor multiple-output (SIMO) switching dc–dc converters," *IEEE Transactions on Circuits and Systems II (TCAS II)*, **Invited**, vol. 56, no. 8, Aug. 2009.
- J28. **E.O. Torres** and G.A. Rincón-Mora, "Electrostatic energy-harvesting and battery-charging CMOS system prototype," *IEEE Transactions on Circuits and Systems I (TCAS I)*, vol. 56, no. 9, pp. 1938–1948, Sept. 2009.
- J29. **L.A. Milner** and G.A. Rincón-Mora, "Limits of predictive current-ripple suppression in switching power supply ICs," *IET Power Electronics (PE)*, vol. 3, no. 1, pp. 43–53, Jan. 2010.
- J30. **V. Gupta** and G.A. Rincón-Mora, "A low-impedance, sub-bandgap 0.6 $\mu$ m CMOS reference with 0.84% trimless 3-sigma accuracy and –30dB worst-case PSRR up to 50MHz," *Analog Integrated Circuits and Signal Processing Journal (AICSP)*, vol. 62, no. 3, p. 345, 2010.
- J31. **E.O. Torres** and G.A. Rincón-Mora, "A 0.7  $\mu$ m BiCMOS electrostatic energy-harvesting system IC," *IEEE Journal of Solid-State Circuits (JSSC)*, vol. 45, no. 2, pp. 483–496, Feb. 2010.
- J32. **N. Keskar** and G.A. Rincón-Mora, "One clock-cycle response 0.5 $\mu$ m CMOS dual-mode sigma–delta dc–dc bypass boost converter stable over wide  $R_{ESR}LC$  variations," *Advances in Power Electronics (APE)*, vol. 2010, no. 253508, p. 9, 2010.
- J33. **L.A. Milner** and G.A. Rincón-Mora, "A feed-forward 10 $\times$  CMOS current-ripple suppressor for switching power supplies," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 57, no. 5, pp. 354–378, May 2010.
- J34. **E.O. Torres** and G.A. Rincón-Mora, "Self-tuning electrostatic energy-harvester IC," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 57, no. 10, pp. 808–812, Oct. 2010.
- J35. **A. Patel** and G.A. Rincón-Mora, "High power-supply-rejection (PSR) current-mode low-dropout (LDO) regulator," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 57, no. 11, pp. 868–873, Nov. 2010.
- J36. **D. Kwon** and G.A. Rincón-Mora, "A 2- $\mu$ m BiCMOS rectifier-free ac–dc piezoelectric energy harvester–charger IC," *IEEE Transactions on Biomedical Circuits and Systems (TBioCAS)*, **Invited**, vol. 4, no. 6, pp. 400–409, Dec. 2010.

## CURRICULUM VITA

- J37. **D. Kwon**, G.A. Rincón-Mora, and **E.O. Torres**, "Harvesting ambient kinetic energy with switched-inductor converters," *IEEE Transactions on Circuits and Systems I (TCAS I)*, Invited, vol. 58, no. 7, pp. 1551–1560, July 2011.
- J38. **R.D. Prabha**, **D. Kwon**, **O. Lazaro**, **K.D. Peterson**, and G.A. Rincón-Mora, "Increasing electrical damping in energy-harnessing transducers," *IEEE Transactions on Circuits and Systems II (TCAS II)*, Special Issue on Energy Harvesting, vol. 58, no. 12, pp. 787–791, Dec. 2011.
- J39. **L.A. Milner** and G.A. Rincón-Mora, "Small saturating inductors for more compact switching power supplies," *IEEE Transactions on Electrical and Electronic Engineering (TEEE)*, vol. 7, no. 1, pp. 69–73, Jan. 2012.
- J40. **S. Kim** and G.A. Rincón-Mora, "Single-inductor fuel cell–Li ion charger–supply IC with nested hysteretic control," *Analog Integrated Circuits and Signal Processing Journal (AICSP)*, vol. 70, no. 1, Page 33–45, Jan. 2012.
- J41. G.A. Rincón-Mora, **A.A. Blanco**, and **J.P. Vogt**, "A 1.3- $\mu$ W, 0.6-m CMOS current–frequency analog–digital converter for implantable blood-glucose monitors," *Journal of Low Power Electronics (JOLPE)*, vol. 8, pp. 47–57, Feb. 2012.
- J42. **O. Lazaro**, G.A. Rincón-Mora, and **J.P. Vogt**, "1–50-MHz VHF electromagnetic sensor-interface power-attenuation detector circuit," *International Journal of Electronics and Communications (IJEC)*, vol. 66, no. 6, pp. 502–508, Jun. 2012.
- J43. **O. Lazaro** and G.A. Rincón-Mora, "Inductively coupled 180-nm CMOS charger with adjustable energy-investment capability," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 60, no. 8, pp. 482–486, Aug. 2013.
- J44. **O. Lazaro** and G.A. Rincón-Mora, "180-nm CMOS wideband capacitor-free inductively coupled power receiver and charger," *IEEE Journal of Solid-State Circuits (JSSC)*, vol. 48, no. 11, pp. 2839–2849, Nov. 2013.
- J45. **D. Kwon** and G.A. Rincón-Mora, "A single-inductor 0.35- $\mu$ m CMOS energy-investing piezoelectric harvester," *IEEE Journal of Solid-State Circuits (JSSC)*, vol. 49, no. 10, pp. 2277–2291, Oct. 2014.
- J46. **A. Blanco** and G.A. Rincón-Mora, "A 44–93- $\mu$ s 250–400-mV 0.18- $\mu$ m CMOS starter for dc-sourced switched-inductor energy harvesters," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 61, no. 12, pp. 1002–1006, Dec. 2014.
- J47. **O. Lazaro** and G.A. Rincón-Mora, "A non-resonant self-synchronizing inductively coupled 0.18- $\mu$ m CMOS power receiver and charger," *IEEE Journal of Emerging and Selected Topics in Power Electronics (ESTPE)*, vol. 3, no. 1, pp. 261–271, Mar. 2015.
- J48. **S. Kim** and G.A. Rincón-Mora, "Dual-source hysteretic switched-inductor 0.18- $\mu$ m complementary metal–oxide–semiconductor charger–supply system," *IET Circuits, Devices, and Systems (CDS)*, vol. 9, no. 4, pp. 275–282, 2015.
- J49. **R. Damodaran Prabha** and G.A. Rincón-Mora, "Maximizing power-transfer efficiency in low-power DC–DC converters," *IET Electronic Letters (EL)*, vol. 51, no. 23, pp. 1918–1920, Nov. 2015.
- J50. **R. Damodaran Prabha** and G.A. Rincón-Mora, "0.18- $\mu$ m light-harvesting battery-assisted charger–supply CMOS system," *IEEE Transactions on Power Electronics (TPE)*, vol. 31, no. 4, pp. 2950–2958, Apr. 2016.
- J51. G.A. Rincón-Mora and **S. Yang**, "Tiny piezoelectric harvesters: Principles, constraints, and power conversion," *IEEE Transactions on Circuits and Systems I (TCAS I)*, Invited, vol. 63, no. 5, pp. 639–649, May 2016.
- J52. **R. Damodaran Prabha** and G.A. Rincón-Mora, "Drawing the most power from low-cost single-well 1-mm<sup>2</sup> CMOS photovoltaic cells," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 64, no. 1, pp. 46–50, Jan. 2017.
- J53. **C. Solis** and G.A. Rincón-Mora, "0.6- $\mu$ m CMOS switched-inductor dual-supply hysteretic current-mode buck converter," *IEEE Transactions on Power Electronics (TPE)*, vol. 32, no. 3, pp. 2387–2394, Mar. 2017.
- J54. **A. Blanco** and G.A. Rincón-Mora, "Bootstrapping and Resetting CMOS Starter for Thermoelectric and Photovoltaic Chargers," *IEEE Transactions on Circuits and Systems II (TCAS II)*, Accepted, Mar. 2017.

**Invited Trade Journal Articles:** \* Boldface authors are engineers and students Prof. Rincón-Mora advised.

- T1. **N. Keskar** and G.A. Rincón-Mora, "A user-friendly boost dc–dc converter topology – it's fast and widely stable," *Power Management Design Line (PMDL)*, Jan. 23, 2005.
- T2. **N. Keskar** and G.A. Rincón-Mora, "A user-friendly boost dc–dc converter topology – it's fast and widely stable," *Planet Analog*, Jan. 26, 2005.
- T3. **V. Gupta** and G.A. Rincón-Mora, "Inside the belly of the beast: A map for the wary bandgap reference designer when confronting process variations," *Power Management Design Line (PMDL)*, Feb. 18, 2005.
- T4. G.A. Rincón-Mora and **P. Forghani**, "Accurate and lossless current-sensing techniques: A practical myth?" *Power Management Design Line (PMDL)*, Mar. 17, 2005.
- T5. G.A. Rincón-Mora and **M. Chen**, "Self-powered chips – the work of fiction," *Power Management Design Line (PMDL)*, Apr. 28, 2005.

## CURRICULUM VITA

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2. (Only instructor: **3-day** short course, 18 participants), *Low Voltage, State-of-the-Art Integrated Power Management Circuits – A Top-Down Design Approach*. **Hong Kong Science and Technology Park**, Hong Kong, China, Aug. **2003**.
3. (Only instructor: **5-day** short course, 15 participants), *CMOS Analog Integrated Circuits*. **Georgia Tech** Global Learning and Conference Center, Atlanta, Georgia, Sept. 26–30, **2005**.
4. (Only instructor: **1-hour** short course), *Dynamically Adaptive Power Supply Circuits for Radio-Frequency (RF) Power Amplifier (PA) Applications*. Invited **IEEE Expert Now** (On-Line) Module, Fall **2005**.
5. (Only instructor: **4-day** short course, 23 participants), *Power Management Integrated Circuits – A Top-Down Design Approach*. **ON Semiconductor** in **Bratislava**, Slovakia, Dec. 12–15, **2005**.
6. (Only instructor: **4-day** short course, 22 participants), *Power Management Integrated Circuits – A Top-Down Design Approach*. **ON Semiconductor** in **Toulouse**, France, Dec. 19–22, **2005**.
7. (Only instructor: **4-day** short course, 20 participants), *Power Management Integrated Circuits – A Top-Down Design Approach*. **Navy**: Space and Naval Warfare Systems Command, **San Diego**, California, Apr. 10–13, **2006**.
8. (Only instructor: **4-day** short course, 20 participants), *Power Management Integrated Circuits – A Top-Down Design Approach*. **Cypress Semiconductor** in **Colorado Springs**, Colorado, Apr. 17–20, **2006**.
9. (Only instructor: **1-day** short course, 70 participants), *Linear Regulators - From the Ground Up...* **IEEE SSCS Distinguished Lecture** in **Hsinchu**, Taiwan, Jun. 8, **2006**.
10. (Only instructor: **1-day** short course, 40 participants), *Linear Regulators - From the Ground Up...* **IEEE SSCS Distinguished Lecture** in **Taipei**, Taiwan, Jun. 9, **2006**.
11. (Only instructor: **4-day** short course, 15 participants), *Power Management Integrated Circuits – A Top-Down Design Approach*. **Toko Inc.** in **Saitama**, Japan, Dec. 11–14, **2006**.
12. (Only instructor: **3-day** short course, 20 participants), *Power Management Integrated Circuits – A Top-Down Design Approach*. **Intel Corp.** in **Hillsboro**, Oregon, May 9–11, **2007**.
13. (Only instructor: **4-day** short course, 15 participants), *Power Management Integrated Circuits – A Top-Down Design Approach*. **Spyro Technology** in **Singapore**, May 21–24, **2007**.
14. (Only instructor: **4-day** short course, 41 participants), *Analog IC Design – An Intuitive Approach*. Integrated Device Technology (**IDT**) in Duluth, **Georgia**, Jan. 26, Feb. 23, and Mar. 9 and 23, **2009**.
15. (Only instructor: **2-day** short course, 50 participants), *Designing Bandgap Voltage References*, National Cheng-Kung University (**NCKU**) in Tainan, Taiwan, Dec. 5–6, **2012**.
16. (Only instructor: **2-day** short course, 50 participants), *Stabilizing Complex Single-Inductor DC–DC Power Supplies*, National Cheng-Kung University (**NCKU**), Tainan IEEE Solid-State Circuits and Circuits and System Society Chapters (**SSCS** and **CASS**) in Tainan, Taiwan, Nov. 17 and 20, **2015**.
17. (Only instructor: **3-day** short course, 25 participants), *Linear Regulators – From the Ground Up*, **Dialog Semiconductor** in **Swindon**, United Kingdom, Dec. 1–3, **2015**.
18. (Only instructor: **2-day** short course), *Designing Photovoltaic-Sourced Charger-Supply Microsystems*, National Cheng-Kung University (**NCKU**), Tainan IEEE Solid-State Circuits and Circuits and System Society Chapters (**SSCS** and **CASS**) in Tainan, Taiwan, Nov. 17–18, **2016**.

## CURRICULUM VITA

19. (Only instructor: **2-day** short course), *Design Insight and Intuition of Negative Feedback at the Transistor Level*, National Cheng-Kung University (NCKU), Tainan IEEE Solid-State Circuits and Circuits and System Society Chapters (SSCS and CASS) in Tainan, Taiwan, Nov. 18 and 21, **2016**.
20. (Only instructor: **4-day** short course), *Low-Power and High-Speed Switched-Inductor Power Supplies and Energy Harvesters*, **Dialog Semiconductor** in Santa Clara, California, Mar. 20–23, **2017**.

### Conference Seminar/Tutorial Presentations:

1. "Self-Oscillating Hysteretic V-Mode DC–DC Controllers: From the Ground Up," IEEE Power Electronics Specialists Conference (**PESC**), Vancouver, Canada, Jun. **2001**.
2. "Integrated LDOs: From the Ground Up!" IEEE International Symposium on Circuits and Systems (**ISCAS**), Scottsdale, Arizona, May **2002**.
3. "Integrated DC–DC Converters: A Topological Journey!" IEEE Midwest Symposium on Circuits and Systems (**MWSCAS**), Tulsa, Oklahoma, U.S.A., Aug. **2002**.
4. "Dynamically Adaptive Power Supply Circuits for PA Wireless Applications," IEEE International Microwave Symposium (**IMS**), Long Beach, California, Jun. **2005**.
5. "Hybrid Fuel Cell/Lithium-Ion Powered, Power Conscious SiP ICs," 1<sup>st</sup> International Workshop on 3S - **SOP, SIP, SOC Electronic Technologies**, Atlanta, Georgia, Sept. **2005**.
6. "AC Design and Performance of Low-Dropout Regulators (LDOs)," IEEE European Conference on Circuit Theory and Design (**ECCTD**), Sevilla, España, Aug. 30, **2007**.
7. "Powering Micro-Systems with Fuel-Cell Hybrids," 10th Annual International Conference on **Small Fuel Cells**, Atlanta, Georgia, Apr. 30, **2008**.
8. "Low-Dropout Regulator (LDO) ICs," IEEE International **NEWCAS–TAISA** Conference, Montreal, Canada, Jun. 22, **2008**.
9. "Powering Microsystems," **CMOS Emerging Technologies Workshop**, Vancouver, Canada, Sept. 25–27, **2009**.
10. "Switching DC-DC Supplies and their Single-Inductor, Multiple-Output (SIMO) Derivatives," IEEE International Symposium on Circuits and Systems (**ISCAS**), Paris, France, May 30, **2010**.
11. "Energy-Harvesting Switching Converter ICs," International Workshop on **Power Supply On Chip**, Cork, Ireland, Oct. 15, **2010**.
12. "Energizing and Powering Microsystems," The Materials Research Society (**MRS**) Fall Meeting, Boston, Massachusetts, Nov. 29–Dec. 2, **2010**.
13. "Power-Management Systems on Chip (SoC) for Mobile Applications," IEEE International Conference on Microelectronics (**ICM**), Cairo, Egypt, Dec. 19–22, **2010**.
14. "Power-Supply Circuits and Systems for Battery-Powered Devices," IEEE Very Large-Scale Integration Design, Automation and Test (**VLSI–DAT**), Hsinchu, Taiwan, Apr. 25–27, **2011**.
15. "Energy-Harvesting ICs," IEEE European Solid-State Circuits Conference (**ESSCIRC**), Helsinki, Finland, Sept. 12–16, **2011**.
16. "Energizing and Powering Microsystems," IEEE Faible Tension Faible Consommation (**FTFC**), Paris, France, Jun. 6–8, **2012**.
17. "Energy-Harvesting Integrated Circuits," IEEE International **NEWCAS** Conference, Montreal, Canada, Jun. 17–20, **2012**.
18. "Harvesting ICs," **CMOS Emerging Technologies Workshop**, Vancouver, Canada, Jul. 18–20, **2012**.
19. "Energy-Harnessing Integrated Circuits," **Seminario de Nanoelectrónica y Diseño Avanzado**, Departamento de Electrónica del Instituto Nacional de Astrofísica, Óptica y Electrónica (INAOE), Puebla, México, Sept. 19–21, **2012**.
20. "Energizing and Powering Microsystems," **SHPE National Conference**, Ft. Worth, Texas, Nov. 14–18, **2012**.
21. "Feedback Control of Switched-Inductor Supplies - An Intuitive Approach," IEEE Asia Pacific Conference on Circuits and Systems (**APCCAS**), Kaohsiung, Taiwan, Dec. 2–5, **2012**.
22. "Designing Bandgap-Voltage References," IEEE International Symposium on Circuits and Systems (**ISCAS**), Beijing, China, May 19–23, **2013**.
23. "Energy-Harvesting Integrated Circuits," IEEE International Symposium on Industrial Electronics (**ISIE**), Taipei, Taiwan, May 28–31, **2013**.

## CURRICULUM VITA

24. "Single-Inductor Multiple-Output Power-Supply ICs," IEEE International **NEWCAS** Conference, Paris, France, Jun. 16–19, **2013**.
25. "Designing Bandgap-Voltage References," IEEE Faible Tension Faible Consommation (**FTFC**), Paris, France, Jun. 20–21, **2013**.
26. "Power Electronic Interfaces for Energy Harvesters," **PowerMEMS 2013**, London, England, Dec. 2–6, **2013**.
27. "Energizing Wireless Microsensors," **International Forum on Green Energy Electronics**, National Taiwan University of Science and Technology (**NTUST**), Taipei, Taiwan, July 28, **2014**.
28. "Energizing Wireless Microsensors," **International Forum on Green Energy Electronics**, National Cheng Kung University (**NCKU**), Tainan, Taiwan, July 29, **2014**.
29. "Tiny DC-Sourced Single-Inductor Charger–Supply ICs," IEEE Midwest Symposium on Circuits and Systems (**MWSCAS**), College Station, Texas, Aug. 3–6, **2014**.
30. "Miniaturized Energy-Harvesting Piezoelectric Chargers," IEEE Custom Integrated Circuits Conference (**CICC**), San Jose, California, Sep. 15–17, **2014**.
31. "Miniaturized Energy-Harvesting Piezoelectric Chargers," IEEE International Symposium on Integrated Circuits (**ISIC**), Singapore, Dec. 10–12, **2014**.
32. "Powering Microsystems," IEEE International Symposium on Quality Electronic Design (**ISQED**), Santa Clara, California, March 2–4, **2015**.
33. "Energy-Harvesting Microsystems," IEEE Very Large-Scale Integration Design, Automation and Test (**VLSI-DAT**), Hsinchu, Taiwan, Apr. 27–29, **2015**.
34. "Miniaturized Energy-Harvesting Piezoelectric Chargers," IEEE International Symposium on Circuits and Systems (**ISCAS**), Lisbon, Portugal, May 24–27, **2015**.
35. "Miniaturized Energy-Harvesting Piezoelectric Chargers," IEEE International Symposium on Industrial Electronics (**ISIE**), Rio de Janeiro, Brazil, June 3–5, **2015**.
36. "Tiny DC-Sourced Single-Inductor Charger–Supply ICs," IEEE International System-on-Chip Conference (**SOCC**), Beijing, China, Sept. 8–11, **2015**.
37. "Tiny Inductively Powered Battery Chargers," IEEE International **NEWCAS** Conference, Vancouver, Canada, June 26–29, **2016**.
38. "Tiny Inductively Powered Battery Chargers," IEEE Asia Pacific Conference on Circuits and Systems (**APCCAS**), Jeju, South Korea, Oct. 25–28, **2016**.
39. "Low-Dropout Regulator ICs – From the Ground Up," IEEE International Conference on Electronics Circuits and Systems (**ICECS**), Monte Carlo, Monaco, Dec. 11–14, **2016**.
40. "Tiny Energy-Harvesting Piezoelectric Chargers," IEEE International Conference on Industrial Technology (**ICIT**), Toronto, Canada, Mar. 22–25, **2017**.
41. "Light-Harvesting Photovoltaic Charger–Supplies," IEEE Canadian Conference on Electrical and Computer Engineering (**CCECE**), Windsor, Canada, Apr. 30–May 3, **2017**.
42. "Tiny Inductively Powered Battery Chargers," IEEE International Symposium on Industrial Electronics (**ISIE**), Edinburgh, Scotland, Jun. 19–21, **2017**.
43. "Tiny Light-Harvesting Photovoltaic Charger–Supplies," IEEE/ACM International Symposium on Low Power Electronics and Design (**ISLPED**), Taipei, Taiwan, Jul. 24–26, **2017**.

### Invited Research Seminar Presentations:

1. "Integrated Power Management Circuits." National Semiconductor Corporation (**NSC**), Santa Clara, Dec. **2002**.
2. "Power Conscious ICs." **Texas A&M University**, College Station, Jun. 21, **2004**.
3. "El Mundo es Análogo, y las Oportunidades son Muchas." **University of Puerto Rico** at Mayagüez, Oct. 18, **2004**.
4. "Hybrid Fuel Cell/Lithium-Ion Powered, Power Conscious ICs." National Semiconductor Corporation (**NSC**), Santa Clara, Jun. **2005**.
5. "Microsystems: Power and Energy." Army Research Lab (**ARL**) Advanced Microsystems Workshop, Virginia, Jan. 30, **2006**.
6. "Self-Sustaining, Self-Powered, Energy and Power Conscious ICs for Micro-Scale Devices," **Universitat Politècnica de Catalunya**, Barcelona, Spain, Jul. 10, **2006**.

## CURRICULUM VITA

7. "Self-Powered, Self-Sustaining System-on-Chip (SoC) and System-in-Package (SiP) Power Solutions," *National Science Foundation (NSF) and Intelligence Community Workshop on Micro-Scale Power Sources*, Langley, Virginia, Apr. 24-25, **2007**.
8. "Powering Micro-Systems," National Semiconductor Corporation (NSC), Santa Clara, California, Nov. 30, **2007**.
9. "AC Design and Performance of Low-Dropout Regulators (LDOs)," **Texas A&M University** at College Station, Jun. 9, **2008**.
10. "Power Losses in Switching DC-DC Converter ICs," **Texas A&M University** at College Station, Jun. 9, **2008**.
11. "Powering Micro-Systems," **Shanghai Jiao Tong University**, Shanghai, China, Oct. 8, **2008**.
12. "Powering Micro-Systems," Linear Technology Corporation (LTC), San Jose, California, Feb. 13, **2009**.
13. "Powering Micro-Systems," *IEEE CASS Distinguished Lecture*, Montreal, Canada, Jul. 17, **2009**.
14. "Energizing and Powering Microsystems," *IEEE Electron Device Society Chapter*, Vancouver, Canada, Sept. 24, **2009**.
15. "Harvesting Ambient Energy in Miniaturized Systems," *Energy and Power Analog Circuit Challenges Workshop*, SRC **Texas Analog Center of Excellence**, Dallas, Texas, Sept. 28, **2009**.
16. "Single-Inductor Multiple-Output Switching DC-DC Converters," **Inha University**, Incheon, Korea, Nov. 19, **2009**.
17. "Single-Inductor Multiple-Output Switching DC-DC Converters," **Samsung**, Seoul, Korea, Nov. 20, **2009**.
18. "Power Management ICs for Portable Devices," **University of Seoul**, Korea, Feb. 17, **2010**.
19. "Energizing and Powering Microsystems," Electronics and Telecommunications Research Institute (ETRI), Daejeon, Korea, Feb. 18, **2010**.
20. "Power Management ICs for Portable Devices," **Silicon Works Co.-Daejeon**, Korea, Feb. 18, **2010**.
21. "Energizing and Powering Microsystems," Korea Advanced Institute of Science and Technology (KAIST), Daejeon, Korea, Feb. 19, **2010**.
22. "Harvesting Ambient Energy in Miniaturized Systems," *IT Convergence Research Project Workshop* at **KAIST**, Daejeon, Korea, Feb. 19, **2010**.
23. "Harvesting Kinetic Energy in Miniaturized Systems," *IEEE's CASS Distinguished Lecture*, Montreal, Canada, Sept. 17, **2010**.
24. "Energizing and Powering Microsystems," Texas Instruments (TI), Dallas, Texas, Oct. 25, **2010**.
25. "AC Design and Performance of Low-Dropout Regulators (LDOs)," *IEEE's CASS Distinguished Lecture*, Mayagüez, Puerto Rico, Nov. 12, **2010**.
26. "Harvesting Kinetic Energy in Miniaturized Systems," National Taiwan University (NTU), Taipei, Taiwan, Nov. 23, **2010**.
27. "Energizing and Powering Microsystems," *IEEE SSCS Hsinchu Chapter*, Taiwan, Nov. 24, **2010**.
28. "AC Design and Performance of Low Dropout Regulators," *IEEE CASS Distinguished Lecture*, Tainan, Taiwan, Nov. 26, **2010**.
29. "Power Losses in Switching DC-DC Converter ICs," National Cheng Kung University (NCKU), Tainan, Taiwan, Nov. 26, **2010**.
30. "Energy-Harnessing ICs," National Semiconductor Corporation (NSC), Santa Clara, California, July 15, **2011**.
31. "AC Design and Performance of LDOs," *IEEE CASS Taipei Chapter*, Hsinchu, Taiwan, Sept. 5, **2011**.
32. "Energy-Harnessing ICs," ON Semiconductor (ON), Phoenix, Arizona, Oct. 4, **2011**.
33. "Energy-Harnessing ICs," Texas Instruments (TI), Dallas, Texas, Oct. 5, **2011**.
34. "Energy-Harnessing ICs," *IEEE Industrial Electronics, Power Electronics, and Industry Applications Society Taipei Chapters* in National Tsing Hua University, Hsinchu, Taiwan, Dec. 9, **2011**.
35. "Frequency Response of Switching DC-DC Converters," National Cheng Kung University (NCKU), Tainan, Taiwan, Dec. 14, **2011**.
36. "Feedback Control of Switching DC-DC Converters," National Cheng Kung University (NCKU), Tainan, Taiwan, Dec. 14, **2011**.
37. "Energy-Harnessing Microchips," *IEEE Power Electronics Society (PELS) and IEEE Life Members' Chapters*, Atlanta, Georgia, Mar. 28, **2012**.
38. "Harvesting Kinetic Energy in Miniaturized Systems," Texas Instruments (TI), Dallas, Texas, Jun. 5, **2012**.

## CURRICULUM VITA

39. "Harnessing Ambient Energy with Integrated Circuits," National Cheng Kung University (NCKU), Tainan, Taiwan, Dec. 6, **2012**.
40. "Design of High-Performance Low-Dropout Regulator ICs," National Cheng Kung University (NCKU) and IEEE Circuits and Systems Society (CAS), Tainan, Taiwan, Dec. 17, **2013**.
41. "Designing Tiny DC-Sourced Single-Inductor Charger–Supply ICs," National Cheng Kung University (NCKU), IEEE Circuits and Systems Society (CAS), and IEEE Solid-State Circuits Society (SSCS), Tainan, Taiwan, Dec. 18, **2013**.
42. "Designing Tiny Energy-Harvesting Piezoelectric Chargers," IEEE Circuits and Systems Society (CAS) in **National Sun Yat-Sen University**, Kaohsiung, Taiwan, Dec. 19, **2013**.
43. "Designing Tiny Energy-Harvesting Piezoelectric Chargers," IEEE Industrial Electronics Society (IE) and IEEE Power Electronics Society (PELS) at National Taiwan University of Science and Technology (NTUST), Taipei, Taiwan, Dec. 20, **2013**.
44. "Energizing and Powering Microsystems," Texas Instruments (TI), Dallas, Texas, May 8, **2014**.
45. "Power-Supply Rejection in Amplifiers and LDOs," National Cheng Kung University (NCKU), Tainan, Taiwan, Nov. 19, **2014**.
46. "Miniaturized Energy-Harvesting Piezoelectric Chargers," National Cheng Kung University (NCKU), Tainan, Taiwan, Nov. 20, **2014**.
47. "Powering Microsensors," Department of Energy (DoE), National Security Campus, Kansas City, Feb. 6, **2015**.
48. "Tiny and Distant Inductively-Powered Battery Chargers," National Cheng Kung University (NCKU), Tainan IEEE Solid-State Circuits and Circuits and System Society Chapters (SSCS and CASS), Tainan, Taiwan, Nov. 19, **2015**.
49. "Energizing and Powering Microsystems," Department of Energy (DoE), National Security Campus, Albuquerque, Jan. 7, **2016**.
50. "Energizing and Powering Microsystems," MediaTek-NTU Distinguished Professor Talk, Nanyang Technological University (NTU), Singapore, Feb. 29, **2016**.
51. "Energizing and Powering Microsystems," Khalifa University, Abu Dhabi, United Arab Emirates, May 2, **2016**.
52. "Energizing and Powering Microsystems," Texas Instruments, Dallas, Texas, June 10, **2016**.
53. "Energizing and Powering Microsystems," KTH Royal Institute of Technology, Stockholm, Sweden, June 9, **2017**.

### Invited Keynote and Plenary Speeches:

1. "Orgullo Hispano," **Robins Air Force Base**, Sept. 23, **2003**.
2. "Robins AFB Hispanic Heritage Luncheon," **Robins Air Force Base**, Oct. 3, **2005**.
3. "Energy and Power Management Trends," **Analog Leaders Forum**, Seoul, Korea, Oct. 16, **2009**.
4. "Energizing and Powering Microsystems," IEEE's International System-on-Chip Design Conference (ISOCC), Busan, Korea, Nov. 23, **2009**.
5. "Energy-Harvesting ICs – The State of the Art," IEEE Circuits and Systems for Medical and Environmental Applications (CASME), Mérida, Mexico, Dec. 13–15, **2010**.
6. "Powering Wireless Microsensors," Semiconductor Equipment and Material International Conference (SEMICON), Seoul, Korea, Feb. 12–14, **2013**.
7. "Non-academic Routes Beyond the Engineering Ph.D.," **Sloan Foundation**, Atlanta, Georgia, Apr. 14, **2014**.
8. "Powering Microsystems – From Fuel Cells to Ambient Energy," IEEE International Conference on Microelectronics (ICM), Doha, Qatar, Dec. 14–17, **2014**.
9. "Microwatt CMOS Harvesters," Infrared Radiation, Thermoelectricity and Chaos Workshop, U.S. Office of Naval Research (ONR) in James Madison University, Harrisonburg, Virginia, Jun. 17, **2015**.
10. "Higher Education, Success, and Life in Electrical Engineering," Tainan IEEE Solid-State Circuits and Circuits and System Society Chapters (SSCS and CASS), Tainan, Taiwan, Nov. 19, **2015**.
11. "Powering Intelligent IoT Microsensors," IEEE International System-on-Chip Design Conference (ISOCC), Jeju, Korea, Oct. 25, **2016**.
12. "Life, Happiness, Success, and Higher Education in Electrical Engineering," Tainan IEEE Solid-State Circuits and Circuits and System Society Chapters (SSCS and CASS), Tainan, Taiwan, Nov. 21, **2016**.
13. "Energy-Harvesting IoT Microsensors," **CMOS Emerging Technologies**, Warsaw, Poland, Mar. 28–30, **2017**.

### Expert Panelist (Invited):

## CURRICULUM VITA

1. Expert Panelist for "Power Management for SoCs," *IEEE VLSI Symposium*, Hawaii, Jun. 15-17, **2006**.

### Refereed Non-Engineering Publications:

1. G.A. Rincón-Mora, "Strawberry Delight" [poem], *Forgotten Moments* (ISBN: 1-58235-159-7), Editor's Choice Award, **2000**.
2. G.A. Rincón-Mora, "Ojitos Verdes" [poem], *Nuevo Impacto*, Oct. **2002**.
3. G.A. Rincón-Mora, "A Christmas Tale" [short story], *ISB Cafe* ([www.InternationalStoryBook.com](http://www.InternationalStoryBook.com)), Dec. **2002**.
4. G.A. Rincón-Mora, "Mi Querida Daniela" [short story], *Shades Of Romance Magazine (SORM)* (<http://www.sormag.com>), Jan.–Feb. **2003**.
5. G.A. Rincón-Mora, "Mi Querida Daniela" [short story], *ECESIS* (<http://www.ece.gatech.edu/ecesis/>), Spring **2004**.
6. G.A. Rincón-Mora, "Flor Andina" [poem], *Nuevo Impacto*, Jul. **2004**.
7. G.A. Rincón-Mora, "The Bund" [photograph] - *Chapter & Verse - A Publication of the Hong Kong International Literary Festival Ltd.*, **2004**.
8. G.A. Rincón-Mora, "Little Lots" [poem], *ECESIS* (<http://www.ece.gatech.edu/ecesis/>), Spring **2005**.
9. G.A. Rincón-Mora, "Just passing through (Island of Idra)" [photograph] – *Photographers of Greece* ([http://grecja.home.pl/eng/efotograficy\\_grecji.htm](http://grecja.home.pl/eng/efotograficy_grecji.htm)).
10. G.A. Rincón-Mora, "Working and Teaching in Tanzania," *Volunteers for Peace (VFP) - Tanzania* (<http://www.vfpcanada.org/Tanzania.pdf>), Nov. **2005**.
11. G.A. Rincón-Mora, "Let me...let me in there! (Mwanga, Tanzania)," *Volunteers for Peace - VFP 2006 Newsletter* (<http://www.vfp.org/2006NL.htm>), Jan. **2006**.
12. G.A. Rincón-Mora, "Home!" [photograph] - *ECESIS* (<http://www.ece.gatech.edu/ecesis/>), Spring **2006**.
13. G.A. Rincón-Mora, "Mind and Heart" [poem] - *ECESIS* (<http://www.ece.gatech.edu/ecesis/>), Spring **2007**.
14. M. Cheng, G.A. Rincón-Mora, and G. Heaney, "Volunteers For Peace - Vermont non-profit serving the world," *Wishtank, Journal of Intellectual Freedom*, Jun. **2007**.
15. G.A. Rincón-Mora, "The Girl in White" [poem] - *ECESIS* (<http://www.ece.gatech.edu/ecesis/>), Spring **2008**.

### Artistic Performances:

1. **St. Rita's** Contemporary Choir in Dallas, Texas. Tenor in *A Festival of Nine Lessons and Carols* (Concert), Dec. **2000**.
2. **Alpharetta Chamber Singers** in Alpharetta, Georgia. Tenor in *The American Folk Spirit: A Concert of Folk Songs, Hymns, and Spirituals* (Concert), Nov. **2001**.
3. **Alpharetta Chamber Singers** in Alpharetta, Georgia. Tenor in *Refresh of 2002: Five Services of Preaching and Proclamation* (Concert), Jan. **2002**.
4. **Christ the King Theatre Ministry** in Atlanta, Georgia. Cast as Zebulon in *Joseph and the Amazing Technicolor Dreamcoat* (Musical) Apr. **2002**.
5. **Cobb Playhouse** (Little General) in Marietta, Georgia. Cast as Bernardo in *West Side Story* (Musical), Sept. **2002**.
6. **Holy Spirit's** Traditional Choir in Atlanta, Georgia. Tenor in *Mass of Remembrance* (Concert: Requiem by Gabriel Fauré), Nov. **2002**.
7. **Alpharetta Chamber Singers** in Alpharetta, Georgia. Tenor in *Expressions of Praise* (Concert), Nov. **2002**.
8. **Gwinnett County Seat Players** in Lawrenceville, Georgia. Cast as Homer Smith in *Lilies of the Field*, Feb. **2003**.
9. **Alpharetta Chamber Singers** in Alpharetta, Georgia. Tenor in *Mass of a New Millennium* (Concert), Apr. **2003**.
10. **Christ the King Theatre Ministry** in Atlanta, Georgia. Cast as Soldier in the *Sound of Music* (Musical), Apr. **2003**.
11. **Alpharetta Chamber Singers** in Alpharetta, Georgia. Tenor in *Sing a Song of Shakespeare* (Concert), May **2003**.
12. **Archdiocesan Festival Choir** in Atlanta, Georgia. Tenor in *Magnificat* (Concert), May **2003**.
13. **Marietta First Methodist Church** in Marietta, Georgia. Cast as Sid Philips in *Singin' in the Rain* (Musical), Jul. **2003**.
14. **Art Farm** in Cabbage Town, Georgia. Cast as Jimmy Luv in *Snuff Darlings from Dahlonaga*, Aug. **2003**.
15. **Neighborhood Playhouse** in Decatur, Georgia. Cast as Henry Steward in *Civil War* (Musical), Sept. **2003**.
16. **Alpharetta Chamber Singers** in Alpharetta, Georgia. Tenor in *Wonder Tidings* (Concert), Dec. **2003**.
17. **Theatre Arts Guild** in Clarkston, Georgia. Cast as Fred Casely in *Chicago* (Musical), Feb. **2004**.
18. **Aurora Theatre** in Duluth, Georgia. Cast as Soldier, Guard, and Servant in *Life is a Dream/La Vida es Sueño* (performed in English and Spanish), Apr. **2004**.



## CURRICULUM VITA

19. **Corpus Christi Theatre Ministry** in Stone Mountain, Georgia. Cast as Paco (Muleteer) in *Man of La Mancha* (Musical), Oct. **2004**.
20. **ACT 1** in Alpharetta, Georgia. Cast as Tommy Keeler in *Annie Get Your Gun* (Musical), Nov.–Dec. **2004**.
21. **Theatre in the Square** in Marietta, Georgia. Cast as Martinez in *Take Me Out*, Mar.–Apr. **2005**.
22. **Christ the King Theatre Ministry** in Atlanta, Georgia. Cast as Ali Hakim in *Oklahoma* (Musical), Mar.–Apr. **2006**.
23. **Stage 2 Players** in Roswell, Georgia. Cast as Boxhall in *Titanic* (Musical), Oct. **2006**.
24. **Theatre Arts Guild** in Clarkston, Georgia. Cast as Roberto Nuñez and Charlie Blossom in *Working* (Musical), Oct.–Nov. **2006**.
25. Tenor in **St. Rita's** Contemporary Choir in Dallas, Texas, **1999–2001**.
26. Tenor in **Alpharetta Chamber Singers** in Alpharetta, Georgia, **2001–2004**.
27. Tenor in **Holy Spirit's** Traditional Choir in Atlanta, Georgia, **2001–2004**.
28. Tenor in **Archdiocesan Festival Choir** in Atlanta, Georgia, **2002–2004**.
29. Tenor in **Sacred Heart's** Spanish Choir in Atlanta, Georgia, **2004–2005**.

### IV. Teaching

#### Ph.D. Students Graduated:

1. **Biranchi Sahu**, *Dynamically Adaptive Supplies for Linear RF Power Amplifiers*, Ph.D. Dec. **2004**.
2. **Pooya Forghani**, *Lossless Current-Sensor IC for Switching DC-DC Converters*, Ph.D. Jun. 1, **2006**.
3. **Vishal Gupta**, *An Accurate, Trimless, High PSRR, Low-Voltage, CMOS Reference IC*, Ph.D. Jul. 3, **2007**.
4. **Neeraj Keskar**, *High-Bandwidth, Wide LC- $R_{ESR}$  Compliant  $\Sigma\Delta$  Boost DC-DC Converters*, Ph.D., Mar. 24, **2008**.
5. **Erick O. Torres**, *An Electrostatic CMOS/BiCMOS Vibration-Based Harvester-Charger IC*, Ph.D., May 4, **2010**.
6. **Dongwon Kwon**, *Piezoelectric Kinetic Energy-Harvesting ICs*, Ph.D., Mar. 4, **2013**.
7. **Orlando Lazaro**, *CMOS Inductively Coupled Power Receiver for Wireless Microsensors*, Ph.D., Apr. 2, **2014**.
8. **Suhwan Kim**, *Mixed-Source Charger–Supply CMOS IC*, Ph.D., Apr. 14, **2014**.
9. **Andres Arturo Blanco**, *Fast-Waking and Low-Voltage Thermoelectric and Photovoltaic CMOS Chargers for Energy-Harvesting Wireless Microsensors*, Ph.D., July 14, **2017**.

#### Master Students Advised:

1. **Mark Guildersleeve**, *Low Voltage Power Saving Techniques for DC-DC Converters*, M.S.E.E., Aug. **2002**.
2. **Abbas Poonawala**, *Precision, Low-Voltage, Integrated Capacitor Multipliers*, M.S.E.E., Dec. **2003**.
3. **Aditya Makharia**, *Inductorless DC-DC Converters for Portable Applications*, M.S.E.E., Dec. **2003**.
4. **Oscar Palomino**, M.S.E.E., Dec. **2007**.
5. **Amisha Manek**, M.S.E.E., Dec. **2008**.
6. **Justin Vogt**, *nW Analog-Digital Converter for Blood-Glucose Monitors*, M.S.E.E., Dec. **2008**.
7. **Amit Patel**, Thesis: "High PSR Low Dropout Regulator ICs," M.S.E.E., May **2009**.
8. **Priyanka Lakhe**, M.S., May **2010**.
9. **Luke Milner**, M.S., May **2010**.
10. **Tim Guglielmo**, M.S., May **2011**.
11. **José Vidal**, M.S., May **2011**.
12. **Joshua Cowan**, M.S., Dec. **2013**.
13. **Jun-Yang Lei**, M.S., Dec. **2014**.
14. **Amy Wilson**, *Electrostatic Energy-Harvesting CMOS Charger ICs*, M.S., Dec. **2017**.

#### Ph.D. Students Currently Advised:

1. **Rajiv Damodaran**, *Light-Harvesting ICs for Miniaturized Photovoltaic CMOS Cells*.  
Start: Fall '09, Preliminary Exam: Spring '09, Proposal Exam: Summer '15.
2. **Carlos Javier Solís**, *Single-Inductor, Multiple-Output Switching CMOS Supplies*.  
Start: Fall '10, Preliminary Exam: Fall '10, Proposal Exam: Spring '16.

## CURRICULUM VITA

3. **Nan Xing**, *Wireless Inductively Coupled Charger–Supply ICs*.  
Start: Summer '14, Preliminary Exam: Fall '13.
4. **Siyu Yang**, *Piezoelectric Energy-Harvesting CMOS Charger–Supply ICs*.  
Start: Spring '15, Preliminary Exam: Fall '13.
5. **Devon Janke**, *Single-Inductor Multiple-Output CMOS Power Supply*.  
Start: Spring '17, Preliminary Exam: Fall '15.
6. **Tianyu Chang**, *Low-Voltage Starters for DC-Sourced Energy Harvesters*.  
Start: Spring '17.

### Undergraduate Students Advised:

1. **R. Dokania** (Intern from India: Summer '02), *Cancellation of Load Regulation in Low Drop-out Regulators*.
2. **K. Dash** (Intern from India: Summer '03), *Active Bulk Capacitor Multipliers*.
3. **Carlos Cubero Ponce** (Intern from University of Puerto Rico: Summer '05), *Drain Follower Buffer*.
4. **Freddie Alequín Ramos** (Intern from University of Puerto Rico: Summer '07), *System-in-Package Integration*.
5. **LaVonda Brown** (Intern from Norfolk: Summer '08), *Piezoelectric Modeling*.
6. **Adilson Cardoso** (Georgia Tech: Fall '06–Fall '07).

### Visiting Scholars Advised:

1. **H.I. Pan** (Ph.D. student from University of Taiwan in Taipei, Taiwan: Jan. to Dec. 2005), *Asynchronous Power-Tracking Supplies for RF PAs*.

### Course Developed (at Georgia Tech):

**ECE 6445** – *Power IC Design* (first developed and offered in Fall '09): Model, analyze, and design power-supply ICs.

### Professional Four-day Short Courses Developed:

**Analog IC Design – An Intuitive Approach** (first developed and offered at Georgia Tech Global Learning and Conference Center in Sept. 26–30, 2005): Model, analyze, and design analog ICs.

**Power IC Design – From the Ground Up** (first developed and offered in Bratislava, Slovakia, for ON Semiconductor in Dec. 12–15, 2005): Survey, model, analyze, and design power-management and -conditioning ICs.

### Courses Taught (at Georgia Tech):

1. **ECE 3040** – *Microelectronic Circuits*: Spring '02, '03.
2. **ECE 3050** – *Analog Electronics*: Fall '01, '02, '03, '04, '05, '06, '07, '10, Spring '05, '06.
3. **ECE 4430** – *Analog Integrated Circuits*: Fall '02, '03.
4. **ECE 6412** – *Analog IC Design*: Spring '04, '07, '08, '09, '10, '11, '12, '13, '14, '15, '16; for GT-Shanghai in Fall '08, '11, '13; and for GT-Shenzhen in Fall '14, '15.
5. **ECE 6445** – *Power IC Design*: Summer '15; For GT-Shanghai in Fall '09, '12; For GT-Shenzhen in Fall '15.

## V. Service

### Professional Leadership:

1. **Chapter Vice-Chair**, Atlanta's IEEE Solid-State Circuits and Circuits and Systems Society (**SSCS–CASS**), **2003–2004**.
2. **Chapter Chair**, Atlanta's IEEE Solid-State Circuits and Circuits and Systems Society (**SSCS–CASS**), **2004–2011**.
3. **Technical Program Co-Chair**, IEEE Midwest Symposium on Circuits and Systems (**MWSCAS**), Puerto Rico, **2006**.
4. **Technical Program Chair**, Joint IEEE 50<sup>th</sup> Midwest Symposium on Circuits and Systems (**MWSCAS**) and 5<sup>th</sup> IEEE International NEWCAS Conference, Montreal, **2007**.
5. **Circuit Design Vice Chair**, IEEE International Caribbean Conference on Devices, Circuits and Systems (**ICCDCS**), Cancun, Mexico, **2008**.
6. **General Chair**, Energy and Power Integrated Circuits Workshop, **SRC Texas Analog Center of Excellence (TxACE)**, Sept. 28-29, **2009**.
7. **Special Session Co-Organizer**, "Emerging Energy and Power Integrated Circuits," IEEE International Symposium on Circuits and Systems (**ISCAS**), Rio de Janeiro, Brazil, May **2011**.
8. **Technical Program Co-Chair**, IEEE International System-on-Chip Conference (**ISOCC**), Jeju, Korea, Nov. **2011**.
9. **Advisory Panel**, IEEE International Conference on Power Electronics and Energy Systems (PEES), Chitkara, India, **2012**.
10. **Technical Program Committee**, IEEE Faible Tension Faible Consommation (FTFC), Paris, France, **2013–2014**.

## CURRICULUM VITA

11. **International Advisory Board**, IEEE International Conference on Power Electronics and Drive Systems (PEDS), Kitakyushu City, Japan, **2013**.
12. **General Co-Chair**, IEEE International System-on-Chip Conference (**ISOCC**), Busan, Korea, Nov. **2013**.
13. **General Vice Chair**, IEEE International System-on-Chip Conference (**ISOCC**), Jeju Island, Korea, Nov. **2014**.
14. **International Steering Committee**, International Future Energy Electronics Conference (**IFEEC**), Taipei, Taiwan, Nov. **2015**.
15. **Technical Program Chair**, IEEE International Symposium on Circuits and Systems (**ISCAS**), Montreal, Canada, May **2016**.
16. **International Liaison**, IEEE Conference on Design of Circuits and Integrated Systems (**DCIC**), Barcelona, España, Nov. **2017**.
17. **Technical Program Co-Chair**, IEEE International Symposium on Circuits and Systems (**ISCAS**), Sapporo, Japan, May **2019**.

### Editorial Boards:

1. **Guest Co-Editor**, *Analog Integrated Circuits and Signal Processing Journal (AICSP)*, Special Issue on Analog and RF, Aug. **2009**.
2. **Associate Editor**, *IEEE Transactions on Circuits and Systems II (TCAS II)*, **2007–2009** and **2010–2011**.
3. **Associate Editor**, *IEEE Journal of Solid-State Circuits (JSSC)*, **2011**.
4. **Editorial Board Member**, *Journal of Low-Power Electronics (JOLPE)*, **since 2009**.
5. **Guest Editor**, *IEEE Transactions on Circuits and Systems II (TCAS II)*, Special Issue on Energy Harvesting, Dec. **2011**.

### Committee Membership:

1. **Technical Program Committee**, IEEE Southwest Symposium on Mixed-Signal Design (**SSMSD**), **2002**.
2. **Selection Committee Review Panel**, NSF SBIR/STTR Committee on Power Management, Mar. **2003**.
3. **Selection Committee Review Panel**, NSF SBIR/STTR Committee on Power Management, Sept. **2003**.
4. **Technical Committee**, IEEE CASS Analog Signal Processing (**ASP**), **since 2003**.
5. **Selection Committee Review Panel**, NSF SBIR/STTR Committee on Signal Processing & IC Design, Oct. **2004**.
6. **Selection Committee Review Panel**, NSF SBIR/STTR Committee on IC Design: Testing, Aug. **2005**.
7. **Steering Committee**, IEEE Midwest Symposium on Circuits and Systems (**MWSCAS**), **since 2006**.
8. **Selection Committee Review Panel**, NSF SBIR/STTR Committee on IC Design I, Feb. **2007**.
9. **Technical Committee**, IEEE CASS Power and Energy Circuits and Systems (**PECAS**), **since 2009**.
10. **Fellows Evaluation Committee**, IEEE Circuits and Systems Society (**CASS**), **2011** and **2014**.
11. **Distinguished Lecturer Committee**, IEEE Circuits and Systems Society (**CASS**), **2011–2013**.
12. **Industrial Advisory Committee**, IEEE Circuits and Systems Society (**CASS**), **2012–2013**, **2014–2015**.
13. **Steering Committee**, IEEE International Symposium on Circuits and Systems (**ISCAS**), **since 2016**.

### Professional Membership:

1. Institute of Electrical and Electronics Engineers (**IEEE**), Student '90, Member '97, Senior Member '01, and **Fellow** '11 (less than 1% of IEEE members earn rank of Fellow).
2. Institution of Engineering and Technology (**IET/IEE**), Member '06 and **Fellow** '09.
3. Society of Hispanic Professional Engineers (**SHPE**), **Life Member** '00.

### International Ph.D. Committee:

1. [**Rapporteur, Jury**] Vincent Telandro, *On-Chip Voltage Regulator Protecting Against Power Analysis Attacks*, Laboratoire Matériaux et Microélectronique de Provence, Institut Supérieur d'Electronique du Nord, France, Nov. 2007.
2. [**External Examiner & Chair**] Mohammad Radwan Alhawari, *Multi-Source Energy-Harvesting Interface Circuits for Biomedical Wearable Electronics*, Khalifa University, Abu Dhabi, United Arab Emirates, May 2, 2016.
3. [**Examiner, Opponent**] Janko Katic, *Efficient Energy Harvesting Interfaces for Implantable Applications*, KTH Royal Institute of Technology, Stockholm, Sweden, June 9, 2017.

### Georgia Tech Service:

1. ECE **Graduate Student Recruitment Committee**, Member, **2001–2003** and **2004–2005**.

## CURRICULUM VITA

2. ECE Student–Faculty Committee, Member, 2003–2004, 2005–2008, and 2011–2012; Chair, 2008–2011.
3. ECE Student Award Committee, Member, 2006.
4. ECE Georgia Power Distinguished Professor Search Committee, Member, 2006.
5. Freshmen Partner for Freshmen Partnership Program, 2006.
6. Outstanding Electrical and Computer Engineering Senior Student Awards Committee, 2009, 2010, and 2011.
7. ECE Student Awards Committee, 2010.
8. ECE Electronic Design and Applications (EDA) Technical Interest Group (TIG) Chair, 2013–2014, 2014–2015, 2015–2016 and Faculty Recruitment Representative in 2016–2017.
9. ECE Course Content Review Panel for ECE 3040, Member, 2015.
10. Graduate Student Committees:

	Student	Proposal Committee	Reading Committee	Defense Committee	Degree
1	Sidharth Dalmia	Chair: 3/14/02			Ph.D.
2	Zhiwei Dong		Member: 7/15/02	Member: 7/15/02	Ph.D.
3	Theocharis Boukas	Chair: 8/12/02		Member: 03/26/03	Ph.D.
4	Susanta Sengupta	Member: 4/15/02	Member: 07/08/04	Member: 07/08/04	Ph.D.
5	Kyu-won Choi	Chair: 10/29/02	Member: 09/09/03	Member: 09/09/03	Ph.D.
6	Woopoung Kim	Chair: 4/30/03			Ph.D.
7	Biranchinath Sahu	Advisor: 3/24/04	Chair: 11/4/04	Chair: 11/4/04	Ph.D.
8	Bhyrav Mutnury	Member: 1/28/05			Ph.D.
9	Pooya Forghani	Advisor: 6/24/04	Chair: 6/1/06	Chair: 6/1/06	Ph.D.
10	Vishal Gupta	Advisor: 9/20/05	Chair: 7/3/07	Chair: 7/3/07	Ph.D.
11	Neeraj Keskar	Advisor: 9/20/05	Chair: 3/24/08	Chair: 3/24/08	Ph.D.
12	Jau-Horng Chen	Chair: 9/22/05	Member: 5/25/06	Member: 06/30/06	Ph.D.
13	Soumendu Bhattacharya			Member: 06/23/05	Ph.D.
14	Jacob Minz	Member: 10/19/05		Member: 07/19/06	Ph.D.
15	Shruti Prakash	Member: 7/27/06	Member: 03/04/09	Member: 03/04/09	Ph.D.
16	Kenta Nakayashiki	Member: 9/28/06	Member: 10/2/07	Member: 10/2/07	Ph.D.
17	Ripal Nathuji	Member: 5/2/07			Ph.D.
18	Rajeswari Chandrasekaran	Member: 08/22/07		Member: 7/15/10	Ph.D.
19	David Pritchett	Member: 12/13/07		Member: 2/4/09	Ph.D.
20	N. Lalgudi Subramanian	Member: 1/17/07		Member: 3/26/08	Ph.D.
21	Erick Torres	Advisor: 4/9/08	Chair: 5/4/10	Chair: 5/4/10	Ph.D.
22	Krishna Bharath	Member: 4/21/08			Ph.D.
23	Muhammad Nisar	Member: 7/30/08			Ph.D.
24	Dale Scott Douglas		Member: Fall 08		M.S.
25	Tahir Zaidi	Member: 6/1/09			Ph.D.
26	Luke Milner	Advisor: 7/15/09			Ph.D.
27	Suhwan Kim	Advisor: 3/13/11	Chair: 4/14/14	Chair: 4/14/14	Ph.D.
28	Sang Taek Han	Member: 3/3/11			Ph.D.
29	Mauricio Pardo Gonzalez	Chair: 4/29/11	Member: 1/18/12	Member: 1/18/12	Ph.D.
30	Debrup Das	Member: 6/1/11			Ph.D.
31	Dongwon Kwon	Advisor: 7/21/11	Chair: 3/4/13	Chair: 3/4/13	Ph.D.
32	Hengzhao Yang	Member: 4/24/12		Member: 5/2/13	Ph.D.
33	Hakan Toreyin	Member: 11/25/12			Ph.D.
34	Orlando Lazaro	Advisor: 7/23/12	Chair: 4/2/14	Chair: 4/2/14	Ph.D.
35	Chris Valenta	Member: 7/25/12		Member: 6/25/14	Ph.D.
36	Jae Won Shim	Member: 3/13/13			Ph.D.
37	Yaesuk Jeong	Member: 7/24/14	Member: 5/11/17	Member: 5/11/17	Ph.D.
38	Andres Blanco	Advisor: 8/21/14	Chair: 7/14/18	Chair: 7/14/18	Ph.D.
39	Rajiv Damodaran	Advisor: 6/22/15			Ph.D.
40	Carlos Solis	Advisor: 1/27/16			Ph.D.
41	Jaemyum Lim	Chair: 3/16/16	Member: 3/16/17	Member: 3/16/17	Ph.D.

### Community Service:

## CURRICULUM VITA

1. **Volunteers for Peace (VFP)**, Kigonigoni, **Tanzania** (school and levy construction), Summer (2 weeks) 2006.
2. **Volunteers for Peace (VFP)**, Bangalore, **India** (teach children with AIDS and disabilities), Summer (2w) 2008.
3. **Service Civil International (SCI)**, Ulaan Baatar, **Mongolia** (construction and farming at orphanage), Summer (2w) 2009.
4. **Service Civil International (SCI)** in Viet Tri, **Vietnam** (teach orphans English), Summer (2w) 2010.

### VI. Honors, Awards, and Visibility

#### Awards and Distinctions:

1. Dr. Rincón-Mora's TPS5210, "**Top 100 Products**" of 1998 by *EDN* (on cover of *Electronic Design*).
2. One of **Top 7 Most Cited TCAS II Papers in 1998**: B.J. Blalock, P.E. Allen, and G.A. Rincón-Mora, "Designing 1V Op Amps Using Standard Digital CMOS Technology," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 45, no. 7, pp. 769-780, Jul. 1998.
3. TIDN Forum's **Significant TI Contributor**, Texas Instruments, 1999.
4. **Three Year Patent Award** for U.S. 5491437, 5500625, and 5519341, Texas Instruments, 1999.
5. **Adjunct Professor**, Georgia Institute of Technology (**Georgia Tech**), 1999–2001.
6. **Charles E. Perry Visionary Award**, Florida International University, 2000.
7. **Council of Outstanding Young Engineering Alumni Inductee**, Georgia Tech, 2000.
8. **Hispanic in Technology Award**, Society of Hispanic Professional Engineers (SHPE), 2000.
9. Voted one of "**The 100 Most Influential Hispanics**," *Hispanic Business*, 2000.
10. One of IEEE's **Top 25 Most Downloaded TCAS II Papers in 2004**: G.A. Rincón-Mora and R. Stair, "A low voltage, rail-to-rail, class AB CMOS amplifier with highdrive and low output impedance characteristics," *IEEE Transactions on Circuits and Systems II (TCAS II)*, vol. 48, no. 8, pp. 753-761, Aug. 2001.
11. Elevated to **Senior Member**, Institute of Electrical and Electronics Engineers (IEEE), 2001.
12. **State of California Commendation Certificate**, from former Lieutenant Governor Cruz M. Bustamante, 2001.
13. **Orgullo Hispano Award**, Robins Air Force Base, Sept. 23, 2003.
14. One of IEEE's **Top 200 Most Downloaded Journal Papers in 2004** (177 times in one month): B. Sahu and G.A. Rincón-Mora, "A High-Efficiency Linear RF Power Amplifier With a Power-Tracking Dynamically Adaptive Buck-Boost Supply," *IEEE Transactions on Microwave Theory and Techniques (TMTT)*, vol. 52, no. 1, pp. 112-120, Jan. 2004.
15. **HENAAC Role Model of the Week**, Hispanic Engineer National Achievement Awards Corporation, Jul. 5, 2005.
16. "**7<sup>th</sup> Most Read** Power Management Design Line How-To Article in 2005" for G.A. Rincón-Mora and V. Gupta, "Power Supply Ripple Rejection and Linear Regulators: What's all the noise about?" *Power Management Design Line (PMDL)*, Sept. 20, 2005.
17. **Hispanic Heritage Award**, Robins Air Force Base, 2005.
18. "**2<sup>nd</sup> Most Read** Power Management Design Line How-To Article in 2006" for E. Torres and G.A. Rincón-Mora, "Harvesting energy into lithium-ion batteries," *Power Management Design Line (PMDL)*, Feb. 14, 2006.
19. **IEEE CASS Service Award**, MWSCAS–NEWCAS, Aug. 8, 2007.
20. **2<sup>nd</sup> Place Award** for 2009 Science Applications International Corporation's Georgia Tech Paper Competition for D. Kwon and G.A. Rincón-Mora, "A Rectifier-Free Piezoelectric Energy Harvester Circuit."
21. *Solid-State Circuits Magazine* (Spring 2010) reported Rincón-Mora's *Analog IC Design with Low-Dropout Regulators* as one of two **best sellers** at IEEE International Solid-State Circuits Conference (ISSCC) 2009.
22. Elevated to **Fellow**, Institution of Engineering and Technology (IET), 2009.
23. Elected **IEEE Distinguished Lecturer**, Circuits and Systems Society, 2009–2010.
24. **Thank a Teacher Certificate**, Georgia Institute of Technology, 2010.
25. Elevated to **Fellow** "for contributions to energy and power integrated circuit design," Institute of Electrical and Electronics Engineers (IEEE), 2011.
26. **Thank a Teacher Certificate**, Georgia Institute of Technology, 2012.
27. *IEEE Solid-State Circuits Magazine* (Spring 2012) reported Rincón-Mora's *Analog IC Design with Low-Dropout Regulators* as one of three "**Third Best in Show**" at the IEEE International Solid-State Circuits Conference (ISSCC) 2012.
28. **IEEE Certificate of Appreciation** for "Notable Services and Contributions towards the advancement of IEEE and the Engineering Professions" as 2005–2012 Joint CASS–SSCS Chapter Chair, IEEE, 2012.

## CURRICULUM VITA

### Magazine Covers/Feature Stories on Dr. Rincón-Mora:

1. "Bravo – National Award Winners," *Official Magazine of the Society of Hispanic Professional Engineers*, Spring 2000.
2. "The 100 Most Influential Hispanics," *Hispanic Business* magazine, Oct. 2000.
3. "A high-tech engineer with a low-tech lifestyle," *La Fuente* (Dallas publication), Mar. 2000.
4. "Gabriel Rincón-Mora - Impacta en la alta tecnología," *Nuevo Impacto* (Atlanta publication), Aug. 2002.
5. "Profesionales Latinos – La nueva cara de Georgia," *Nuevo Impacto* (Atlanta publication), Oct. 2003.
6. "Gabriel Rincón Mora – Un ingeniero polifacético: Inventor, profesor, escritor y actor/Gabriel Rincón Mora – Outstanding engineer and writer," *Nuevo Impacto* (Atlanta publication), Nov. 2004.

### Feature Stories on Dr. Rincón-Mora:

1. "Passion for design, apathy for gizmos," *Electronic Engineering Times*, Jun. 2000.
2. "Designer has passion for work, apathy for gizmos," *Planet Analog*, Jun. 2000.
3. "By Day an Engineer," *Intown* (Atlanta publication), Aug. 2002.
4. "Notar – Short Stories and Poems to Boot," *Official Magazine of the Society of Hispanic Professional Engineers*, Aug. 2002.
5. "Innovators Matter," *Hispanic Business* magazine, Sept. 2002.
6. "Innovators Matter," *Hispanic Business* magazine, Dec. 2002.
7. "Hispanic Engineering Talent," *Georgia Tech Society of Professional Hispanic Engineers*, Feb. 2003.
8. "World-class training workshop on analog IC power management by top Integrated Circuit (IC) expert from the United States," *Hong Kong Science and Technology Parks News & Newsletter*, Oct. 2003.
9. "SSCS Subsidizes Short Course on Linear Regulator Design in Taipei," *IEEE Solid-State Circuits Society Newsletter*, Sept. 2006.
10. "Alumni Profile: Gabriel A. Rincón-Mora," *Summa Cum Laude*, Florida International University Honors College, Winter 2011, vol. 1, no. 3.
11. "Featured Engineer: Gabriel Alfonso Rincón-Mora," *EEWeb – Electrical Engineering Community*, November 2012.

### Other Awards and Recognitions:

1. *Presidential Academic Fitness Award*, (signed by President George Bush, Sr.), 1989.
2. *Insignis Scholarship*, University of Detroit, 1989.
3. *Phi Kappa Phi* (national honor society), 1991.
4. *Dean's List*, Florida International University, 1989–1992.
5. *B.S.E.E. with High Honors*, Florida International University, 1992.
6. *Florida Undergraduate Scholars Fund Scholarship*, State of Florida, 1989–1992.
7. *Faculty Scholars Scholarship*, Florida International University, 1989–1992.
8. *Honorary Award Recognition*, National Dean's List, 1990–1992.
9. *Eta Kappa Nu* (national electrical engineering honor society), 1992.
10. *Honorable Mention*, National Science Foundation (NSF), 1993.
11. *Tau Beta Pi* (Life Member: national engineering honor society), 1994.
12. *Outstanding Ph.D. Graduate*, Georgia Institute of Technology, 1996.