

CURRICULUM VITA

Prof. Gabriel Alfonso Rincón-Mora, Ph.D.

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I. Degrees

B.S. in Electrical Eng. (Magna Cum Laude), Florida Int. Univ., 1992.

M.S. in Electrical Eng. (Highest Honors), Georgia Inst. of Tech., 1994.

Ph.D. in Electrical Eng. (Outstanding Ph.D. Candidate), Georgia Inst. of Tech., 1996.

II. Employment

Electrical Engineer and Laboratory Syst. Specialist, Northern Telecom, 1993.

Analog IC Design Engineer, Texas Instruments, 1994–1996.

Senior Design Engineer and Design Team Leader, Texas Instruments, 1997–2001.

Member of Group Technical Staff, Texas Instruments, 1999–2003.

Adjunct Professor, Electrical and Computer Eng., Georgia Inst. of Tech., 1999–2001.

Senior Analog IC Des. Consultant, Texas Instruments, 2003–2004.

Assistant Professor, Electrical and Computer Eng., Georgia Inst. of Tech., 2001–2007.

Director, Georgia Tech Analog Consortium, Georgia Inst. of Tech., 2001–2004.

Director, TI Analog Fellowship Program, Georgia Inst. of Tech., 2001–2015.

Associate Professor with Tenure, Electrical and Computer Eng., Georgia Inst. of Tech., 2007–2012.

Visiting Professor, Electrical Eng., Nat. Cheng Kung Univ., Taiwan, since 2011.

Professor, Electrical and Computer Eng., Georgia Inst. of Tech., since 2012.

III. Output

Ph.D. Dissertation: *Current Efficient, Low Voltage, Low Dropout Regulators*. Georgia Inst. of Tech., 1996.

Books (sole author)

B1. *Voltage References*. New Jersey: IEEE Press and John Wiley & Sons, Inc., 2001 [Translated into Chinese].

B2. *Power Management ICs*. Raleigh: Lulu, 2005.

B3. *Analog IC Design with Low-Dropout Regulators*. New York: McGraw-Hill, Jan. 2009 [Translated into Chinese].

B4. *Analog IC Design with Low-Dropout Regulators, 2nd Edition*. New York: McGraw-Hill, 2014 [Translated into Chinese].

B5. *Analog IC Design, 6th Edition*. Raleigh: Lulu, 2016.

B6. *Power IC Design, 5th Edition*. Raleigh: Lulu, 2016.

B7. *Short Stories and Poems to Boot!* New York: Vantage Press, 2001.

B8. *Triple Engagement*. New York: iUniverse, 2004.

B9. *Vanish*. Raleigh: Lulu, 2009.

Handbooks (sole author)

HB1. *CMOS Switched-Inductor Power Supplies*. New York: KDP, 2018.

Book Chapters (sole author)

BC1. "Harvesting Microelectronic Circuits," *Energy Harvesting Technologies*. Springer, Jan. 2009.

BC2. "Energizing and Powering Microsystems," *Integrated Microsystems*. CRC Press, Oct. 2011.

BC3. "Vibration-Based Energy-Harvesting Integrated Circuits," *Advances in Energy Harvesting Methods*. Springer, Feb. 2013.

BC4. "Energy-Harvesting Integrated Circuits," *Energy Harvesting with Functional Materials and Microsystems*. CRC Press, Nov. 2013.

Patents Issued and Licensed (Prof. Rincón-Mora advised boldfaced inventors.)

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- P1. G.A. Rincón *et al.*, "Amplifier circuit and method," U.S. 5,491,437, Feb. 13, 1996.
- P2. G.A. Rincón *et al.*, "Controlled current output stage amplifier circuit and method," U.S. 5,500,625, Mar. 19, 1996.
- P3. M. Corsi and G.A. Rincón, "Cross coupled quad comparator for current sensing...", U.S. 5,519,341, May 21, 1996.
- P4. G.A. Rincón *et al.*, "Output stage of amplifier circuit," EP 715405, Jun. 5, 1996.
- P5. G.A. Rincón *et al.*, "Controlled current output stage amplifier circuit and method," JP 8237046, Sept. 13, 1996.
- P6. M. Corsi and G.A. Rincón, "Current sensing circuit and method," U.S. 5,614,850, Mar. 25, 1997.
- P7. M. Corsi, G.A. Rincón *et al.*, "A voltage regulator," EP 851332, Jan. 7, 1998.
- P8. M. Corsi, G.A. Rincón *et al.*, "Drop-out voltage controller," JP 10187258, Jul. 14, 1998.
- P9. M. Corsi, G.A. Rincón *et al.*, "Low drop-out regulator with PMOS pass element," U.S. 5,867,015, Feb. 2, 1999.
- P10. G.A. Rincón-Mora, "DC-DC converter with voltage loss compensation," EP 928056, Jul. 7, 1999.
- P11. G.A. Rincón-Mora, "...piecewise-linear curvature corrected bandgap reference," U.S. 5,952,873, Sept. 14, 1999.
- P12. G.A. Rincón-Mora, "Optimized frequency shaping circuit topologies for LDOs," U.S. 5,982,226, Nov. 9, 1999.
- P13. G.A. Rincón-Mora and M. Corsi, "Low-drop-out voltage regulator...", EP 957421, Nov. 17, 1999.
- P14. G.A. Rincón-Mora *et al.*, "Low-dropout...regulator...transient...boost circuit," U.S. 6,046,577, Apr. 4, 2000.
- P15. G.A. Rincón-Mora, "Miller compensated amplifier for operation with capacitive loading," EP 1006648, Jun. 7, 2000.
- P16. G.A. Rincón-Mora, "Active compensating capacitive multiplier," U.S. 6,084,475, Jul. 4, 2000.
- P17. G.A. Rincón-Mora, "Bandgap circuits with curvature-correction," EP 1041480, Oct. 4, 2000.
- P18. G.A. Rincón-Mora, "Exact curvature-correcting method for bandgap circuits," U.S. 6,157,245, Dec. 5, 2000.
- P19. G.A. Rincón-Mora and M. Corsi, "Current-efficient low-drop-out voltage regulator...", U.S. 6,188,211, Feb. 13, 2001.
- P20. G.A. Rincón-Mora, "Accurate, fast, and user programmable hysteretic comparator," U.S. 6,229,350, May 8, 2001.
- P21. G.A. Rincón-Mora and M. Huggins, "High power supply...rejection...low drop-out...", U.S. 6,304,131, Oct. 16, 2001.
- P22. G.A. Rincón-Mora, "...low ripple, high frequency hysteretic...dc-dc converters," U.S. 6,369,555, Apr. 9, 2002.
- P23. G.A. Rincón-Mora and **B. Abesingha**, "Method of minimizing package-shift effects...", U.S. 6,432,753, Aug. 13, 2002.
- P24. G.A. Rincón-Mora, "Adjustable temperature-compensated threshold circuit...", EP 1265363, Dec. 11, 2002.
- P25. G.A. Rincón-Mora and **R. Stair**, "Buffer/driver for low dropout regulators," U.S. 6,501,305, Dec. 31, 2002.
- P26. G.A. Rincón-Mora, "...threshold circuit with trip-points exceeding the...supplies," U.S. 6,545,511, Apr. 8, 2003.
- P27. G.A. Rincón-Mora and **M. Pulkin**, "...low impedance driver for linear regulators," U.S. 6,573,694, Jun. 3, 2003.
- P28. G.A. Rincón-Mora, "Temperature-compensated threshold circuit," EP 1351063, Oct. 8, 2003.
- P29. G.A. Rincón-Mora and M. Corsi, "Current-efficient low-drop-out voltage regulator...", DE 69910888, Oct. 9, 2003.
- P30. G.A. Rincón-Mora, "Integrated low ripple, high frequency...hysteretic controller...", U.S. 6,628,109, Sept. 30, 2003.
- P31. G.A. Rincón *et al.*, "Output stage of amplifier circuit," DE 69532061, Dec. 11, 2003.
- P32. G.A. Rincón-Mora *et al.*, "...minimizes package-shift effects in integrated circuits...", U.S. 6,750,553, Jun. 15, 2004.
- P33. G.A. Rincón-Mora and **R. Stair**, "...threshold voltage extraction...capacitor multiplier," U.S. 6,806,762, Oct. 19, 2004.
- P34. M. Corsi, G.A. Rincón *et al.*, "A voltage regulator," DE 69727783, Dec. 30, 2004.
- P35. G.A. Rincón-Mora, **V. Gupta**, and P. Raha, "Low dropout...linear regulator...", U.S. 6,847,260, Jan. 25, 2005.
- P36. G.A. Rincón-Mora, "Active compensating capacitive multiplier," DE 69934566, Feb. 8, 2007.
- P37. G.A. Rincón-Mora and **M. Arnold**, "...buffer with rail-to-rail output for low dropout," U.S. 7,339,416, Mar. 4, 2008.
- P38. G.A. Rincón-Mora, "Temperature-compensated threshold circuit," DE 60225626, Apr. 30, 2008.
- P39. G.A. Rincón-Mora, "Exact curvature-correcting method for bandgap circuits," DE 60042142, June 18, 2009.
- P40. G.A. Rincón-Mora and **M. Arnold**, "Gate driver circuit for power transistor," U.S. 7,560,973, Jul. 14, 2009.
- P41. G.A. Rincón-Mora, "Increase in active compensation capacitive property," JP 4528394, Aug. 18, 2010.
- P42. **D. Kwon** and G.A. Rincón-Mora, "Rectifier-free piezoelectric energy harvester...", U.S. 8,368,290, Feb. 5, 2013.

Journal Articles (Prof. Rincón-Mora advised boldfaced authors.)

- J1. G.A. Rincón-Mora and P.E. Allen, "A low-voltage, low quiescent current, low drop-out regulator," *IEEE J. of Solid-State Circuits*, vol. 33, no. 1, pp. 36–44, Jan. 1998.

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- J2. G.A. Rincón-Mora and P.E. Allen, "Optimized frequency-shaping circuit topologies for LDO's," *IEEE Trans. on Circuits and Syst. II*, vol. 45, no. 6, pp. 703–708, Jun. 1998.
- J3. B.J. Blalock, P.E. Allen, and G.A. Rincón-Mora, "Designing 1V op amps using standard digital CMOS technology," *IEEE Trans. on Circuits and Syst. II*, vol. 45, no. 7, pp. 769–780, Jul. 1998.
- J4. G.A. Rincón-Mora and P.E. Allen, "A 1.1 V current-mode and piecewise-linear curvature corrected bandgap reference," *IEEE J. of Solid-State Circuits*, vol. 33, no. 10, pp. 1551–1554, Oct. 1998.
- J5. G.A. Rincón-Mora, "Active capacitor multiplier in Miller-compensated circuits," *IEEE J. of Solid-State Circuits*, vol. 35, no. 1, pp. 26–32, Jan. 2000.
- J6. **R. Stair** and G.A. Rincón-Mora, "A low voltage, rail-to-rail, class AB CMOS amplifier with high drive and low output impedance characteristics," *IEEE Trans. on Circuits and Syst. II*, vol. 48, no. 8, pp. 753–761, Aug. 2001.
- J7. **B. Abesingha**, G.A. Rincón-Mora, and D. Briggs, "Voltage shift in plastic-packaged bandgap references," *IEEE Trans. on Circuits and Syst. II*, vol. 49, no. 10, pp. 681–685, Oct. 2002.
- J8. **R. Dokania** and G.A. Rincón-Mora, "Cancellation of load-regulation in low drop-out regulators," *IET Electronic Letters*, vol. 38, issue 22, pp. 1300–1302, Oct. 2002.
- J9. **B. Sahu** and G.A. Rincón-Mora, "A high-efficiency linear RF power amplifier with a power-tracking dynamically adaptive buck–boost Supply," *IEEE Trans. on Microwave Theory and Techniques*, vol. 52, no. 1, pp. 112–120, Jan. 2004.
- J10. **B. Sahu** and G.A. Rincón-Mora, "A low voltage, dynamic, non-inverting, synchronous buck–boost converter for portable applications," *IEEE Trans. on Power Electronics*, vol. 19, no. 2, pp. 443–452, Feb. 2004.
- J11. **S. Zhou** and G.A. Rincón-Mora, "A high efficiency, soft switching dc–dc converter with adaptive current-ripple control for portable applications," *IEEE Trans. on Circuits and Syst. II*, vol. 53, no. 4, pp. 294–298, Apr. 2006.
- J12. **M. Chen** and G.A. Rincón-Mora, "An accurate electrical battery model capable of predicting runtime and i–v performance," *IEEE Trans. on Energy Conversion*, vol. 21, no. 2, pp. 504–511, Jun. 2006.
- J13. **M. Chen** and G.A. Rincón-Mora, "Accurate, compact, and power efficient li-ion battery charger circuit," *IEEE Trans. on Circuits and Syst. II*, vol. 53, no. 11, pp. 1180–1184, Nov. 2006.
- J14. **H.P. Forghani-zadeh** and G.A. Rincón-Mora, "Low-power CMOS ramp generator circuit for dc–dc converters," *J. of Low Power Electronics*, vol. 2, no. 3, pp. 437–441, Dec. 2006.
- J15. **B. Sahu** and G. A. Rincón-Mora, "An accurate, low voltage, CMOS switching power supply with adaptive on-time pulse-frequency modulation," *IEEE Trans. on Circuits and Syst. I*, vol. 54, no. 2, pp. 312–321, Feb. 2007.
- J16. **B. Sahu** and G.A. Rincón-Mora, "A high efficiency WCDMA RF power amplifier (PA) with adaptive, dual-mode buck–boost supply and bias-current control," *IEEE Microwave and Wireless Components Letters*, vol. 17, no. 3, pp. 238–240, Mar. 2007.
- J17. **V. Gupta** and G.A. Rincón-Mora, "Achieving less than 2% 3- σ mismatch with minimum channel-length CMOS devices," *IEEE Trans. on Circuits and Syst. II*, vol. 54, no. 3, pp. 232–236, Mar. 2007.
- J18. **H.P. Forghani-zadeh** and G.A. Rincón-Mora, "An accurate, continuous, and lossless self-learning CMOS current-sensing scheme for inductor-based dc–dc converters," *IEEE J. of Solid-State Circuits*, vol. 42, no. 3, pp. 665–679, Mar. 2007.
- J19. **H.P. Forghani-zadeh** and G.A. Rincón-Mora, "A fast and reliable top-level simulation strategy for mixed-signal ICs and its application to dc–dc converter circuits," *IET Circuits, Devices, and Syst.*, vol. 1, no. 2, pp. 143–150, Apr. 2007.
- J20. **H.P. Forghani-zadeh** and G.A. Rincón-Mora, "A programmable 210 μ V offset rail-to-rail G_M -C filter," *IEEE Trans. on Circuits and Syst. I*, vol. 54, no. 8, pp. 1636–1646, Aug. 2007.
- J21. **V. Gupta** and G.A. Rincón-Mora, "Low output impedance 0.6 μ m-CMOS sub-bandgap reference," *IET Electronic Letters*, vol. 43, pp. 1085–1087, Sept. 2007.
- J22. **N. Keskar** and G.A. Rincón-Mora, "A fast, sigma–delta boost dc–dc converter tolerant to wide LC filter variations," *IEEE Trans. on Circuits and Syst. II*, vol. 55, pp. 198–202, Feb. 2008.
- J23. **N. Keskar** and G.A. Rincón-Mora, "A compact 1–30 μ H, 1–350 μ F, 5–50m Ω ESR compliant, 1.5% accurate 0.6 μ m CMOS differential sigma–delta boost dc–dc converter," *Analog Integrated Circuits and Signal Processing J.*, vol. 54, no. 3, pp. 157–169, 2008.
- J24. **M. Chen** and G.A. Rincón-Mora, "A compact electrical model for microscale fuel cells capable of predicting runtime and i–v polarization performance," *IEEE Trans. on Energy Conversion*, vol. 23, no. 3, pp. 842–850, Sept. 2008.
- J25. **E.O. Torres** and G.A. Rincón-Mora, "Energy-harvesting system-in-package (SiP) microsystem," *ASCE J. of Energy Eng.*, Invited, vol. 134, no. 4, pp. 121–129, Dec. 2008.

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- J26. **S. Kim** and G.A. Rincón-Mora, "Achieving high efficiency under micro-watt loads with switching buck dc–dc converters," *J. of Low Power Electronics*, vol. 5, no. 2, pp. 229–240, Aug. 2009.
- J27. **D. Kwon** and G.A. Rincón-Mora, "Single-inductor multiple-output (SIMO) switching dc–dc converters," *IEEE Trans. on Circuits and Syst. II*, Invited, vol. 56, no. 8, Aug. 2009.
- J28. **E.O. Torres** and G.A. Rincón-Mora, "Electrostatic energy-harvesting and battery-charging CMOS system prototype," *IEEE Trans. on Circuits and Syst. I*, vol. 56, no. 9, pp. 1938–1948, Sept. 2009.
- J29. **L.A. Milner** and G.A. Rincón-Mora, "Limits of predictive current-ripple suppression in switching power supply ICs," *IET Power Electronics*, vol. 3, no. 1, pp. 43–53, Jan. 2010.
- J30. **V. Gupta** and G.A. Rincón-Mora, "A low-impedance, sub-bandgap 0.6 μ m CMOS reference with 0.84% trimless 3-sigma accuracy and –30dB worst-case PSRR up to 50MHz," *Analog Integrated Circuits and Signal Processing J.*, vol. 62, no. 3, p. 345, 2010.
- J31. **E.O. Torres** and G.A. Rincón-Mora, "A 0.7 μ m BiCMOS electrostatic energy-harvesting system IC," *IEEE J. of Solid-State Circuits*, vol. 45, no. 2, pp. 483–496, Feb. 2010.
- J32. **N. Keskar** and G.A. Rincón-Mora, "One clock-cycle response 0.5 μ m CMOS dual-mode sigma–delta dc–dc bypass boost converter stable over wide $R_{ESR}LC$ variations," *Advances in Power Electronics*, vol. 2010, no. 253508, p. 9, 2010.
- J33. **L.A. Milner** and G.A. Rincón-Mora, "A feed-forward 10 \times CMOS current-ripple suppressor for switching power supplies," *IEEE Trans. on Circuits and Syst. II*, vol. 57, no. 5, pp. 354–378, May 2010.
- J34. **E.O. Torres** and G.A. Rincón-Mora, "Self-tuning electrostatic energy-harvester IC," *IEEE Trans. on Circuits and Syst. II*, vol. 57, no. 10, pp. 808–812, Oct. 2010.
- J35. **A. Patel** and G.A. Rincón-Mora, "High power-supply-rejection (PSR) current-mode low-dropout (LDO) regulator," *IEEE Trans. on Circuits and Syst. II*, vol. 57, no. 11, pp. 868–873, Nov. 2010.
- J36. **D. Kwon** and G.A. Rincón-Mora, "A 2- μ m BiCMOS rectifier-free ac–dc piezoelectric energy harvester–charger IC," *IEEE Trans. on Biomedical Circuits and Syst.*, Invited, vol. 4, no. 6, pp. 400–409, Dec. 2010.
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- J38. **R.D. Prabha**, **D. Kwon**, **O. Lazaro**, **K.D. Peterson**, and G.A. Rincón-Mora, "Increasing electrical damping in energy-harnessing transducers," *IEEE Trans. on Circuits and Syst. II*, Invited, vol. 58, no. 12, pp. 787–791, Dec. 2011.
- J39. **L.A. Milner** and G.A. Rincón-Mora, "Small saturating inductors for more compact switching power supplies," *IEEJ Trans. on Electrical and Electronic Eng.*, vol. 7, no. 1, pp. 69–73, Jan. 2012.
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- J41. G.A. Rincón-Mora, **A.A. Blanco**, and **J.P. Vogt**, "A 1.3- μ W, 0.6-m CMOS current–frequency analog–digital converter for implantable blood-glucose monitors," *J. of Low Power Electronics*, vol. 8, pp. 47–57, Feb. 2012.
- J42. **O. Lazaro**, G.A. Rincón-Mora, and **J.P. Vogt**, "1–50-MHz VHF electromagnetic sensor-interface power-attenuation detector circuit," *Int. J. of Electronics and Communications*, vol. 66, no. 6, pp. 502–508, Jun. 2012.
- J43. **O. Lazaro** and G.A. Rincón-Mora, "Inductively coupled 180-nm CMOS charger with adjustable energy-investment capability," *IEEE Trans. on Circuits and Syst. II*, vol. 60, no. 8, pp. 482–486, Aug. 2013.
- J44. **O. Lazaro** and G.A. Rincón-Mora, "180-nm CMOS wideband capacitor-free inductively coupled power receiver and charger," *IEEE J. of Solid-State Circuits*, vol. 48, no. 11, pp. 2839–2849, Nov. 2013.
- J45. **D. Kwon** and G.A. Rincón-Mora, "A single-inductor 0.35- μ m CMOS energy-investing piezoelectric harvester," *IEEE J. of Solid-State Circuits*, vol. 49, no. 10, pp. 2277–2291, Oct. 2014.
- J46. **A. Blanco** and G.A. Rincón-Mora, "A 44–93- μ s 250–400-mV 0.18- μ m CMOS starter for dc-sourced switched-inductor energy harvesters," *IEEE Trans. on Circuits and Syst. II*, vol. 61, no. 12, pp. 1002–1006, Dec. 2014.
- J47. **O. Lazaro** and G.A. Rincón-Mora, "A non-resonant self-synchronizing inductively coupled 0.18- μ m CMOS power receiver and charger," *IEEE J. of Emerging and Selected Topics in Power Electronics*, vol. 3, no. 1, pp. 261–271, Mar. 2015.
- J48. **S. Kim** and G.A. Rincón-Mora, "Dual-source hysteretic switched-inductor 0.18- μ m complementary metal–oxide–semiconductor charger–supply system," *IET Circuits, Devices, and Syst.*, vol. 9, no. 4, pp. 275–282, 2015.
- J49. **R.D. Prabha** and G.A. Rincón-Mora, "Maximizing power-transfer efficiency in low-power DC–DC converters," *IET Electronic Letters*, vol. 51, no. 23, pp. 1918–1920, Nov. 2015.

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- J51. G.A. Rincón-Mora and **S. Yang**, "Tiny piezoelectric harvesters: Principles, constraints, and power conversion," *IEEE Trans. on Circuits and Syst. I*, Invited, vol. 63, no. 5, pp. 639–649, May 2016.
- J52. **R.D. Prabha** and G.A. Rincón-Mora, "Drawing the most power from low-cost single-well 1- mm^2 CMOS photovoltaic cells," *IEEE Trans. on Circuits and Syst. II*, vol. 64, no. 1, pp. 46–50, Jan. 2017.
- J53. **C. Solís** and G.A. Rincón-Mora, "0.6- μm CMOS switched-inductor dual-supply hysteretic current-mode buck converter," *IEEE Trans. on Power Electronics*, vol. 32, no. 3, pp. 2387–2394, Mar. 2017.
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- J56. **C. Solís** and G.A. Rincón-Mora, "87%-Efficient 330-mW 0.6- μm Single-Inductor Triple-Output Buck–Boost Power Supply," *IEEE Trans. on Power Electronics*, vol. 33, no. 8, pp. 6837–6844, Aug. 2018.
- J57. **R.D. Prabha** and G.A. Rincón-Mora, "Light-Harvesting CMOS Power-Supply Syst. for 0–10-mW Wireless Microsensors," *IEEE Sensors J.*, vol. 19, no. 2, pp. 726–734, Jan. 2019.
- J58. **N. Xing** and G.A. Rincón-Mora, "180-nm 85%-Efficient Inductively Coupled Switched Resonant Half-Bridge Power Receiver," *IEEE Trans. on Circuits and Syst. II*, accepted, 2018.
- J59. **S. Yang** and G.A. Rincón-Mora, "Energy-Harvesting Piezoelectric-Powered CMOS Series Switched-Inductor Bridge," *IEEE Trans. on Power Electronics*, accepted, 2018.

Invited Trade Journal Articles (Prof. Rincón-Mora advised boldfaced authors.)

- T1. **N. Keskar** and G.A. Rincón-Mora, "A user-friendly boost dc–dc converter topology – it's fast and widely stable," *Power Management Des. Line*, Jan. 23, 2005.
- T2. **N. Keskar** and G.A. Rincón-Mora, "A user-friendly boost dc–dc converter topology – it's fast and widely stable," *Planet Analog*, Jan. 26, 2005.
- T3. **V. Gupta** and G.A. Rincón-Mora, "Inside the belly of the beast: A map for the wary bandgap reference designer when confronting process variations," *Power Management Des. Line*, Feb. 18, 2005.
- T4. G.A. Rincón-Mora and **P. Forghani**, "Accurate and lossless current-sensing techniques: A practical myth?" *Power Management Des. Line*, Mar. 17, 2005.
- T5. G.A. Rincón-Mora and **M. Chen**, "Self-powered chips – the work of fiction," *Power Management Des. Line*, Apr. 28, 2005.
- T6. G.A. Rincón-Mora and **M. Chen**, "Self-powered chips – the work of fiction," *Planet Analog*, Apr. 28, 2005.
- T7. **L. Milner** and G.A. Rincón-Mora, "Taming power inductors for system-on-Chip (SoC) integration," *Power Management Des. Line*, May 18, 2005.
- T8. **N. Keskar** and G.A. Rincón-Mora, "A user-friendly boost dc–dc converter topology," *Electronic Eng. Times Japan*, no. 1, 2005.
- T9. **E. Torres** and G.A. Rincón-Mora, "Energy-harvesting chips and the quest for everlasting life," *Power Management Des. Line*, Jun. 30, 2005.
- T10. G.A. Rincón-Mora and **H. Pan**, "Quenching the thirst of RF power amps and extending the life of portable devices," *Power Management Des. Line*, Jul. 15, 2005.
- T11. G.A. Rincón-Mora and **H. Pan**, "Quenching the thirst of RF power amps and extending the life of portable devices," *Planet Analog*, Jul. 31, 2005.
- T12. **N. Keskar** and G.A. Rincón-Mora, "A fast, accurate, LC compliant dc–dc boost regulator...Is it possible?" *Power Management Des. Line*, Aug. 22, 2005.
- T13. **E. Torres** and G.A. Rincón-Mora, "Harvesting ambient energy will make embedded devices autonomous," *Electronic Eng. Times' Embedded*, Aug. 29, 2005.
- T14. G.A. Rincón-Mora and **V. Gupta**, "Power supply ripple rejection and linear regulators: What's all the noise about?" *Power Management Des. Line*, Sept. 20, 2005.
- T15. **E. Torres** and G.A. Rincón-Mora, "Harvesting ambient energy," *Electronic Eng. Times*, Aug. 29, 2005.

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IV. Presentations

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4. "Energizing & Powering Microsystems," IEEE Int. Syst.-on-Chip Des. Conf., Busan, Korea, Nov. 23, 2009.
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9. "Microwatt CMOS Harvesters," Infrared Radiation, Thermolectricity and Chaos Workshop, U.S. Office of Naval Res., James Madison Univ., Harrisonburg, VA, Jun. 17, 2015.
10. "Higher Education, Success, and Life in Electrical Eng.," IEEE Solid-State Circuits & Circuits and Syst. Soc., Tainan, Taiwan, Nov. 19, 2015.
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CURRICULUM VITA

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4. "AC Des. & Performance of Low Dropout Regulators," IEEE Circuits and Syst. Soc., Tainan, Taiwan, Nov. 26, 2010.
5. "Energizing & Powering Microsystems," MediaTek-NTU Distinguished Professor Talk, Nanyang Technological Univ., Singapore, Feb. 29, 2016.
6. "Energizing & Powering Microsystems," IEEE Circuits and Syst. Soc., Univ. of Puerto Rico at Mayagüez, Mar. 22, 2018.
7. "Energizing & Powering Microsystems," IEEE Circuits and Syst. Soc., Montevideo, Uruguay, Apr. 30, 2018.
8. "Energizing & Powering Microsystems," IEEE Circuits and Syst. Soc., Curitiba, Brazil, May 2, 2018.
9. "Energizing & Powering Microsystems," IEEE Circuits and Syst. Soc., Santiago, Chile, May 10, 2018.
10. "Energizing & Powering Microsystems," IEEE Circuits and Syst. Soc., Valparaíso, Chile, May 11, 2018.
11. "Energizing & Powering Microsystems," IEEE Circuits and Syst. Soc., Thessaloniki, Greece, Dec. 7, 2018.
12. "Energizing & Powering Microsystems," IEEE Circuits and Syst. Soc., Santa Clara, California, Feb. 28, 2019.
13. "Energizing & Powering Microsystems," IEEE Circuits and Syst. Soc., Lima, Peru, Mar. 21, 2019.
14. "Energizing & Powering Microsystems," IEEE Circuits and Syst. Soc., Seoul, Korea, May 9, 2019.
15. "Energizing & Powering Microsystems," IEEE Circuits and Syst. Soc., Seoul, Korea, May 10, 2019.

Professional Short Courses (sole instructor)

1. *Integrated DC–DC Converters* (1 day). RF Micro-Devices in Greensboro, NC, Apr. 2002.
2. *Power Management ICs* (3 days). Hong Kong Science & Tech. Park, Hong Kong, China, Aug. 2003.
3. *CMOS Analog Integrated Circuits* (5 days). Global Learning & Conf. Center, Atlanta, GA, Sept. 26–30, 2005.
4. *Power Supply ICs for RF PAs* (1 hour). Invited IEEE Expert Now Module, Fall 2005.
5. *Power Management ICs* (4 days). ON Semiconductor in Bratislava, Slovakia, Dec. 12–15, 2005.
6. *Power Management ICs* (4 days). ON Semiconductor in Toulouse, France, Dec. 19–22, 2005.
7. *Power Management ICs* (4 days). Space & Naval Warfare Syst. Command, San Diego, CA, Apr. 10–13, 2006.
8. *Power Management ICs* (4 days). Cypress Semiconductor in Colorado Springs, CO, Apr. 17–20, 2006.
9. *Linear Regulators* (1 day). IEEE SSCS Distinguished Lecture in Hsinchu, Taiwan, Jun. 8, 2006.
10. *Linear Regulators* (1 day). IEEE SSCS Distinguished Lecture in Taipei, Taiwan, Jun. 9, 2006.
11. *Power Management ICs* (4 days). Toko Inc. in Saitama, Japan, Dec. 11–14, 2006.
12. *Power Management ICs* (3 days). Intel Corp. in Hillsboro, OR, May 9–11, 2007.
13. *Power Management ICs* (4 days). Spyro Tech. in Singapore, May 21–24, 2007.
14. *Analog IC Des.* (4 days). Integrated Device Tech. in Duluth, GA, Jan. 26, Feb. 23, and Mar. 9 and 23, 2009.
15. *Designing Bandgap Voltage References* (2 days). Nat. Cheng-Kung Univ. in Tainan, Taiwan, Dec. 5–6, 2012.
16. *Stabilizing Complex Single-Inductor DC–DC Power Supplies* (2 days), Nat. Cheng-Kung Univ., Tainan IEEE Solid-State Circuits and Circuits and Syst. Soc. Chapters in Tainan, Taiwan, Nov. 17 and 20, 2015.
17. *Linear Regulators* (3 days). Dialog Semiconductor in Swindon, United Kingdom, Dec. 1–3, 2015.
18. *Designing Photovoltaic-Sourced Charger-Supply Microsystems* (2 days). Nat. Cheng-Kung Univ., Tainan IEEE Solid-State Circuits and Circuits and Syst. Soc. Chapters in Tainan, Taiwan, Nov. 17–18, 2016.

CURRICULUM VITA

19. *Des. Insight and Intuition of Negative Feedback at the Transistor Level* (2 days). Nat. Cheng-Kung Univ., Tainan IEEE Solid-State Circuits and Circuits and Syst. Soc. Chapters in Tainan, Taiwan, Nov. 18 and 21, 2016.
20. *Switched Power Supplies & Energy Harvesters* (4 days). Dialog Semiconductor in Santa Clara, CA, Mar. 20–23, 2017.

Conference Seminars and Tutorials (sole presenter)

1. "Self-Oscillating Hysteretic DC–DC Controllers," IEEE Power Electronics Specialists Conf., Vancouver, Canada, 2001.
2. "Integrated LDOs: From the Ground Up!" IEEE Int. Symp. on Circuits and Syst., Scottsdale, AZ, 2002.
3. "Integrated DC–DC Converters: A Topological Journey!" IEEE Midwest Symp. on Circuits and Syst., Tulsa, OK, 2002.
4. "Dynamically Adaptive Power Supply Circuits for PA Wireless Appl.," IEEE Int. Microwave Symp., Long Beach, CA 2005.
5. "Hybrid Fuel Cell/Lithium-Ion Powered, Power Conscious SiP ICs," 1st Int. Workshop on 3S - SOP, SIP, SOC Electronic Technologies, Atlanta, GA, 2005.
6. "AC Des. and Performance of Low-Dropout Regulators (LDOs)," IEEE European Conf. on Circuit Theory and Des., Seville, Spain, 2007.
7. "Powering Micro-Syst. with Fuel-Cell Hybrids," 10th Annual Int. Conf. on Small Fuel Cells, Atlanta, GA 2008.
8. "Low-Dropout Regulator (LDO) ICs," IEEE Int. NEWCAS–TAISA Conf., Montreal, Canada, 2008.
9. "Powering Microsystems," CMOS Emerging Technologies Workshop, Vancouver, Canada, 2009.
10. "Switching DC-DC Supplies and their Single-Inductor, Multiple-Output (SIMO) Derivatives," IEEE Int. Symp. on Circuits and Syst., Paris, France, 2010.
11. "Energy-Harvesting Switching Converter ICs," Int. Workshop on Power Supply On Chip, Cork, Ireland, 2010.
12. "Energizing and Powering Microsystems," The Materials Res. Soc. Fall Meeting, Boston, MA, 2010.
13. "Power-Management Syst. on Chip for Mobile Appl.," IEEE Int. Conf. on Microelectronics, Cairo, Egypt, 2010.
14. "Power-Supply Circuits and Syst. for Battery-Powered Devices," IEEE Very Large-Scale Integration Des., Automation and Test, Hsinchu, Taiwan, 2011.
15. "Energy-Harvesting ICs," IEEE European Solid-State Circuits Conf., Helsinki, Finland, 2011.
16. "Energizing and Powering Microsystems," IEEE Faible Tension Faible Consommation, Paris, France, 2012.
17. "Energy-Harvesting Integrated Circuits," IEEE Int. NEWCAS Conf., Montreal, Canada, 2012.
18. "Harvesting ICs," CMOS Emerging Technologies Workshop, Vancouver, Canada, 2012.
19. "Energy-Harnessing Integrated Circuits," Seminario de Nanoelectrónica y Diseño Avanzado, Departamento de Electrónica del Instituto Nacional de Astrofísica, Óptica y Electrónica, Puebla, México, 2012.
20. "Energizing and Powering Microsystems," SHPE Nat. Conf., Ft. Worth, Texas, 2012.
21. "Feedback Control of Switched-Inductor Supplies," IEEE Asia Pacific Conf. on Circuits and Syst., Kaohsiung, Taiwan, 2012.
22. "Designing Bandgap-Voltage References," IEEE Int. Symp. on Circuits and Syst., Beijing, China, 2013.
23. "Energy-Harvesting Integrated Circuits," IEEE Int. Symp. on Ind. Electronics, Taipei, Taiwan, 2013.
24. "Single-Inductor Multiple-Output Power-Supply ICs," IEEE Int. NEWCAS Conf., Paris, France, 2013.
25. "Designing Bandgap-Voltage References," IEEE Faible Tension Faible Consommation, Paris, France, 2013.
26. "Power Electronic Interfaces for Energy Harvesters," PowerMEMS 2013, London, England, 2013.
27. "Energizing Wireless Microsensors," Int. Forum on Green Energy Electronics, Nat. Taiwan Univ. of Science and Tech., Taipei, Taiwan, July 28, 2014.
28. "Energizing Wireless Microsensors," Int. Forum on Green Energy Electronics, Nat. Cheng Kung Univ., Tainan, Taiwan, July 29, 2014.
29. "Tiny DC-Sourced Single-Inductor Charger–Supply ICs," IEEE Midwest Symp. on Circuits and Syst., College Station, TX, 2014.
30. "Miniaturized Energy-Harvesting Piezoelectric Chargers," IEEE Custom Integrated Circuits Conf., San Jose, CA, 2014.
31. "Miniaturized Energy-Harvesting Piezoelectric Chargers," IEEE Int. Symp. on Integrated Circuits, Singapore, 2014.
32. "Powering Microsystems," IEEE Int. Symp. on Quality Electronic Des., Santa Clara, CA, 2015.
33. "Energy-Harvesting Microsystems," IEEE Very Large-Scale Integration Des., Automation and Test, Hsinchu, Taiwan, 2015.

CURRICULUM VITA

34. "Miniaturized Energy-Harvesting Piezoelectric Chargers," IEEE Int. Symp. on Circuits and Syst., Lisbon, Portugal, 2015.
35. "Miniaturized Energy-Harvesting Piezoelectric Chargers," IEEE Int. Symp. on Ind. Electronics, Rio de Janeiro, Brazil, 2015.
36. "Tiny DC-Sourced Single-Inductor Charger–Supply ICs," IEEE Int. Syst.-on-Chip Conf., Beijing, China, 2015.
37. "Tiny Inductively Powered Battery Chargers," IEEE Int. NEWCAS Conf., Vancouver, Canada, 2016.
38. "Tiny Inductively Powered Battery Chargers," IEEE Asia Pacific Conf. on Circuits and Syst., Jeju, South Korea, 2016.
39. "Low-Dropout Regulator ICs," IEEE Int. Conf. on Electronics Circuits and Syst., Monte Carlo, Monaco, 2016.
40. "Tiny Energy-Harvesting Piezoelectric Chargers," IEEE Int. Conf. on Ind. Tech., Toronto, Canada, 2017.
41. "Light-Harvesting Photovoltaic Charger–Supplies," IEEE Canadian Conf. on Electrical and Computer Eng., Windsor, Canada, 2017.
42. "Tiny Inductively Powered Battery Chargers," IEEE Int. Symp. on Ind. Electronics, Edinburgh, Scotland, 2017.
43. "Tiny Light-Harvesting Photovoltaic Charger-Supplies," IEEE/ACM Int. Symp. on Low Power Electronics and Des., Taipei, Taiwan, 2017.
44. "Energizing and Powering Intelligent Microsensors," IEEE/IEIE Int. Conf. On Consumer Electronics, Jeju, Korea, 2018.
45. "Tiny Inductively Powered Battery Chargers," IEEE Midwest Symp. on Circuits and Syst., Windsor, Canada, 2018.

Research Seminars (sole presenter)

1. "Integrated Power Management Circuits." Nat. Semiconductor Corp., Santa Clara, CA, Dec. 2002.
2. "Power Conscious ICs." Texas A&M Univ., College Station, TX, Jun. 21, 2004.
3. "El Mundo es Análogo, y las Oportunidades son Muchas." Univ. of Puerto Rico at Mayagüez, Oct. 18, 2004.
4. "Hybrid Fuel Cell/Lithium-Ion Powered, Power Conscious ICs." Nat. Semiconductor Corp., Santa Clara, CA, Jun. 2005.
5. "Microsystems: Power and Energy." Army Res. Lab Advanced Microsystems Workshop, Langley, VA, Jan. 30, 2006.
6. "Self-Sustaining, Self-Powered, Energy and Power Conscious ICs for Micro-Scale Devices," Universitat Politècnica de Catalunya, Barcelona, Spain, Jul. 10, 2006.
7. "Self-Powered, Self-Sustaining Syst.-on-Chip and Syst.-in-Package Power Solutions," *Nat. Science Foundation and Intelligence Community Workshop on Micro-Scale Power Sources*, Langley, VA, Apr. 24-25, 2007.
8. "Powering Micro-Syst.," Nat. Semiconductor Corp., Santa Clara, CA, Nov. 30, 2007.
9. "AC Des. and Performance of Low-Dropout Regulators," Texas A&M Univ. at College Station, TX, Jun. 9, 2008.
10. "Power Losses in Switching DC-DC Converter ICs," Texas A&M Univ. at College Station, TX, Jun. 9, 2008.
11. "Powering Micro-Syst.," Shanghai Jiao Tong Univ., Shanghai, China, Oct. 8, 2008.
12. "Powering Micro-Syst.," Linear Tech. Corp., San Jose, CA, Feb. 13, 2009.
13. "Energizing and Powering Microsystems," *IEEE Electron Device Soc. Chapter*, Vancouver, Canada, Sept. 24, 2009.
14. "Harvesting Ambient Energy in Miniaturized Syst.," *Energy and Power Analog Circuit Challenges Workshop*, Semiconductor Res. Corp. Texas Analog Center of Excellence, Dallas, TX, Sept. 28, 2009.
15. "Single-Inductor Multiple-Output Switching DC-DC Converters," Inha Univ., Incheon, Korea, Nov. 19, 2009.
16. "Single-Inductor Multiple-Output Switching DC-DC Converters," Samsung, Seoul, Korea, Nov. 20, 2009.
17. "Power Management ICs for Portable Devices," Univ. of Seoul, Korea, Feb. 17, 2010.
18. "Energizing & Powering Microsystems," Electronics & Telecommunications Res. Inst., Daejeon, Korea, Feb. 18, 2010.
19. "Power Management ICs for Portable Devices," Silicon Works Co., Daejeon, Korea, Feb. 18, 2010.
20. "Energizing & Powering Microsystems," Korea Advanced Inst. of Science and Tech., Feb. 19, 2010.
21. "Harvesting Energy in Miniaturized Syst.," *IT Convergence Res. Project Workshop*, KAIST, Korea, Feb. 19, 2010.
22. "Energizing & Powering Microsystems," Texas Instruments, Dallas, TX, Oct. 25, 2010.
23. "Harvesting Kinetic Energy in Miniaturized Syst.," Nat. Taiwan Univ., Taipei, Taiwan, Nov. 23, 2010.
24. "Energizing & Powering Microsystems," IEEE SSCS Hsinchu Chapter, Taiwan, Nov. 24, 2010.
25. "Power Losses in Switching DC–DC Converter ICs," Nat. Cheng Kung Univ., Tainan, Taiwan, Nov. 26, 2010.
26. "Energy-Harnessing ICs," Nat. Semiconductor Corp., Santa Clara, CA, July 15, 2011.
27. "AC Des. & Performance of LDOs," IEEE CASS Taipei Chapter, Hsinchu, Taiwan, Sept. 5, 2011.

CURRICULUM VITA

28. "Energy-Harnessing ICs," ON Semiconductor, Phoenix, AZ, Oct. 4, 2011.
29. "Energy-Harnessing ICs," Texas Instruments, Dallas, TX, Oct. 5, 2011.
30. "Energy-Harnessing ICs," IEEE Ind. Electronics, Power Electronics, & Industry Appl. Soc., Nat. Tsing Hua Univ., Hsinchu, Taiwan, Dec. 9, 2011.
31. "Frequency Response of Switching DC–DC Converters," Nat. Cheng Kung Univ., Tainan, Taiwan, Dec. 14, 2011.
32. "Feedback Control of Switching DC–DC Converters," Nat. Cheng Kung Univ., Tainan, Taiwan, Dec. 14, 2011.
33. "Harvesting Kinetic Energy in Miniaturized Syst.," Space and Naval Warfare Syst. Command, San Diego, CA, Mar. 2, 2012.
34. "Energy-Harnessing Microchips," IEEE Power Electronics Soc. & IEEE Life Members, Atlanta, GA, Mar. 28, 2012.
35. "Harvesting Kinetic Energy in Miniaturized Syst.," Texas Instruments, Dallas, TX, Jun. 5, 2012.
36. "Harnessing Ambient Energy with Integrated Circuits," Nat. Cheng Kung Univ., Tainan, Taiwan, Dec. 6, 2012.
37. "Des. of High-Performance Low-Dropout Regulator ICs," Nat. Cheng Kung Univ. & IEEE Circuits and Syst. Soc., Tainan, Taiwan, Dec. 17, 2013.
38. "Designing Tiny DC-Sourced Single-Inductor Charger–Supply ICs," Nat. Cheng Kung Univ., IEEE Circuits and Syst. Soc., & IEEE Solid-State Circuits Soc., Tainan, Taiwan, Dec. 18, 2013.
39. "Designing Tiny Energy-Harvesting Piezoelectric Chargers," IEEE Circuits and Syst. Soc., Nat. Sun Yat-Sen Univ., Kaohsiung, Taiwan, Dec. 19, 2013.
40. "Designing Tiny Energy-Harvesting Piezoelectric Chargers," IEEE Ind. Electronics Soc. & IEEE Power Electronics Soc., Nat. Taiwan Univ. of Science and Tech., Taipei, Taiwan, Dec. 20, 2013.
41. "Energizing & Powering Microsystems," Texas Instruments, Dallas, TX, May 8, 2014.
42. "Power-Supply Rejection in Amplifiers and LDOs," Nat. Cheng Kung Univ., Tainan, Taiwan, Nov. 19, 2014.
43. "Miniaturized Energy-Harvesting Piezoelectric Chargers," Nat. Cheng Kung Univ., Tainan, Taiwan, Nov. 20, 2014.
44. "Powering Microsensors," Department of Energy, Nat. Security Campus, Kansas City, Feb. 6, 2015.
45. "Tiny and Distant Inductively-Powered Battery Chargers," Nat. Cheng Kung Univ., Tainan IEEE Solid-State Circuits & Circuits and Syst. Soc., Tainan, Taiwan, Nov. 19, 2015.
46. "Energizing & Powering Microsystems," Department of Energy, Nat. Security Campus, Albuquerque, NM, Jan. 7, 2016.
47. "Energizing & Powering Microsystems," Khalifa Univ., Abu Dhabi, UAE, May 2, 2016.
48. "Energizing & Powering Microsystems," Texas Instruments, Dallas, TX, June 10, 2016.
49. "Energizing & Powering Microsystems," KTH Royal Inst. of Tech., Stockholm, Sweden, June 9, 2017.
50. "Energizing & Powering Microsystems," Nat. Cheng Kung Univ., IEEE Circuits and Syst. Soc., Tainan, Taiwan, Nov. 16, 2017.
51. "On-Chip Bias Currents," Nat. Cheng Kung Univ., IEEE Circuits and Syst. Soc., Tainan, Taiwan, Nov. 20, 2017.
52. "On-Chip Voltage References," Nat. Cheng Kung Univ., IEEE Circuits and Syst. Soc., Tainan, Taiwan, Nov. 21, 2017.
53. "Powering Microsystems," Analog Devices, Phoenix, AZ, Mar. 9, 2018.
54. "Energizing & Powering Wireless Microsensors," Texas A&M Univ., College Station, TX, Mar. 19, 2018.
55. "Energizing & Powering Microsystems," Texas Instruments, Dallas, TX, Jun. 15, 2018.
56. "Energizing & Powering Microsystems," Analog Devices, Milpitas, CA, Nov. 9, 2018.
57. "Energizing & Powering Microsystems," Univ. of Illinois at Urbana–Champaign, IL, Nov. 26, 2018.

Panelist

1. "Power Management for SoCs," IEEE VLSI Symp., Hawaii, Jun. 15-17, 2006.
2. "Non-Academic Routes Beyond the Eng. Ph.D.," Sloan Foundation, Atlanta, GA, Apr. 14, 2015.
3. "Res. Funding: A View from the Other Side," Univ. Center of Exemplary Mentoring, Atlanta, GA, Feb. 13, 2018.

Artistic Performances

1. Tenor in *A Festival of Nine Lessons and Carols* (Concert). St. Rita's Contemporary Choir, Dallas, TX, Dec. 2000.
2. Tenor in *The American Folk Spirit: A Concert of Folk Songs, Hymns, and Spirituals* (Concert). Alpharetta Chamber Singers, Alpharetta, GA, Nov. 2001.

CURRICULUM VITA

3. Tenor in *Refresh of 2002: Five Services of Preaching and Proclamation* (Concert). Alpharetta Chamber Singers, Alpharetta, GA, Jan. 2002.
4. Zebulon in *Joseph and the Amazing Technicolor Dreamcoat* (Musical). Christ the King Theatre Ministry, Atlanta, GA, Apr. 2002.
5. Bernardo in *West Side Story* (Musical). Cobb Playhouse/Little General, Marietta, GA, Sept. 2002.
6. Tenor in *Mass of Remembrance* (Concert). Holy Spirit's Traditional Choir, Atlanta, GA, Nov. 2002.
7. Tenor in *Expressions of Praise* (Concert). Alpharetta Chamber Singers, Alpharetta, GA, Nov. 2002.
8. Homer Smith in *Lilies of the Field* (Play). Gwinnett County Seat Players, Lawrenceville, GA, Feb. 2003.
9. Tenor in *Mass of a New Millennium* (Concert). Alpharetta Chamber Singers, Alpharetta, GA, Apr. 2003.
10. Soldier in the *Sound of Music* (Musical). Christ the King Theatre Ministry, Atlanta, GA, Apr. 2003.
11. Tenor in *Sing a Song of Shakespeare* (Concert). Alpharetta Chamber Singers, Alpharetta, GA, May 2003.
12. Tenor in *Magnificat* (Concert). Archdiocesan Festival Choir, Atlanta, GA, May 2003.
13. Sid Philips in *Singin' in the Rain* (Musical). Marietta First Methodist Church, Marietta, GA, Jul. 2003.
14. Jimmy Luv in *Snuff Darlings from Dahlonga* (Play). Art Farm, Cabbage Town, GA, Aug. 2003.
15. Henry Steward in *Civil War* (Musical). Neighborhood Playhouse, Decatur, GA, Sept. 2003.
16. Tenor in *Wonder Tidings* (Concert). Alpharetta Chamber Singers, Alpharetta, GA, Dec. 2003.
17. Fred Casely in *Chicago* (Musical). Theatre Arts Guild, Clarkston, GA, Feb. 2004.
18. Soldier, Guard, and Servant in *Life is a Dream/La Vida es Sueño* (Play: Performed in English and Spanish), Aurora Theatre, Duluth, GA, Apr. 2004.
19. Paco in *Man of La Mancha* (Musical). Corpus Christi Theatre Ministry, Stone Mountain, GA, Oct. 2004.
20. Tommy Keeler in *Annie Get Your Gun* (Musical). ACT 1, Alpharetta, GA Nov.–Dec. 2004.
21. Martinez in *Take Me Out* (Play). Theatre in the Square, Marietta, GA, Mar.–Apr. 2005.
22. Ali Hakim in *Oklahoma* (Musical). Christ the King Theatre Ministry, Atlanta, GA, Mar.–Apr. 2006.
23. Boxhall in *Titanic* (Musical). Stage 2 Players, Roswell, GA, Oct. 2006.
24. Roberto Nuñez & Charlie Blossom in *Working* (Musical). Theatre Arts Guild, Clarkston, GA, Oct.–Nov. 2006.
25. Tenor in St. Rita's Contemporary Choir, Dallas, TX, 1999–2001.
26. Tenor in Alpharetta Chamber Singers, Alpharetta, GA, 2001–2004.
27. Tenor in Holy Spirit's Traditional Choir, Atlanta, GA, 2001–2004.
28. Tenor in Archdiocesan Festival Choir, Atlanta, GA, 2002–2004.
29. Tenor in Sacred Heart's Spanish Choir, Atlanta, GA, 2004–2005.

V. Service

Professional Leadership

1. Chapter Vice-Chair, Atlanta's IEEE Solid-State Circuits and Circuits and Syst. Soc., 2003–2004.
2. Chapter Chair, Atlanta's IEEE Solid-State Circuits and Circuits and Syst. Soc., 2004–2011.
3. Technical Program Co-Chair, IEEE Midwest Symp. on Circuits and Syst., Puerto Rico, 2006.
4. Technical Program Chair, Joint IEEE Midwest Symp. on Circuits and Syst./Int. NEWCAS Conf., Canada, 2007.
5. Circuit Des. Vice Chair, IEEE Int. Caribbean Conf. on Devices, Circuits and Syst., Mexico, 2008.
6. General Chair, Energy & Power IC Workshop, SRC Texas Analog Center of Excellence, Sept. 28-29, 2009.
7. Special Session Co-Organizer, "Emerging Energy/Power ICs," IEEE Int. Symp. on Circuits and Syst., Brazil, May 2011.
8. Technical Program Co-Chair, IEEE Int. Syst.-on-Chip Conf., Korea, Nov. 2011.
9. Advisory Panel, IEEE Int. Conf. on Power Electronics and Energy Syst., India, 2012.
10. Technical Program Committee, IEEE Faible Tension Faible Consommation, France, 2013–2014.
11. Int. Advisory Board, IEEE Int. Conf. on Power Electronics and Drive Syst., Japan, 2013.
12. General Co-Chair, IEEE Int. Syst.-on-Chip Conf., Korea, Nov. 2013.
13. General Vice Chair, IEEE Int. Syst.-on-Chip Conf., Korea, Nov. 2014.
14. Int. Steering Committee, Int. Future Energy Electronics Conf., Taiwan, Nov. 2015.

CURRICULUM VITA

15. Technical Program Chair, IEEE Int. Symp. on Circuits and Syst., Canada, May 2016.
16. Int. Liaison, IEEE Conf. on Des. of Circuits and Integrated Syst., España, Nov. 2017.
17. Technical Program Co-Chair, IEEE Int. Symp. on Circuits and Syst., Japan, May 2019.

Editorial Boards

1. Guest Co-Editor, *Analog Integrated Circuits and Signal Processing J.*, Special Issue on Analog and RF, Aug. 2009.
2. Associate Editor, *IEEE Trans. on Circuits and Syst. II*, 2007–2009 and 2010–2011.
3. Associate Editor, *IEEE J. of Solid-State Circuits*, 2011.
4. Editorial Board Member, *J. of Low-Power Electronics*, since 2009.
5. Guest Editor, *IEEE Trans. on Circuits and Syst. II*, Special Issue on Energy Harvesting, Dec. 2011.

Committee Memberships

1. Technical Program Committee, IEEE Southwest Symp. on Mixed-Signal Des., 2002.
2. Selection Committee Review Panel, NSF SBIR/STTR Committee on Power Management, Mar. 2003.
3. Selection Committee Review Panel, NSF SBIR/STTR Committee on Power Management, Sept. 2003.
4. Technical Committee, IEEE CASS Analog Signal Processing, since 2003.
5. Selection Committee Review Panel, NSF SBIR/STTR Committee on Signal Processing & IC Des., Oct. 2004.
6. Selection Committee Review Panel, NSF SBIR/STTR Committee on IC Des.: Testing, Aug. 2005.
7. Steering Committee, IEEE Midwest Symp. on Circuits and Syst., since 2006.
8. Selection Committee Review Panel, NSF SBIR/STTR Committee on IC Des. I, Feb. 2007.
9. Technical Committee, IEEE CASS Power and Energy Circuits and Syst., since 2009.
10. Fellows Evaluation Committee, IEEE Circuits and Syst. Soc., 2011 and 2014.
11. Distinguished Lecturer Committee, IEEE Circuits and Syst. Soc., 2011–2013.
12. Ind. Advisory Committee, IEEE Circuits and Syst. Soc., 2012–2013, 2014–2015.
13. Steering Committee, IEEE Int. Symp. on Circuits and Syst., 2016–2017.

Professional Memberships

1. Inst. of Electrical and Electronics Engineers: Student '90, Member '97, Sr. Member '01, Fellow '11.
2. Institution of Eng. and Tech.: Member '06, Fellow '09.
3. Soc. of Hispanic Professional Engineers: Life Member '00.

International Ph.D. Committees

1. [Rapporteur, Jury] Vincent Telandro, *On-Chip Voltage Regulator Protecting Against Power Analysis Attacks*, Laboratoire Matériaux et Microélectronique de Provence, Institut Supérieur d'Electronique du Nord, France, Nov. 2007.
2. [External Examiner, Chair] Mohammad Radwan Alhawari, *Multi-Source Energy-Harvesting Interface Circuits for Biomedical Wearable Electronics*, Khalifa Univ., Abu Dhabi, United Arab Emirates, May 2, 2016.
3. [Examiner, Opponent] Janko Katic, *Efficient Energy Harvesting Interfaces for Implantable Appl.*, KTH Royal Inst. of Tech., Stockholm, Sweden, June 9, 2017.

Georgia Tech Service

1. ECE Graduate Student Recruitment Committee: '01–'03, '04–'05.
2. ECE Student–Faculty Committee: '03–'08, '11–'12; Chair in '08–'11.
3. ECE Student Award Committee: '06.
4. ECE Georgia Power Distinguished Professor Search Committee: '06.
5. Freshmen Partner for Freshmen Partnership Program: '06.
6. Outstanding Electrical & Computer Eng. Sr. Student Awards Committee: '09–'11.
7. ECE Student Awards Committee: '10.
8. ECE Electronic Des. & Appl. Technical Interest Group: Chair in '13–'16, Faculty Recruitment Rep. in '16–'17.
9. ECE Course Content Review Panel for ECE 3040: '15.
10. Graduate Student Committees:

	Student	Proposal Committee	Reading Committee	Defense Committee	Degree
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CURRICULUM VITA

1	Sidharth Dalmia	Chair: 3/14/02			Ph.D.
2	Zhiwei Dong		Member: 7/15/02	Member: 7/15/02	Ph.D.
3	Theocharis Boukas	Chair: 8/12/02		Member: 03/26/03	Ph.D.
4	Susanta Sengupta	Member: 4/15/02	Member: 07/08/04	Member: 07/08/04	Ph.D.
5	Kyu-won Choi	Chair: 10/29/02	Member: 09/09/03	Member: 09/09/03	Ph.D.
6	Woopoung Kim	Chair: 4/30/03			Ph.D.
7	Biranchinath Sahu	Advisor: 3/24/04	Chair: 11/4/04	Chair: 11/4/04	Ph.D.
8	Bhyrav Mutnury	Member: 1/28/05			Ph.D.
9	Pooya Forghani	Advisor: 6/24/04	Chair: 6/1/06	Chair: 6/1/06	Ph.D.
10	Vishal Gupta	Advisor: 9/20/05	Chair: 7/3/07	Chair: 7/3/07	Ph.D.
11	Neeraj Keskar	Advisor: 9/20/05	Chair: 3/24/08	Chair: 3/24/08	Ph.D.
12	Jau-Horng Chen	Chair: 9/22/05	Member: 5/25/06	Member: 06/30/06	Ph.D.
13	Soumendu Bhattacharya			Member: 06/23/05	Ph.D.
14	Jacob Minz	Member: 10/19/05		Member: 07/19/06	Ph.D.
15	Shruti Prakash	Member: 7/27/06	Member: 03/04/09	Member: 03/04/09	Ph.D.
16	Kenta Nakayashiki	Member: 9/28/06	Member: 10/2/07	Member: 10/2/07	Ph.D.
17	Ripal Nathuji	Member: 5/2/07			Ph.D.
18	Rajeswari Chandrasekaran	Member: 08/22/07		Member: 7/15/10	Ph.D.
19	David Pritchett	Member: 12/13/07		Member: 2/4/09	Ph.D.
20	N. Lalgudi Subramanian	Member: 1/17/07		Member: 3/26/08	Ph.D.
21	Erick Torres	Advisor: 4/9/08	Chair: 5/4/10	Chair: 5/4/10	Ph.D.
22	Krishna Bharath	Member: 4/21/08			Ph.D.
23	Muhammad Nisar	Member: 7/30/08			Ph.D.
24	Dale Scott Douglas		Member: Fall 08		M.S.
25	Tahir Zaidi	Member: 6/1/09			Ph.D.
26	Luke Milner	Advisor: 7/15/09			Ph.D.
27	Suhwan Kim	Advisor: 3/13/11	Chair: 4/14/14	Chair: 4/14/14	Ph.D.
28	Sang Taek Han	Member: 3/3/11			Ph.D.
29	Mauricio Pardo Gonzalez	Chair: 4/29/11	Member: 1/18/12	Member: 1/18/12	Ph.D.
30	Debrup Das	Member: 6/1/11			Ph.D.
31	Dongwon Kwon	Advisor: 7/21/11	Chair: 3/4/13	Chair: 3/4/13	Ph.D.
32	Hengzhao Yang	Member: 4/24/12		Member: 5/2/13	Ph.D.
33	Hakan Toreyin	Member: 11/25/12			Ph.D.
34	Orlando Lazaro	Advisor: 7/23/12	Chair: 4/2/14	Chair: 4/2/14	Ph.D.
35	Chris Valenta	Member: 7/25/12		Member: 6/25/14	Ph.D.
36	Jae Won Shim	Member: 3/13/13			Ph.D.
37	Yaesuk Jeong	Member: 7/24/14	Member: 5/11/17	Member: 5/11/17	Ph.D.
38	Andres Blanco	Advisor: 8/21/14	Chair: 7/14/17	Chair: 7/14/17	Ph.D.
39	Rajiv Damodaran	Advisor: 6/22/15	Chair: 12/7/17	Chair: 12/7/17	Ph.D.
40	Carlos Solís	Advisor: 1/27/16	Chair: 4/3/18	Chair: 4/3/18	Ph.D.
41	Jaemyum Lim	Chair: 3/16/16	Member: 3/16/17	Member: 3/16/17	Ph.D.
42	Nan Xing	Advisor: 4/16/18			Ph.D.
43	Siyu Yang	Advisor: 7/13/18			Ph.D.

Community Service

1. Volunteers for Peace (VFP), Kigonigoni, Tanzania (school and levy construction), Su. '06.
2. Volunteers for Peace (VFP), Bangalore, India (teach children with AIDS and disabilities), Su. '08.
3. Service Civil Int. (SCI), Ulaan Baatar, Mongolia (construction and farming at orphanage), Su. '09.
4. Service Civil Int. (SCI) in Viet Tri, Vietnam (teach orphans English), Su. '10.

VI. Teaching

Ph.D. Students Graduated

1. Biranchi Sahu, *Dynamically Adaptive Supplies for Linear RF Power Amplifiers*, Ph.D. Dec. 2004.
2. Pooya Forghani, *Lossless Current-Sensor IC for Switching DC-DC Converters*, Ph.D. Jun. 1, 2006.
3. Vishal Gupta, *An Accurate, Trimless, High PSRR, Low-Voltage, CMOS Reference IC*, Ph.D. Jul. 3, 2007.

CURRICULUM VITA

4. Neeraj Keskar, *High-Bandwidth, Wide LC-RESR Compliant $\Sigma\Delta$ Boost DC-DC Converters*, Ph.D., Mar. 24, 2008.
5. Erick O. Torres, *An Electrostatic CMOS/BiCMOS Vibration-Based Harvester-Charger IC*, Ph.D., May 4, 2010.
6. Dongwon Kwon, *Piezoelectric Kinetic Energy-Harvesting ICs*, Ph.D., Mar. 4, 2013.
7. Orlando Lazaro, *CMOS Inductively Coupled Power Receiver for Wireless Microsensors*, Ph.D., Apr. 2, 2014.
8. Suhwan Kim, *Mixed-Source Charger-Supply CMOS IC*, Ph.D., Apr. 14, 2014.
9. Andrés Arturo Blanco, *Fast-Waking & Low-Voltage Thermoelectric & Photovoltaic CMOS Chargers for Energy-Harvesting Wireless Microsensors*, Ph.D., Jul. 14, 2017.
10. Rajiv Damodaran Prabha, *Light-Harvesting Photovoltaic Charger-Supply Microsystems*, Ph.D., Dec. 7, 2017.
11. Carlos Javier Solís, *Battery-Sourced Switched-Inductor Multiple-Output CMOS Power-Supply Syst.*, Ph.D., Apr. 3, 2018.

Master Students Advised

1. Mark Guildersleeve, *Low Voltage Power Saving Techniques for DC-DC Converters*, M.S.E.E., Aug. 2002.
2. Abbas Poonawala, *Precision, Low-Voltage, Integrated Capacitor Multipliers*, M.S.E.E., Dec. 2003.
3. Aditya Makharia, *Inductorless DC-DC Converters for Portable Appl.*, M.S.E.E., Dec. 2003.
4. Oscar Palomino, M.S.E.E., Dec. 2007.
5. Amisha Manek, M.S.E.E., Dec. 2008.
6. Justin Vogt, *nW Analog-Digital Converter for Blood-Glucose Monitors*, M.S.E.E., Dec. 2008.
7. Amit Patel, Thesis: "*High PSR Low Dropout Regulator ICs*," M.S.E.E., May 2009.
8. Priyanka Lakhe, M.S., May 2010.
9. Luke Milner, M.S., May 2010.
10. Tim Guglielmo, M.S., May 2011.
11. José Vidal, M.S., May 2011.
12. Joshua Cowan, M.S., Dec. 2013.
13. Jun-Yang Lei, M.S., Dec. 2014.
14. Amy Wilson, *Electrostatic Energy-Harvesting CMOS Charger ICs*, M.S., Dec. 2017.

Ph.D. Students Currently Advised

1. Nan Xing, *Inductively Coupled Chargers*. Start: Su. '14, Pre. Exam: Fa. '13, Proposal Exam: Sp. '18.
2. Siyu Yang, *Piezoelectric Energy-Harvesting Chargers*. Start: Sp. '15, Pre. Exam: Fa. '13, Proposal Exam: Su. '18.
3. Devon Janke, *Monolithic Switched-Inductor CMOS Power Supplies*. Start: Sp. '17, Pre. Exam: Fa. '15.
4. Tianyu Chang, *CMOS Thermoelectric Harvesters*. Start: Sp. '17, Pre. Exam: Sp. '17.

Undergraduate Students Advised

1. R. Dokania (Su. '02), *Cancellation of Load Regulation in Low Drop-out Regulators*.
2. K. Dash (Su. '03), *Active Bulk Capacitor Multipliers*.
3. Carlos Cubero Ponce (Su. '05), *Drain Follower Buffer*.
4. Freddie Alequín Ramos (Su. '07), *Syst.-in-Package Integration*.
5. LaVonda Brown (Su. '08), *Piezoelectric Modeling*.
6. Adilson Cardoso (Fa. '06–Fa. '07).

Visiting Scholars Advised

1. H.I. Pan (from the Univ. of Taiwan: Jan. to Dec. 2005), *Asynchronous Power-Tracking Supplies for RF PAs*.

Courses Developed (at Georgia Tech)

- ECE 4803 *Energy & Power Microelectronics* (at Georgia Tech)
- ECE 6445 *Power IC Design* (at Georgia Tech)

Courses Taught (at Georgia Tech)

1. ECE 3040 *Microelectronic Circuits*: Sp. '02, '03.
2. ECE 3050/3400 *Analog Electronics*: Fa. '01, '02, '03, '04, '05, '06, '07, '10, Sp. '05, '06.
3. ECE 4430 *Analog Integrated Circuits*: Fa. '02, '03.
4. ECE 4803 *Energy/Power Microelectronics*: Su. '18.

CURRICULUM VITA

5. ECE 6412 Analog Integrated Circuit Design: Sp. '04, '07, '08, '09, '10, '11, '12, '13, '14, '15, '16, '17, '18; at GT-Shanghai in Fa. '08, '11, '13; & GT-Shenzhen in Fa. '14, '16, '18.
6. ECE 6445 Power IC Design: Su. '15; at GT-Shanghai in Fa. '09, '12; & at GT-Shenzhen in Fa. '15, '17.

VII. Distinctions

Elevations and Elections

1. One of "The 100 Most Influential Hispanics," *Hispanic Business*, 2000.
2. Sr. Member, Inst. of Electrical and Electronics Engineers, 2001.
3. Fellow, Institution of Eng. and Tech., 2009. (Less than 5% of members earn rank of Fellow.)
4. Distinguished Lecturer, IEEE Circuits and Syst. Soc., 2009–2010.
5. Fellow, Inst. of Electrical and Electronics Engineers, 2011. (Less than 1% of members earn rank of Fellow.)
6. Fellow, Nat. Academy of Inventors, 2017.
7. Distinguished Lecturer, IEEE Circuits and Systems Soc., 2018–2019.

Awards

1. Three-Year Patent Award for U.S. 5491437, U.S. 5500625, & U.S. 5519341, Texas Instruments, 1999.
2. Charles E. Perry Visionary Award, Florida Int. Univ., 2000.
3. Council of Outstanding Young Eng. Alumni Inductee, Georgia Inst. of Tech., 2000.
4. Hispanic in Technology Award, Soc. of Hispanic Professional Engineers, 2000.
5. State of California Commendation Certificate from Lieutenant Governor Cruz M. Bustamante, 2001.
6. Orgullo Hispano Award, Robins Air Force Base, Sep. 23, 2003.
7. Hispanic Heritage Award, Robins Air Force Base, 2005.
8. IEEE Service Award, MWSCAS–NEWCAS, 2007.

Magazine Covers (Mag. with Prof. Rincón-Mora on their cover.)

1. "Bravo – National Award Winners," *Official Magazine of the Soc. of Hispanic Professional Engineers*, Sp. 2000.
2. "The 100 Most Influential Hispanics," *Hispanic Business*, Oct. 2000.
3. "A high-tech engineer with a low-tech lifestyle," *La Fuente* (Dallas publication), Mar. 2000.
4. "Gabriel Rincón-Mora - Impacta en la alta tecnología," *Nuevo Impacto* (Atlanta publication), Aug. 2002.
5. "Profesionales Latinos – La nueva cara de Georgia," *Nuevo Impacto* (Atlanta publication), Oct. 2003.
6. "Gabriel Rincón Mora – Outstanding engineer and author," *Nuevo Impacto* (Atlanta publication), Nov. 2004.

Feature Stories (on Prof. Rincón-Mora)

1. "Passion for design, apathy for gizmos," *Electronic Eng. Times*, Jun. 2000.
2. "Designer has passion for work, apathy for gizmos," *Planet Analog*, Jun. 2000.
3. "By Day an Engineer," *Intown* (Atlanta publication), Aug. 2002.
4. "World-class training workshop on analog IC power management by top Integrated Circuit (IC) expert from the United States," *Hong Kong Science & Tech. Parks News & Newslett.*, Oct. 2003.
5. "Alumni Profile: Gabriel A. Rincón-Mora," *Summa Cum Laude*, Florida Int. Univ. Honors College, vol. 1, no. 3, 2011.
6. "Featured Engineer: Gabriel Alfonso Rincón-Mora," *EEWeb – Electrical Eng. Community*, Nov. 2012.

Recognitions

1. HENAAC Role Model of the Week, Hispanic Engineer Nat. Achievement Awards Corp., Jul. 5, 2005.
2. TIDN Forum's Significant TI Contributor, Texas Instruments, 1999.
3. "Notar – Short Stories and Poems to Boot," *Official Mag. of the Soc. of Hispanic Professional Engineers*, Aug. 2002.
4. "Innovators Matter," *Hispanic Business*, Sept. 2002.
5. "Innovators Matter," *Hispanic Business*, Dec. 2002.
6. "Hispanic Eng. Talent," *Georgia Tech Soc. of Professional Hispanic Engineers*, Feb. 2003.
7. "SSCS Subsidizes Short Course on Linear Regulator Des. in Taipei," *IEEE Solid-State Circuits Soc. Newslett.*, Sep. 2006.
8. IEEE Plaque, IEEE APCCAS, 2009.
9. IEEE CAS Certificate of Appreciation, IEEE Circuits and Syst. Soc., 2009.

CURRICULUM VITA

10. IEEE Plaque of Appreciation, IEEE ISOCC, 2009.
11. Thanks for Being a Great Teacher certificate, Georgia Inst. of Tech., 2010.
12. Certificate of Appreciation, IEEE Circuits and Syst. Soc., 2010.
13. Certificate of Appreciation, IEEE Circuits and Syst. Soc., 2011.
14. Thanks for Being a Great Teacher certificate, Georgia Inst. of Tech., 2012.
15. Certificate of Appreciation, Inst. of Electrical and Electronics Engineers, 2012.
16. Certificate of Appreciation, IEEE Circuits and Syst. Soc., 2012.
17. Certificate of Appreciation, IEEE Ind. Electronics Soc./Power Electronics Soc./IEEE Ind. App. Soc., 2013.
18. NTU/Mediatek Plaque, Nanyang Technological Univ., Singapore, 2016.
19. Plaque of Appreciation, IEEE ISOCC, 2016.
20. Thanks for Being a Great Teacher certificate, Georgia Inst. of Tech., 2018.

Product/Book/Article Distinctions

1. "Top 100 Products" of 1998 by *EDN* (on the cover of *Electronic Des.*) for TPS5210.
2. Top 7 Most Cited IEEE TCAS II '98 Paper: "Designing 1V Op Amps Using Standard Digital CMOS Technology," *IEEE Trans. on Circuits and Syst. II*, vol. 45, no. 7, pp. 769–780, Jul. 1998.
3. Top 25 Most Downloaded IEEE TCAS II '02 Paper: "A low voltage, rail-to-rail, class AB CMOS amplifier with highdrive and low output impedance characteristics," *IEEE Trans. on Circuits and Syst. II*, vol. 48, no. 8, pp. 753–761, Aug. 2001.
4. Top 200 Most Downloaded IEEE '04 Paper: "A High-Efficiency Linear RF Power Amplifier With a Power-Tracking Dynamically Adaptive Buck–Boost Supply," *IEEE Trans. Microw. Theory Techn.*, vol. 52, no. 1, pp. 112–120, Jan. 2004.
5. "7th Most Read Power Management Des. Line How-To Article in 2005": "Power Supply Ripple Rejection and Linear Regulators: What's all the noise about?" *Power Management Des. Line*, Sept. 20, 2005.
6. "2nd Most Read Power Management Des. Line How-To Article in 2006": "Harvesting energy into lithium-ion batteries," *Power Management Des. Line*, Feb. 14, 2006.
7. 2nd Place Award, '09 Science App. Int. Corp.'s Georgia Tech Paper Competition: "A Rectifier-Free Piezoelectric Energy Harvester Circuit."
8. 2nd best seller, IEEE Int. Solid-State Circuits Conf. '09: *Analog IC Des. with Low-Dropout Regulators*.
9. 3rd Best in Show, IEEE Int. Solid-State Circuits Conf. '12: *Analog IC Des. with Low-Dropout Regulators, 2nd Edition*.

Academic Distinctions

1. *Presidential Academic Fitness Award*, (signed by President George Bush, Sr.), 1989.
2. *Insignis Scholarship*, Univ. of Detroit, 1989.
3. *Dean's List*, Florida Int. Univ., 1989–1992.
4. *Florida Undergraduate Scholars Fund Scholarship*, State of Florida, 1989–1992.
5. *Faculty Scholars Scholarship*, Florida Int. Univ., 1989–1992.
6. *Honorary Award Recognition*, Nat. Dean's List, 1990–1992.
7. *Phi Kappa Phi* (honor society), Florida Int. Univ., 1991.
8. *Eta Kappa Nu* (nat. electrical eng. honor society), Florida Int. Univ., 1992.
9. *B.S.E.E. Magna Cum Laude*, Florida Int. Univ., 1992.
10. *Honorable Mention*, Nat. Science Foundation, 1993.
11. *Tau Beta Pi* (nat. eng. honor society), Georgia Inst. of Tech., 1994.
12. *Outstanding Ph.D. Graduate*, Georgia Inst. of Tech., 1996.